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### PV Battery Charger Using an L3C Resonant Converter for Electric Vehicle

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### **ABSTRACT**

In Electric Vehicles with rooftop PV panels, the solar irradiance and surface temperature can affect their performance and output voltages (e.g.,  $V_{pv} = 24\text{--}45\text{VDC}$ ). In these systems, the maximum energy must be extracted from the variable input voltage (PV panel), boosted by different gains, and stored in high voltage battery packs. Furthermore, depending on the battery state of charge, the charger should operate in constant voltage, constant current, or constant power modes, all the way from complete discharge condition, up to the charged floating voltage phase ( $V_{bat} = 230 - 430\text{VDC}$ ).

This combination of the variable PV input voltage and different states of charge creates a significant regulation challenge for the converter. In this paper, a high efficiency fourth orderL3C resonant converter is proposed with an extreme voltage regulation capability that can effectively extract the maximumpower from the PV panels and respond to the battery states of charge at different voltage and current levels. The experimental results from a 350W prototype prove the features of the proposed L3C resonant converter and demonstrate its ability to track the maximum input power while responding to the battery various states of charge.

Keywords:- Batteries, Voltage control, Inductance, Temperature, Topology, State of charge

#### INTRODUCTION

DC-DC power converters with variable voltage gain and boosting features are among the main components for PV energy systems, since the maximum power must be extracted from the low voltage, variable input source and boosted to a high voltage level [1, 2]. In PV energy systems for Electric Vehicles (EVs) with rooftop PV panel, it is also essential to employ a rechargeable battery pack to store energy and release it later [3–7]. Using battery energy packs storage imposes challenging design constraints for the power converter, due to the different battery operating modes, including constant current, constant voltage, constant power, and noload condition [8]. The combination of a PV panel and are chargeable battery in an energy system requires extreme voltage gain variations from the input 24-45VDC) to the  $output(V_{bat})$ 230-430VDC), and this needs to be supported by solar battery chargers. In this case, the charger should not only track the input voltage variation in order to extract the maximum available power from the PV panel, but also boost the input voltage according to the level of variable gain and respond to the battery different states of charge.

Recently, different studies have been dedicated to developing reliable and efficient non-isolated and isolated power converters for PV applications. Non-isolated power converter topologies which have

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been used in PV systems include boost converters, Cockcraft-Walton multipliers, coupled inductors, and switched inductorscapacitors with high voltage gain capability as the DC-DC power conversion stage for PV to grid applications(boosts a low, variable input voltage of 30 - 50VDC to a high, fixed output voltage of 400VDC) [9-13]. Although non-isolated power converters can successfully boost the PV voltage and reach high performance, they are not recommended for Electric Vehicle applications that require mandatory galvanic isolation between the PV panel and the high voltage battery pack.

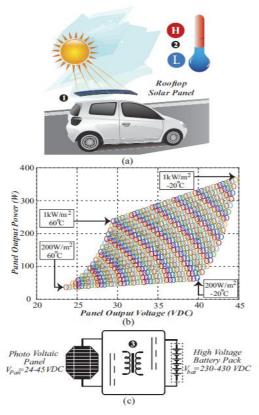


Fig. 1. a) Example of EV with rooftop solar panel subject to changing sun irradiance 1) and temperature 2), b) PV panel Power-Voltage characteristic covered by the DC-DC converter, and 3) galvanic isolation 3 regulation requirement to supply 230 – 430VDC wide battery voltage from 24 –45VDC PV voltage.

According to safety standards UL60950 and UL1741, isolated applications require at least 4kV galvanic isolation to be provided between the PV panel and the battery pack in order to prevent any high voltage shock path [14, 15]. Among isolated PWM power converter topologies, fly back, current fed push-pull topology, z-source, and Phase-Shift-Full-Bridge power converters have the most attention. converters can achieve a high step-up gain from transformers with large turn ratios while tracking the input voltage variation by changing the effective duty cycle [16-181.Different improvements such interleaving, and active clamp circuit have been presented in order to decrease the input current ripple and transfer the maximum input power to the output with soft switching across the main switches [19, 20]. These interesting converters target the maximum energy transfer from a variable input voltage to a fixed output voltage, rather than responding to different voltage and current operating modes of the high voltage battery pack. In particular, storage applications with new battery technologies (e.g., Li-Ion) push the charger output requirements of the PV converter to unprecedented levels. Insightful research on resonant converters has been presented in literature with single and double conversion stages for battery charger or PV applications with fixed input and fixed output voltage, respectively; including current-fed parallel, modified and bidirectional, hybrid, and quasi-Z-source resonant converters [21–28]. These converters successfully handle the PV voltage variations and Maximum Power Point Tracking (MPPT) requirements or respond to different battery states of charge, but do not focus on both input and output variations at the same time. Resonant power converters will be reviewed in detail in Section III in order to present each topology configuration, its application, and the voltage regulation capability. The particular

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application, addressed in this paper, involves the challenging scenario of extracting the energy from a variable input voltage source (PV panel) and transferring it to a high voltage battery pack, which needs to be regulated at different current and voltage levels. As an example, Fig. 1(a) shows a single PV panel on the rooftop of an EV. The power capacity range is from 50W to 350W and the input voltage ranges widely from  $V_{pv} = 24 - 45 VDC$ . This undesirable wide input variations due to the effect of solar irradiance and surface temperature that affects the Power-Voltage characteristics of the PV panel illustrated in Fig. 1(b). As well, the battery pack depicted in Fig.1(c) shows that the output voltage should comply with extreme regulation as well to cover 230 – 430VDC.

This paper proposes a high efficiency, isolated fourth orderL3C resonant converter with extreme regulation capability for PV to high-voltage battery pack applications with the aim of tracking the maximum input power while responding to the battery states of charge at different output voltage and current levels. Incomparison with wellresonant power known converter topologies (e.g. L3C2, LCLC, LCL2, LCC, and LCL), the proposed topology presents a steep voltage gain versus frequency, means lower frequency variation for input-output regulation, which makes it a good candidate for PV to high voltage battery pack applications. Also, the proposed resonant topology has a voltage gain of more than two for the resonant tank, which can be counted as a part of the boost action and reduces the transformer turns ratio and simplifies its structure. Due to its inherent over current capability, the proposed converter can control the charging current during constant current mode or transient conditions; this capability also eliminates the need for an over current and short circuit

protection, and facilitates the current control loop design.

Due to operating in the inductive load region, power MOSFETs in the high frequencyL3C resonant converter always work in ZVS for different load conditions, providing high-efficiency, low-noise output voltage. Moreover, The parallel resonant capacitor, put in the secondary of the transformer, leads to Zero Current Switching (ZCS) of the output rectifier diodes, eliminates the voltage produced by the diodes reverse recovery current and decreases the noise in theoutput voltage. In this paper, the general equations for solar PV panels will be extracted and employed to determine the variation of Power-Voltage characteristics of a specific module versus irradiance and temperature variations.

Then, a detailed review is presented for well-known resonant power converters in order to describe their structures and distinguish the difference between the proposed L3C resonant converter and its features in comparison with other resonant topologies. Later, the complete analysis of L3C the resonant converter with consideration of parasitic elements(including leakage inductances and parasitic capacitances) is obtained. Using the analytical equations extracted for fourthorderL3C resonant circuits, a 350W DC-DC power converter is designed and implemented. Finally, experimental results are presented to demonstrate the circuit performance and to prove the extreme regulation feasibility for both input and output sides. The results show the L3C resonant converter can track all input voltage variations related to maximum input power ( $V_{pv} = 24 - 45$ VDCpresented in Fig. 1(b)), while regulating the battery voltage from 230 VDC to 430 VDC.



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### RESONANT POWER CONVERTERS REVIEW

Softly switched resonant converters are excellent candidates for DC-DC power supply design with wide regulation, due to their capability to produce variable voltage gains in different operating frequencies, while providing soft switching conditions for semiconductor devices [34]. In this section. well-known resonant power converters will be presented in order to review the application of each topology and distinguish the difference between the L3Cresonant circuit and other fourth order ones. Also, this section will present and compare the voltage gain characteristics of each resonant power converter with the proposed L3C. To date. the most complicated resonant power converter contains a fifth order resonant network, including three inductors and capacitors, as presented in Fig. 4(a). The application of LCL2C topology was for high voltage DC-DC power supplies, due to its ability to absorb the parasitic capacitor of the transformer with a high turns ratio, also working with a fixed-frequency phase-shift control approach [35].

Another interesting application of LCL2Cis for battery chargers, due to its capability to extend the switching frequency beyond the second resonant frequency and cover nearly all regions required in the battery VI plane While fifth orderL3C2 resonant converters can regulate the output voltage, a wide switching frequency variation and non-operating area leads to more complicated power and control circuit design. In comparison with fifth order resonant topologies, the fourth order LCLC resonant converter does not take into account the effects of the transformer secondary leakage inductance ( $L_{s2} \rightarrow 0$ ), which leads to a less accurate steady-state

analysis and regulation capability [37]. This resonant circuit is presented in Fig. 4(b). Similar to the LCL2Cresonant circuit, LCLC employs the parallel resonant capacitor in its circuit and can be used for high voltage DC-DC applications.

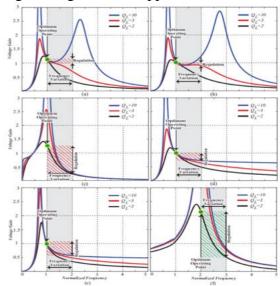


Fig. 5. Voltage gains of different resonant power converters versus normalized switching frequency and constant values of normalized parameters ( $L_n$ , $L_s$ ,  $C_n$ , QL): (a) LCL2C for  $L_n = 0.25$  and  $L_s = 5$  and  $C_n = 0.5$ , (b) LCLC for  $L_n = 0.25$  and  $C_n = 0.5$ , (c) LCC for  $C_n = 0.5$ , (d) LCLfor  $L_n = 0.25$ , (e) LCL2 for  $L_n = 0.25$  and  $L_s = 5$ , (f) L3C for  $L_n = 0.25$  and  $L_s = 5$ .

Besides. **LCLC** has two resonant frequencies and is able to extend the switching frequency beyond the second resonant frequency for a better regulation. Both third order LCC and LCL resonant converters are extracted from the LCLC resonant circuit, when  $L_p \to \infty$  or  $C_p \to 0$ , respectively. These two third order resonant power converters have been reported in the literature for avariety of applications. The LCC resonant converter has been employed in DC-DC power converters with low voltage and high voltage output level [38, 39]. As shown in Fig.4(a)-(f), this resonant converter has a relatively better regulation

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capability in comparison with other wellknown resonant converters. Even though the LCL resonant topology has become a popular resonant converter for battery charger and telecom applications, it is well understood that the existence of diode junction capacitances creates limitations for LLC resonant converter in terms of the maximu m operating frequency regulating the input-output voltage in a wide range[8, 40, 41]. In DC-DC power converters with a low output voltage, the transformer parasitic capacitor can be neglected and in Fig. 4(a)with  $Cp \rightarrow 0$ , a higher order model for LCL resonant circuit can be obtained. In fact, the LCL2 resonant converter employs third order model of the transformer, which leads to a more accurate steady state analysis. The research in [42] has proved that the LCL2 resonant converter has steeper voltage gain curves andwider output voltage regulation in comparison to LCL resonant power converter. Although all of the above-mentioned resonant topologies can to some extent respond to the input voltage variation and operate under MPP, they struggle to attain the required output voltage regulation, which makes them unsuitable for the target application.

The proposed resonant topology presented in this paper can be obtained by removing the series resonant capacitor in Fig. 4(a),Cs  $\rightarrow \infty$ . It means that the fourth order L3C can be obtained from the fifth order LCL2C resonant topology if the series resonant capacitor is selected as a DC blocking capacitor or completely eliminated from the circuit. Studying the behavior of the L3Cresonant circuit presented in Fig. 4(f) shows that this topology presents a completely different behavior for the voltage gain versus frequency. In order to compare the regulation capability of the previous resonant power converters and with the proposed one, normalized steadystate equations of each resonant topology

are used to draw voltage gains versus normalized frequency. Fig. 5presents resonant converters voltage gains for the same amount of normalized parameters and loaded quality factor, QL. For each resonant the normalized converter, frequency variation is selected to be one and the voltage regulation characteristic is studied. According to the obtained results, L3C and LCLC resonant topologies have the highest and lowest regulation capability among all resonant power converters. In comparison to the resonant converter presented in Fig. 4(a)-(e), the proposed topology presents a voltage gain versus frequency, meaning lower frequency variation for input-output regulation. More importantly, all the well known resonant converters present a voltage gain of around one for the optimum operating point, while the proposed L3C topology has a voltage gain of more than two, which makes it an excellent candidate for PV to high-voltage battery pack applications.

Another important advantage of resonant topologies with a parallel resonant capacitor, including LCL2C, LCLC, LLC, andL3C is the ability to provide soft switching conditions for output rectifier in all operating conditions. Fig. 6 shows the schematic of the output rectifier with the parallel resonant capacitor along with its main waveforms. According to Fig. 6, there are two intervals in each switching period that the output rectifier diodes turn off and



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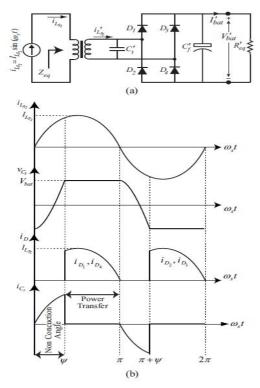


Fig. 6. a) Output rectifier with the parallel resonant capacitor, and b) main waveforms. the resonant circuit is being disconnected from the load, called on-conduction angle. This off-state interval leads to Zero Current Switching of the diodes and eliminates the noise in the output voltage that is produced by the diodes reverse recovery current [35].Also, due to the semi-sinusoidal waveform of the diode voltage, the diodes turn on and off at low dv/dt and the drawback effect of the diode junction capacitance during transient times can be minimized. Once the voltage across the parallel resonant capacitor reaches tothe output voltage, the rectifier diodes are forward biased and start transferring power to the load. Despite resonant topologies with a parallel resonant capacitor, LCL and LCL2 resonant topologies can only provide soft switching condition for output rectifier diodes when the converter operates at or below short circuit resonant frequency [42, 431.

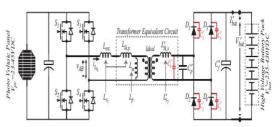


Fig. 7. Full bridge L3C resonant converter schematic with capacitive output filter, applicable for PV to high voltage battery applications.

### STEADY STATE ANALYSIS OF L3C RESONANT CONVERTER

Fig. 7 shows a complete schematic of the proposed L3Cresonant converter, including transformer secondary side and diode junction parasitic capacitances. In this figure, a fourth order model of the transformer has been employed as a model for the non ideal transformer [44]. In the proposed resonant converter, the parallel resonant capacitor in the secondary side of the transformer can merge all parasitic capacitors of the secondary side into the resonant circuit. According to Fig. 7, the proposed topology does not employ any series resonant capacitor in the primary side of the converter, which eliminates the dielectric power loss and facilitates the charger design in terms of component selection. In Fig. 7, all ofthe elements in the secondary side are defined with apostrophe, but in all equations, the variables and elements are transferred to the primary side with respect to the transformer turnratio, and are shown without an apostrophe. According to Fig. 7, the total amount of parallel capacitance in the secondary of the transformer can be obtained as follows [45]:

$$C_t = C_p + C_{w,s} + C_j$$

In order to model the interaction between the output side (theoutput rectifier, output filter, and load) and the parallel resonant capacitor, the output-side and parallel resonant elements are modeled using an equivalent impedance. The elements of the Available at https://edupediapublications.org/journals

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equivalent circuit are defined as follows [37]:

$$R_{eq} = \frac{\sin^2 \psi}{\pi C_t \omega_s}, \ C_{eq} = \frac{\pi C_t}{\psi - \sin \psi \cos \psi}$$

The angle  $\psi$  is the interval during which the output rectifier diodes turn off and the resonant circuit is disconnected from the load (presented in Fig. 6) and is given by:

$$\psi = \cos^{-1}(\frac{\pi - 2\omega_s R_L C_t}{\pi + 2\omega_s R_L C_t})$$

Fig. 8 shows the AC equivalent circuit of the resonant circuit along with the equivalent impedance of the transformer secondary side. By using this equivalent circuit, it is possible to employ the nFHA technique for analysis of the L3C resonant circuit. In order to calculate the output current, the second series resonant inductor current is considered purely sinusoidal. Due to the non-conduction angle of the output rectifier, the output current and output voltage can be obtained as follows:

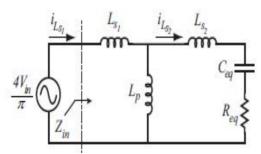


Fig. 8. AC equivalent circuit of the L3C resonant converter.

$$\begin{split} I_{o} &= \frac{1}{2\pi} (\int_{\psi}^{\pi} \mathrm{i}_{\mathrm{L}_{s_{2}}} \, \mathrm{d}(\omega_{s}t) + \int_{\pi+\psi}^{2\pi} \mathrm{i}_{\mathrm{L}_{s_{2}}} \, \mathrm{d}(\omega_{s}t)) = \frac{(1+\cos\psi)}{\pi} I_{L_{s_{2}}} \\ V_{out} &= R_{L}.I_{out} = R_{L} \, \frac{(1+\cos\psi)}{\pi} I_{L_{s_{2}}} \end{split}$$

According to Fig. 8, applying KVL and KCL gives the relation between output voltage and resonant circuit input current.

$$I_{L_{s_2}} = \left| \frac{j\omega_s L_p}{j\omega_s L_p + j\omega_s L_{s_2} + R_{eq} + \frac{1}{j\omega_s C_{eq}}} \right| I_{L_{s_1}}$$

In order to calculate the resonant circuit input current, the input impedance of the AC equivalent circuit should be obtained, which can be expressed by:

$$\mathbf{Z_{in}}(j\omega_s) = j\omega_s L_{s_1} + (j\omega_s L_p)||(j\omega_s L_{s_2} + R_{eq} + \frac{1}{j\omega_s C_{eq}})$$

One of the advantages of the proposed resonant converter is its ability to limit the output current. In this case, the L3C resonant converter can not only provide a constant current for charging the battery pack, but also protect the full-bridge inverter from over current and short circuit conditions. In over-current conditions, Req  $\rightarrow$  0 and by considering (15), Zin would be as follows:

$$\mathbf{Z_{in}}(j\omega_s) = j\omega_s L_{s_1} + j\omega_s L_p ||j\omega_s L_{s_2}|$$

In this case, the inverter switches are loaded by an inductor that can limit the switches current and also load current. In fact, the resonant circuit provides an inherent short circuit protection

at any operating frequency for the full-bridge inverter. In the AC equivalent circuit, the fundamental component of the resonant circuit input voltage (vAB1 (t)) can be obtained as follows:

$$v_{AB_1} = \frac{4V_{in}}{\pi} \sin\left(\omega_s t\right)$$

According to Fig. 8 and Eqs. (15) and (17), the amplitude of  $I_{Ls1}$  (MOSFETs current) can be obtained as follows:

$$I_{L_{s1}} = \frac{4V_{in}}{\pi |\mathbf{Z_{in}}(j\omega_s)|}$$

The following normalized parameters are introduced for the resonant circuit.

$$\begin{split} L &= L_p + L_{s_2}, L_n = \frac{L_{s_1}}{L_p}, L_s = \frac{L_{s_1}}{L_{s_2}}, \\ Z_0 &= \sqrt{\frac{L}{C_t}}, Q_L = \frac{R_L}{Z_0}, \omega_0 = \frac{1}{\sqrt{LC_t}}, \omega_n = \frac{\omega_s}{\omega_0} \end{split}$$

Considering Eq. (13), (14), (18) and (19) the normalized voltagegain of the L3C resonant converter is obtained as follows:



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$$\begin{split} M_v &= \frac{V_{out}}{V_{in}} = \frac{Q_L Z_0(1 + \cos \psi)}{\pi V_{in}}, \\ \left| \frac{\frac{j\omega_n L_s}{L_n + L_s}}{\frac{\sin^2 \psi}{\pi \omega_n} + j(\frac{\omega_n L_s}{L_n + L_s} + \frac{\omega_n L_n}{L_n + L_s} - \frac{\psi - \sin \psi \cos \psi}{\pi \omega_n})} \right| \frac{4}{\pi |\mathbf{Z_{in,n}}(j\omega_n)|} \\ \psi &= \cos^{-1}(\frac{\pi - 2\omega_0 Q_L}{\pi + 2\omega_0 Q_L}) \end{split}$$

In the next section, Eq. (20) will be employed to study the voltage gain behavior of L3C resonant converter and circuit elements design.

### L3C CONTROL STRATEGY FOR PV TO BATTERY APPLICATIONS

In this section, the control block diagram of the L3C resonant converter as a charger for PV to high voltage battery pack will be explained. A Li-Ion battery pack has two different modes in its charging profile, which are constant current and constant voltage modes, and EV on-board battery chargers are designed based on this battery charging profile [46, 47]. During the constant current mode, the battery pack is charged with the maximum allowable charging current until the battery voltage reaches the float voltage. In the constant voltage mode, the battery voltage is regulated at the float voltage ( $V'_{bat} = 430V$ ) and the charging action continues until the charging current drops to a certain amount of the rated current(e.g. 3%). The power level of the on-board battery charger for EV applications is around 3kW, and depending on the battery pack capacity, the charger can provide 10 to 20A during constant current mode [47, 48]. However, the maximum PV panel power is limited to several hundred watts, which means the charging current is much lower than on-board chargers. Although a maximum charging current is allowed for the Li-Ion battery pack, it is also possible to charge the battery at a lower rate with a longer charging time without reducing the life [49].

Fig. 9(a) presents the charging cycle profile of the high voltage battery pack when the maximum input power is available by the PV panel ( $P_{pv} = 365W \text{ at } V_{pv} = 44.5VDC$ according to Fig. 3(b)) and the battery voltage dropped to its minimum level (V'bat = 230VDC). Since the target for the PV to the battery application is to extract the maximum power from the input, the charging profile can be divided into two main parts, which are constant power mode (MPPT) and constant voltage mode. Considering the input power (Ppv) equal to 365Wand an average efficiency of 96% for resonant power converter, transferred power to the battery by the resonant converter will be 350W. During the first stage of the charge, the battery voltage increases from its minimum level to the charge float voltage and the output power is constant; therefore, the charging current slowly decreases from 1.5A to 0.8A, as shown in Fig. 9(a). In the second stage, the battery voltage is regulated at the float voltage until the battery is fully charged. Fig. 9(b) shows operating trajectories of the L3C resonant converter during different states of charge. The details of each trajectory are presented as follows:

1): The starting switching frequency of the L3C resonant converter is set to be the maximum value. Starting from  $f_s = 400 \text{kHz}$ , the output voltage is less than 230VDC, and the converter works under noload condition. In this case, the controller decreases the switching frequency until the output voltage reaches the battery voltage  $(V'_{bat} = 230 \text{VDC})$ .

2): When the output voltage reaches to 230VDC, the battery pack starts to sink the charging current. At this stage, the MPPTalgorithm kicks in and, by decreasing the switching frequency, the maximum power is extracted from the PV panel. Finally, the operating point of the converter



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will be settled in  $V'_{bat} = 230VDC$ and  $I'_{bat} = 1.5ADC$ .

3) and 4): According to Fig. 9(a), during each state of charge the battery pack has different voltage and current levels, and therefore behaves similar to a variable resistor. Starting fromV'bat = 230VDC to V'bat = 430VDC, Fig. 9(b) presents three voltage gain curves of the L3C resonant converter when the maximum power is transferred to the battery pack (gray, red, and yellow curves). During these states, the MPPT algorithm is looking for maximum power from the PV panel and adjusts the switching frequency accordingly. In this stage, the charging current decreases from 1.5ADC to 0.8ADC.

5) and 6): When the battery voltage reaches 430VDC, the battery state of charge enters constant voltage mode. In this stage, the voltage control loop kicks in and regulates the battery voltage at the float voltage by increasing the switching frequency. When the charging current drops to a predefined value (e.g. 3%), the battery charger enters to standby mode. Fig. 9(c) demonstrates the control block diagram, which is employed for the L3C resonant converter for PV to high voltage battery pack applications. As mentioned, the control scheme consists of the MPPT algorithm and voltage loop, which are responsible for extracting the maximum power from PV panel and regulating the output voltage at the float level, respectively. As mentioned in Section IV, since the resonant circuit provides an inherent short circuit protection at any and operating frequency limits the maximum output current to 1.5A, the proposed control scheme does not employ any current loop, neither for the output current regulation, nor for the short circuit protection.

### RESONANT POWER CONVERTER DESIGN

In this section, the design procedure will be presented for power conditioning of a 350W solar PV panel to high voltage battery pack, using the proposed L3C resonant converter. The main object during design procedure is to design L3C in such a way as to cover all regions of the Power-Voltage plane presented in Fig. 1(b)( $V_{pv}=24-45 VDC$ ), while responding to battery different states of charge ( $V_{out}=230-430 VDC$  and  $I_{out}=0-1.5 ADC$ ). According to the steady-state analysis of the L3C resonant converter, the voltage gain equation, obtained for L3C steady state

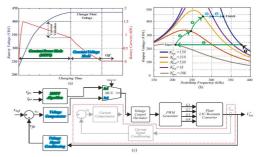


Fig. 9. a) The charging cycle profile for a high voltage battery pack by considering the constant power mode (MPPT) for the PV panel and constant voltage mode during the charger float voltage, b) The L3C battery charger trajectory for different states of charge, and c) The L3C control loop blockdiagram for PV to battery applications. (Due to the inherent current limit capability of the L3C resonant power converter, the current control loop (blocksinside the red dashed line) is eliminated from the control scheme.

condition, is a function of four normalized variables ( $L_n$ ,  $L_s$ ,  $Q_L$ ,and  $\omega_n$ ). The desired normalized parameters should be selectedin such a way to provide essential voltage gain for boosting thevoltage from PV panel to high voltage battery while providing soft switching conditions for all semiconductor devices. Fig. 10 presents the voltage transfer function  $M_v$  versus normalized frequency for  $L_n=0.25$ ,  $L_s=5$ ,  $Q_L=2$  as the normalized parameters. In this case,  $Q_L=2$  presents the load condition related to the

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maximum output voltage and output power,  $V_{out} = 430\,\text{VDC}$  and  $P_{out} = 350\,\text{VDC}$  at the minimum surface temperature and maximum irradiance ( $-20\,^{\circ}\text{C}$  and  $1000\,\text{W/m}^2$ ). The main specifications of the battery charger are presented in Table I. According to Table I, the based resonant frequency is equal  $tof_0 = 140\,\text{kHz}$ . By choosing  $\omega_n = 2$ , the switching frequency is calculated as:

$$f_0 = 140kHz, \ f_n = 2, \ f_n = \frac{f_s}{f_0} \implies f_s = 280kHz$$

The voltage gain at  $f_n = 2$  (brown curve,  $Q_L = 2$ ) for the nominal load conditions are given as follows:

$$M_v = 2.2$$
 for  $Q_L = 2$ ,  $L_n = 0.25$ ,  $L_s = 5$ 

Therefore, the parallel resonant capacitor voltage (output voltage) and transformer turns-ratio are calculated as follows:

$$V_{out,n} = M_v.V_{in} = 2.2 * 44.5 = 98VDC,$$
  
 $n = \frac{N_p}{N_s} = \frac{V_{out,n}}{V'_{out,n}} \implies n = \frac{98}{430} = 0.23$ 

According to Eq. (19), the characteristic impedance and resonance frequency are given by:

$$Z_0 = \frac{V_{out}^2}{P_{out}Q_L} = 13.8\Omega, \ \omega_0 = \frac{\omega_s}{\omega_n} = 280\pi e^3$$

Therefore, the resonant components are obtained as follows:

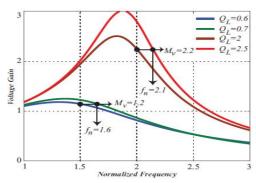


Fig. 10. Magnitude of voltage transfer function  $(M_{\nu})$ , using the voltage gain equation of the L3C resonant converter for  $L_n=0.25$ , and  $L_s=5$ ,with constant values for normalized load resistances  $(Q_L)$ .

### TABLE I L3C PROTOTYPE PLATFORM PARAMETERS

Parameters	Value
Input Voltage, $V_{in}$	24 - 45VDC
Output Voltage, $V'_{out,n}$	230 - 430VDC
Maximum Output Power, Pout	350W
Switching Frequency Range, fs	180 - 400kHz
Based Resonant Frequency, $f_0$	140kHz
First Series Resonant Inductance, $L_{s_1}$	$3.75\mu H$
Second Series Resonant Inductance, $L_{s_2}$	$0.7\mu H$
Parallel Resonant Inductance, $L_p$	$15\mu H$
Parallel Resonant Capacitance, $\hat{C}'_p$	5nF
Transformer Turn's Ratio, $n = \frac{N_p}{N_s}$	4:18
$L = \frac{Z_0}{\omega_0} = 15.7 \mu H, \ C_p = \frac{1}{Z_0}.$	${\omega_0} = 82nF,$
$L_{s_1} = 3.75 \mu H$ , $L_{p_1} = 15 \mu H$ , $I_{p_2} = 15 \mu H$ , $I_{p_3} = 15 \mu H$	$I_{uu} = 0.7 \mu H$

The L3C resonant circuit parameters used in the experimental prototype are summarized in Table I.

### EXPERIMENTAL RESULTS AND THEORY VALIDATION

In order to investigate the performance of the proposed fourth order L3C resonant converter, a prototype platform has been employed to extract the maximum obtainable coverage of Fig. 1(b) under different output voltage and current conditions. The test setup picture is presented in Fig.11. According to Table I, the switching frequency range is between 180 and 350kHz, and the frequency for maximum output power at maximum output voltage set to be 280kHz.



Fig. 11. Prototype test platform of the L3C resonant power converter.

Due to high current amplitude in the primary side, a high efficiency full-bridge



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low inverter with on-resistance MOSFET(2.5 mOhm) is used to convert the input DC voltage to a high-frequency AC voltage. The resonant tank consists ofan external air-core inductor ( $L_{ext} = 3.1 \mu H$ placed in series with the transformer's primary side), an external parallel capacitor (placed in the secondary side of the transformer), and the parasitic components of the transformer (including primary and secondary leakage inductances magnetizing inductance). In order investigate the power converter performance, the power platform was tested under different input-output scenarios, and Fig. 12 shows the experimental results for the minimum and maximum output voltage under different input voltage and maximum available power conditions. Each figure contains of full-bridge inverter voltage, resonant circuit current, transformer secondary side voltage, and transformer secondary side current.

The experimental results show the response of the L3Cresonant converter to inputoutput voltage variations, which caused as a result of either changing in the PV panel, or battery state of charge. As mentioned in the introduction, the battery charger must be able to respond to different modes of the charge algorithms and track the input voltage variation in order to transfer the maximum available power. According to extracted experimental results, the proposed fourth orderL3C resonant converter can regulate the output voltage and current over a wide range  $(V'_{bat} = 230 - 430VDC)$  and  $I'_{bat} = 0- 1.5ADC$ , and track the input voltage variation in order to extract the maximum available power from PVpanel, using the switching frequency modulation. In order to provide soft switching conditions for power MOSFETs, the phase angle of input impedance should be positive. In other words, the input resonant circuit current should remain lagging the full-bridge inverter voltage. According to experimental

results, the zero crossings of the resonant circuit input current are within the inverter output voltage pulse and, as a result, in all of the conditions described, the full-bridge switches are fully turned on under zero voltage.

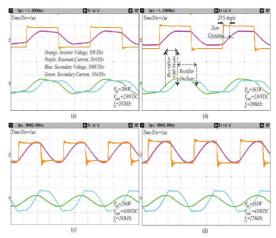


Fig. 12. Experimental results of fourth order L3C resonant converter platform under different PV panel conditions and minimum-maximum output voltage, a)  $1000 \text{W/m}^2$  at  $25\,^{\circ}\text{C}$ , b)  $1000 \text{W/m}^2$  at  $-20\,^{\circ}\text{C}$ , c)  $1000 \text{W/m}^2$  at  $25\,^{\circ}\text{C}$ , and d)  $1000 \text{W/m}^2$  at  $-20\,^{\circ}\text{C}$ .

On the other hand, to provide a high-quality output voltage and maximize the converter efficiency, it is vital to realize soft commutation for the output rectifier diodes. Basically, the reverse recovery current of output rectifier diodes during the turn-off transition leads to high voltage peak across output diodes and as a result, diodes with more withstand voltage should be selected, which means more conduction losses. Experimental results presented in Fig. 12 include the experimental waveforms of voltage and current on the secondary side of theL3C the transformer in resonant converters. According to these figures, ZCS is provided for output rectifier diodes, and voltage peak is eliminated. As indicated in experimental figures, in the end of power transfer interval, once the transformer secondary current reaches zero, the output

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diodes current smoothly reaches zero. This causes ZCS at turn-off and avoids any reverse recovery losses in the output diodes. In addition, the voltage across the diodes is smooth and sinusoidal during the switching transitions which leads to the minimum negative impact of the diode junction capacitance in transient time. As mentioned in Section IV and presented in Fig. 7, there is not any series capacitor in the L3C resonant topology. There is a chance that series capacitor elimination along with no similarity of full bridge gate signals leads to DC current offset of the resonant current and transformer saturation. However, due to the small air gap of the transformer between its ferrite cores for the magnetizing inductance adjustment, the transformer saturation can be prevented. Experimental results show that in all presented conditions, the resonant current has no DC offset and the transformer operates without any saturation.

Experimental results related to over current and short-circuit conditions demonstrate the ability of the proposed converter in terms of output current limitation during overload conditions. Fig. 13 presents a worst-case scenario where the converter operates in output short circuit and maximum input voltage conditions. According to this figure, the converter can safely work in this situation and limit the output current by I'out = 1.5ADC. Fig. 14 presents the efficiency curves for different input PV panel voltages and output battery conditions. experimental point in this figure is related to one maximum power point, presented in Fig. 3. According to these curves, the resonant converters designed for this study present a maximum efficiency equal to 97.5%. Fig. 14(a) shows the converter efficiency is above92% from full load to 50% of the load, but due to the circulating current in light load condition, the converter

efficiency decreases at light loading conditions

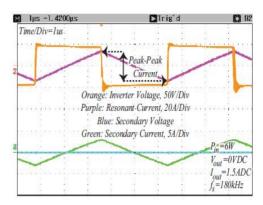


Fig. 13. L3C resonant converter short circuit test at minimum switching frequency ( $f_s = 180 \text{kHz}$ ) and maximum input voltage ( $V_{in} = 44 \text{VDC}$ ).

#### **CONCLUSION**

This paper introduced the L3C resonant power converter with extreme regulation capability, which can be employed for solar battery charger applications. The main advantages of the proposed resonant converter are its ability to respond to different battery states of charge, while tracking the input voltage variations of the PV panel, which occur as a result of changes to the maximum power point. The proposed converter has the voltage gain of more than two, which contributes to the required voltage gain for boosting the input voltage to a high voltage one. Also, the L3C topology can provide an inherent overcurrent protection, which can control the battery current during constant current mode and simplifies the control and protection circuit design. In addition, soft switching of semiconductor devices provides efficiency, and the capability of working in high switching frequency. The complete analysis of the resonant converter along mathematical with equations were presented. The experimental results verify the theoretical analysis and the performance of the proposed converter in terms of tracking the input voltage for maximum



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power extraction and operating under different output voltage and current conditions.

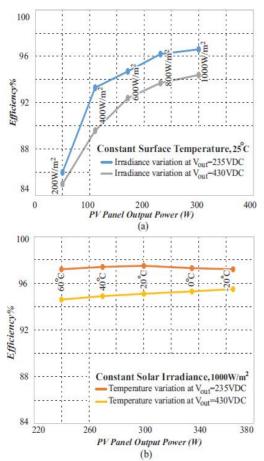


Fig. 14. Efficiency curves of the L3C resonant converter for different conditions, a) Blue and gray lines present efficiency for constant surface temperature (25°C) and irradiance variations at  $V_{out} = 235 \text{VDC}$  and  $V_{out} = 430 \text{VDC}$ , respectively, and b) Orange and yellow lines present efficiency for constant solar irradiance (1000W/m²) and temperature variations at  $V_{out} = 235 \text{VDC}$  and  $V_{out} = 430 \text{VDC}$ , respectively.

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