

# International Journal of Research (IJR) e-ISSN: 2348-6848, p- ISSN: 2348-795X Volume 3, Issue 01, January 2016 Available at http://internationaljournalofresearch.org

# Design and simulation SOLAR Power Generation System

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# ABSTRACT:

This paper proposes a new solar power generation system, which is composed of a DC/DC power converter and a new seven-level inverter. The DC/DC power converter integrates a DC-DC boost converter and a transformer to convert the output voltage of the solar cell array into two independent voltage sources with multiple relationships. This new seven-level inverter is configured using a capacitor selection circuit and a full-bridge power converter, connected in cascade. The capacitor selection circuit converts the two output voltage sources of DC-DC power converter into a three-level DC voltage and the fullbridge power converter further converts this threelevel DC voltage into a seven-level AC voltage. In this way, the proposed solar power generation system generates a sinusoidal output current that is in phase with the utility voltage and is fed into the utility. The salient features of the proposed seven-level inverter are that only six power electronic switches are used and only one power electronic switch is switched at high frequency at any time. A prototype is developed and tested to verify the performance of this proposed solar power generation system.

*Keywords*: multilevel inverter; grid-connected; pulse width modulated (PWM) inverter

# 1. Introduction

The extensive use of fossil fuels has resulted in the global problem of greenhouse emissions. Moreover, as the supplies of fossil fuels are depleted in the future, they will become increasingly expensive. Thus solar energy is becoming more important since it produces less pollution and the cost of fossil fuel energy is rising, while the cost of solar arrays is decreasing. Inparticular, smallcapacity distributed power generation systems using solar energy may be widely used in residential applications in the near future ..

The power conversion interface is important to grid-connected solar power generation systems because it converts the DC power generated by a solar cell array into AC power and feeds this AC power into the utility grid. An inverter is necessary in the power conversion interface to convert the DC power to AC power [2-4]. Since the outputvoltage of a solar cell array is low, aDC-DC power converter is used in a small-capacity solar power generation system to boost the output voltage so it can match the DC bus voltage of the inverter.

#### Existing System:

The power conversion efficiency of the power generation system. The proposed solar power generation system is composed of a solar cell array, a DC-DC power converter and a new seven-level inverter. The solar cell array is connected to the DC-DC power converter, and theDC-DC power converter is a boost converter that incorporates a transformer with a turn ratio of 2:1. The DC-DC power converter converts the output power of the solar cell array into two independent voltage sources with multiplerelationships, which supply the seven-level inverter. This new seven-level inverter is composed of a capacitor selection circuit and a full-bridge power converter, connected in cascade. The power electronic switches of capacitor selection circuit determine the discharge of the two capacitors while the two capacitors are being discharged individually or in series. Because of the multiple relationships between the voltages of the DC capacitors, the capacitor selection circuit outputs a three-level DC voltage. The full-bridge power converter further converts this threelevel DC voltage to a seven-level AC voltage that



issynchronized with the utility voltage. In this way, the proposed solar power generation system.



Figure 1: Matlabsimulink main diagram

#### **Proposed System:**

As seen in Fig. 1, the DC-DC power converter incorporates a boost converter and a current-fed forward converter. The boost converter is composed of an inductor, LD, a power electronic switch, SD1, and a diode, DD3. The boost converter charges capacitor C2 of the seven-level inverter. The current-fed forward converter is composed of an inductor, LD, power electronicswitches, SD1 and SD2, a transformer and diodes, DD and DD2. The current-fed forward converter charges capacitor C1 of the seven-level inverter. The inductor, LD, and the power electronic switch, SD1, of the current-fed forward converter.

Figure1 shows the operating circuit of the DC-DC power converter when SD1 is turned on. The solar cell array supplies energy to the inductor LD. When SD1 is turned off and SD2 is turned on, its operating circuit is shown in Fig.1. Accordingly, capacitor C1 isconnected to capacitor C2 in parallel through the transformer, so the energy of inductor LD and the solar cell array charge capacitor C2 through DD3 and charge capacitor C1 through the transformer and DD1 during the off-state of SD1. Since capacitors C1 and C2 are charged in parallel by using the transformer, the voltage ratio of capacitors C1 and C2 is the same asthe turn ratio (2:1) of the transformer.

#### Securely determining own location.

In mobile environments, self-localization is mainly achieved through Global Navigation Satellite Systems, e.g., GPS, whose security can be provided by cryptographic and noncryptographic defense mechanisms. Alternatively, errestrialspecialpurpose infrastructure could be used along with techniques to deal with non honest beacons. We remark that this problem is orthogonal to the problem of NPV. In the rest of this paper, we will assume that devices employ one of the techniques above to securely determine their own position and time reference. As seen in Fig. 1, the seven-level inverter is composed of a capacitor selection circuit and a full-bridge power converter, which are connected in cascade. Operation of the seven-level inverter can be divided into the positive half cycle and the negative half cycle of the utility. For ease of analysis, the power electronic switches and diodes are assumed to be ideal, while the voltages of both capacitors C1 and C2 in the capacitor selection circuit are constant and equal to Vdc/3 and 2Vdc/3, respectively.

Since the output current of the solar power generation system will be controlled to be sinusoidal and in phase with the utility voltage, the output current of the seven- level inverter is also positive in the positivehalf cycle of the utility. The operation of the seven-level inverter in the positive half cycle of the utility can be further divided into four modes, as shown in Fig.3.

# **Experimental Results**

To verify the performance of the proposed solar power generation system, a prototype was developed with a controller based on the DSP chip TMS320F28035. The power rating of the prototype is 500W, and the prototype was used for a single-phase utility with 110V and 60Hz. Table II shows the main parameters of the prototype.



Figure 2: Voltage and current waveforms without LC filter

# Seven-Level Inverter



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Figure 3:THD analysis 19.44% without LC filter



Figure 4: Voltage and current waveform using LC filter



Figure 5:THD analysis 5.56% with LC filter

Generation system can effectively trace the maximum power of solar cell array.

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