



# Improved Performance Of Micro Grid By Resistive Type SFCL with PMSM

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**Abstract:** This paper tests and analyzes the working capability of a resistive superconducting fault current limiter (SFCL) in a micro grid. The micro grid is a test-bed, consisting of a conventional power plant, two renewable energy sources in the form of wind-farm and solar-farm as two distributed generation (DG) units and five loads (i.e., industrial and domestic). Utilization of DG in mainstream is increasing and hence, various consequences that arise from DG penetration are to be considered accordingly. It is observed that DG penetration in existing distribution alters the fault current during a grid disturbance and hence imbalances the system. Based on this, the research work focuses on contribution of fault current from renewable energy sources in a micro grid, and how resistive SFCL offers positive result in minimizing the fault levels. Permanent magnet synchronous motors (PMSM) are typically used for high-performance and high-efficiency motor drives. High-performance motor control is characterized by smooth rotation over the entire speed range of the motor, full torque control at zero speed, and fast acceleration and deceleration. To achieve such control, vector control techniques are used for PM synchronous motors. The analysis of improved performance of resistive SFCL applied in PMSM drive. The proposed concept can be implemented to resistive type SFCL applied to PMSM drive using MATLAB / SIMULINK software.

**Key words-**transient performance, distributed generation (DG), Superconducting fault current limiters (SFCL), fault current limiter (FCL).

## I INTRODUCTION

Application of fault current limiters (FCLs) based on high temperature superconductors is one of the promising solutions of the fault current problem in power electric systems. Several power prototypes of the FCLs have been successfully tested [1]. The achieved characteristics of some of them could allow applying them in the distribution networks. At present there are developments directed to design superconducting FCL for application in high voltage lines. The intense interest in these FCLs is explained by achievements of the technology of high temperature superconductors and expected advantage from their application in power systems. The

requirements for FCL parameters and for properties of superconductors are discussed in many publications (for examples see [2], and references noted in them). The requirements for FCLs can be separated according to their influence on electromagnetic processes in the system and influence on stability of parallel operation of electrical machines. It was shown that FCLs based on high temperature superconductors meet to the first type requirements: they have low impedance under the normal operation regime of the protected circuit; under a fault their impedance rapidly increases limiting the first peak of a fault current and its steady state value without appearance of dangerous overvoltage; FCLs quickly return into the initial low impedance state after the limitation of fault currents. Usually parameters and installation places of FCLs are chosen to ensure the required current limitation [3].

An FCL application has not to lead to worsening the static and transient stability of the power system. An ideal limiter has zero impedance under the normal conditions of the circuit to be protected. In reality, it is enough that the voltage drop across an FCL under the normal conditions of a circuit is less than several percents (usually 5%) of the rated circuit voltage. In this case the FCL does not disturb the static stability of the power system operation and does not influence handling properties of the lines [4]. There are several studies devoted to analysis of the transient stability of a power system wheresuperconducting FCLs are installed. In these investigations the attention is given to two FCL designs, resistive and inductive, installed in one of the parallel transmission lines. It has been shown that the FCLs not only limit a fault current but also can increase the stability of the synchronic operation of the electric machines [5].

In this paper, we analyze in detail the influence of superconducting inductive FCLs on the transient stability of the power system. The FCL influence on the transient stability was analyzed using the law of equal areas. On the one hand this approach clearly shows how the installation

place of a device and its parameters change the transient stability of power systems [6].

The fault current limiters (FCL) are regarded as the suitable solution to solve excessive fault current problems. Active superconducting fault current limiter (ASFCL) voltage compensation type is a novel topology of FCL. This type SFCL not only preserves the merits of bridge type SFCL such as the automatic switch to the current limiting mode and without the quench of the superconductor, but also has the particular abilities of controlling the steady fault current and compensating active and reactive power for AC main circuit in the normal state [8]. In view of that the introduction of a SFCL can impact the coefficient of grounding, which is a significant contributor to control the induced overvoltage's amplitude; the change of the coefficient may bring positive effects on restraining over voltages. We have proposed a voltage compensation type active SFCL. In previous work and analyzed the active SFCL's control strategy and its influence on relay protection [9].

## II THEORETICAL ANALYSIS

### A) Modeling of the Resistive SFCL

Based on the experimental studies for a resistive SFCL being applied in the actual power distribution system [7], its mathematical model can be expressed as:

$$R(t) = \begin{cases} 0 & (t < t_0) \\ R_n [1 - \exp(-\frac{t-t_0}{\tau})]^{\frac{1}{2}} & (t_0 \leq t < t_1) \\ a_1(t-t_1) + b_1 & (t_1 \leq t < t_2) \\ a_2(t-t_2) + b_2 & (t_2 \leq t < t_3) \\ 0 & (t \geq t_3) \end{cases} \quad (1)$$

Where  $R_n$  and  $\tau$  represent the impedance being saturated at normal temperature and time constant, respectively. In addition,  $t_0$ ,  $t_1$ , and  $t_2$  represent quench-starting time, the first recovery starting time, and the secondary recovery-starting time, respectively. In the case of properly adjusting the thermal environment and setting the system parameters, the SFCL's recovery time may be less than 0.5 s [8], so as to match up the auto-reclosing operation. As shown in Fig.1, it indicates the detailed quenching and recovery characteristics.

### B) Influence of the Resistive SFCL on a Micro-Grid's Transient Performance:

The schematic diagram of a typical micro-grid integrated with the resistive SFCL is shown in Fig.2, where the SFCL is installed at the point of common

coupling (PCC) between the micro-grid and the main network.

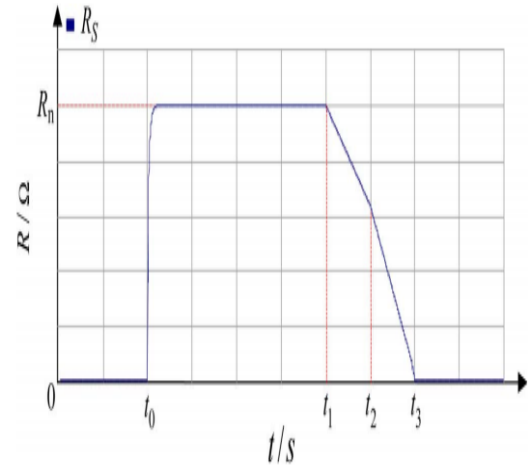


Fig.1. Quenching and recovery characteristics of a resistance-type SFCL

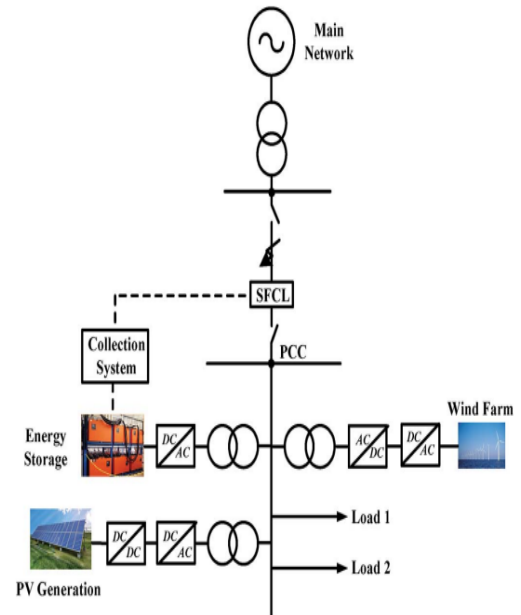


Fig.2. Schematic of a typical microgrid integrated with the SFCL.

In regards to the energy storage device, photovoltaic (PV) plant and wind farm, all of these DG units are accessed to the micro-grid through their inverters [9]. Note that, the energy storage device will be served as a master DG, which is used for stabilizing the micro-grid. In general, the master DG has two control patterns, so called as the P-Q control and the V-f control.

When the micro-grid is under the grid-connected state, each of the DG units will use the P-Q control. In the

case that a short-circuit fault happens in the main network, the micro-grid may be operated to work in the islanded state. The master DG's objective is to maintain the micro-grid's frequency and voltage stability as far as possible, and its control pattern will switch to the V-f control from the original P-Q control. As reasonably controlling the master DG is a basic method of ensuring the transient performance, employing the resistive type SFCL is expected to affect the control mechanism more actively and make the transition process be more smooth. Due to the SFCL's rapid quenching characteristics, it can be conducted as a control trigger since the fault current is timely detected to be larger than its critical value. That is to say, the SFCL's trigger signal caused by the superconducting-normal (S-N) transition will be sent to a collection system, and then be used for activating the master DG's control switching.

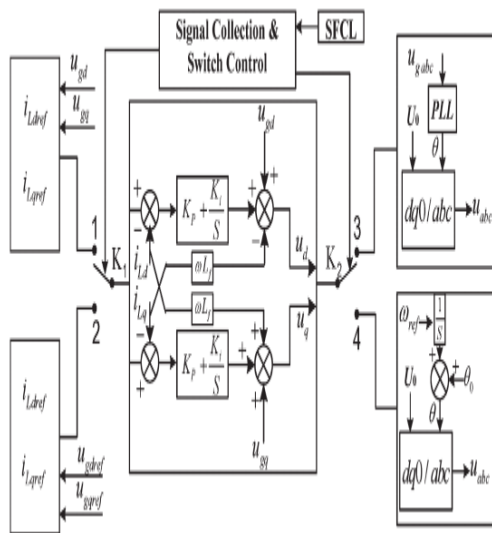


Fig.3. Control strategy of the energy storage device in consideration of the resistive SFCL's trigger signal.

Concerning how to effectively implement the master DG's control switching, a feasible way is presented as follows. Fig.3 shows the control strategy of the energy storage device in consideration of the resistive SFCL's trigger signal, from this figure, specialized current-input and voltage-output channels are arranged for controlling the master DG. The channels 1 and 3 are used for realizing the master DG's P-Q control, and the current-input references ( $i_{Ldref}$ ,  $i_{Lqref}$ ) can be expressed as:

$$\begin{cases} i_{Ldref} = \frac{2}{3} \frac{P_{gref}u_{gd} + Q_{gref}u_{gq}}{u_{gd}^2 + u_{gq}^2} \\ i_{Lqref} = \frac{2}{3} \frac{P_{gref}u_{gq} - Q_{gref}u_{gd}}{u_{gd}^2 + u_{gq}^2} \end{cases} \quad (2)$$

Where  $P_{gref}$  and  $Q_{gref}$  are the given power references;  $u_{gd}$  and  $u_{gq}$  are respectively the d-axis and q-axis components of the network-side voltage. According to the control block diagram as demonstrated in Fig. 3, the P-Q control's output-voltage signals ( $u_d, u_q$ ) can be obtained. From the figure, the following variables are defined.  $\omega$  is the fundamental angular frequency;  $L_f$  is the filter inductance;  $i_{Ld}$  and  $i_{Lq}$  are the d-axis and q-axis components of the energy storage converter's output current. On the other side, the channels 2 and 4 are used to achieve the master DG's V-f control. Based on the given network-side voltage references ( $u_{gdref}, u_{gqref}$ ), the V-f control's current input references ( $i_{Ldref}, i_{Lqref}$ ) can be expressed as:

$$\begin{cases} i_{Ldref} = \left( K_{p1} + \frac{K_{i1}}{s} \right) (u_{gdref} - u_{gd}) - \omega C_f u_{gq} + i_{gd} \\ i_{Lqref} = \left( K_{p1} + \frac{K_{i1}}{s} \right) (u_{gqref} - u_{gq}) + \omega C_f u_{gd} + i_{gq} \end{cases} \quad (3)$$

Where  $C_f$  is the filter capacitance;  $K_{p1}$ ,  $K_{i1}$  are the proportional and integral parameters of the voltage regulator;  $i_{gd}$  and  $i_{gq}$  are the d-axis and q-axis components of the network-side current. In a similar way, the V-f control's output-voltages ( $u_d, u_q$ ) can be obtained.

In accordance to the resistive SFCL's quenching trigger, using the logical switches K1 and K2 can precisely implement the control switching for the master DG. It should be pointed out that, the resistive SFCL also has the responsibility of limiting the fault current from the micro-grid to the short-circuit location. Before the static-state switch equipped at the PCC is operated to make the micro-grid be disconnected from the main network, the current fluctuations can be suppressed within an acceptable level, and thus the micro-grid's transient behaviors can be ensured more effectively.

### C) Technical Discussion on the Design of the SFCL

**1) Superconducting Material:** Currently, the bulk Bi series and YBa2Cu3Ox (YBCO) second-generation (2G) are the main high temperature superconducting (HTS) materials for electric power applications. Considering that the commercial YBCO 2G tapes may have the high resistivity matrix with a linear resistance of 0.354  $\Omega/m$ , the transition to the normal conducting state may occur from 2 ms up to 4 ms after the start of fault current. Besides, the YBCO 2G tapes with stainless steel reinforcement can provide good mechanical properties, such as tensile

strength above 250 MPa at room temperature. Since the YBCO 2G components may actuate faster than the Bi-2212 components, and the expected current limitation is higher for the YBCO 2G components after the S-N transition, the YBCO 2G tapes may be more suitable for making the resistive SFCL.

**AC Loss:** In a sense, the AC loss will be an important factor affecting the SFCL's engineering application. Its AC loss can be measured with a standard electrical technique, and also calculated by finite-element simulations. Theoretically, the superconductor's electrical properties may be modeled with a nonlinear power law where voltage varies as  $(J/J_c)^n$ .  $J$  and  $J_c$  are respectively

Where  $f$  is the frequency,  $S$  is the superconductor's cross-section, and  $J$  and  $E$  are the current density and the electric field at each finite element method (FEM) node. From the simulation and experimental results, the AC loss will be reduced in the presence of an externally applied AC magnetic field, but be increased in the presence of AC transport current. To reduce the SFCL's AC loss as much as possible, the coupling transformer with the superconducting limiting coil may be properly considered.

**2) Electrical Insulation:** In the case of that the sub-cooled liquid nitrogen is used to be the SFCL's cooling system, the dielectric strength and bubble suppression effect as the electrical insulation design's factors in the cryostat can be enhanced by increased pressure. Through injecting the non-condensable gas such as gaseous helium (GHe) or gaseous neon (GNe) into the cryostat, the pressure of the cooling system can be controlled. In addition, the gap distance between a cryostat and superconducting tapes is one of the emphasis factors of the SFCL's insulation design. According to, the shield ring attached to the copper current lead can reduce the gap distance between the cryostat and the SFCL. And based on the request of AC withstand voltage and shield ring's diameter, the gap distance can be designed. From the aforementioned brief discussions, some preliminary conclusions can be obtained, and the detailed engineering design of the SFCL for an actual micro-grid system will be performed in the near future.

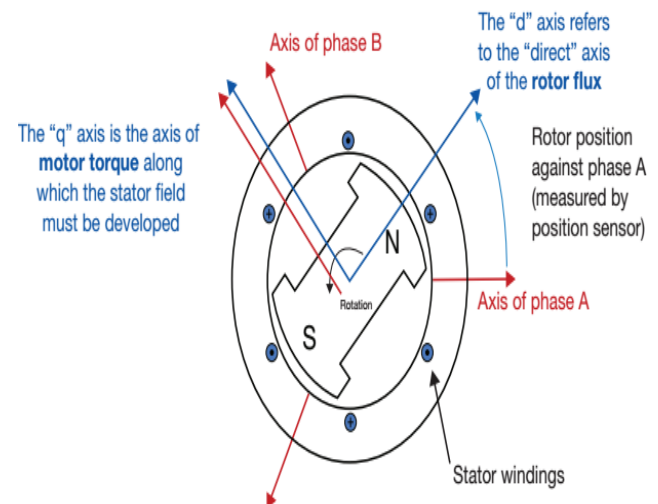
### III PERMANENT MAGNET SYNCHRONOUS MOTORS (PMSM)

Permanent magnet synchronous motors (PMSM) are typically used for high-performance and high-efficiency motor drives. High-performance motor control is characterized by smooth rotation over the entire speed range of the motor, full torque control at zero speed, and fast acceleration and deceleration. To achieve such control, vector control techniques are used for PM synchronous motors. The vector control techniques are

usually also referred to as field-oriented control (FOC). The basic idea of the vector control algorithm is to decompose a stator current into a magnetic field-generating part and a torque-generating part. Both components can be controlled separately after decomposition. Then, the structure of the motor controller (vector control controller) is almost the same as a separately excited DC motor, which simplifies the control of a permanent magnet synchronous motor. Let's start with some basic FOC principles.

$$P = f \int_0^{\frac{1}{f}} \int_s \mathbf{J} \cdot \mathbf{E} dS dt \quad (\text{W/m}) \quad (4)$$

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### Torque Generation

A reactance torque of PMSM is generated by an interaction of two magnetic fields (one on the stator and one on the rotor). The stator magnetic field is represented by the magnetic flux/stator current. The magnetic field of the rotor is represented by the magnetic flux of permanent magnets that is constant, except for the field weakening operation. We can imagine those two magnetic fields as two bar magnets, as we know a force, which tries to attract/repel those magnets, is maximal, when they are



perpendicular to each other. It means that we want to control stator current in such a way that creates a stator vector perpendicular to rotor magnets. As the rotor spins we must update the stator currents to keep the stator flux vector at 90 degrees to rotor magnets at all times. The reactance torque of an interior PM type PMSM (IPMSM) is as follows, when stator and rotor magnetic fields are perpendicular.  $Torque = 32pp l P M I q s$   $pp$  – Number of pole pairs  $l P M$  – Magnetic flux of the permanent magnets  $I q s$  – Amplitude of the current in quadrature axis As shown in the previous equation, reactance torque is proportional to the amplitude of the q-axis current, when magnetic fields are perpendicular. MCUs must regulate the phase stator current magnitude and at the same time in phase/angle, which is not such an easy task as DC motor control.

### How to Simplify Control of Phase Currents to Achieve Maximum Torque

DC motor control is simple because all controlled quantities are DC values in a steady state and current phase/ angle is controlled by a mechanical commutator. How can we achieve that in PMSM control? DC Values/Angle Control First, we need to know the rotor position. The position is typically related to phase A. We can use an absolute position sensor (e.g., resolver) or a relative position sensor (e.g., encoder) and process called alignment. During the alignment, the rotor is aligned with phase A and we know that phase A is aligned with the direct (flux producing) axis. In this state, the rotor position is set to zero (required voltage in d-axis and rotor position is set to zero, static voltage vector, which causes that rotor attracted by stator magnetic field and to align with them [with direct axis]).

1 Three-phase quantities can transform into equivalent two-phase quantities (stationary reference frame) by Clarke transformation.

2. Then, we transform two-phase quantities into DC quantities by rotor electrical position into DC values (rotating reference frame) by Park transformation. The electrical rotor position is a mechanical rotor position divided by numbers of magnetic pole pairs  $pp$ . After a control process we should generate three-phase AC voltages on motor terminals, so DC values of the required/generated voltage should be transformed by inverse Park/Clarke transformations.

### IV MATLAB/SIMULATION RESULTS

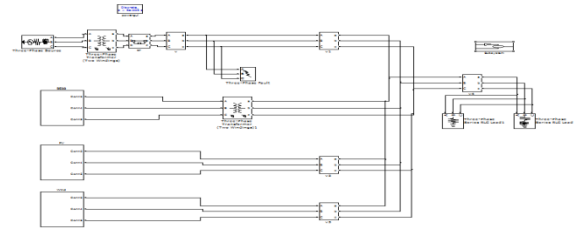


Fig 4 Matlab/simulation circuit of a typical microgrid integrated with the SFCL.

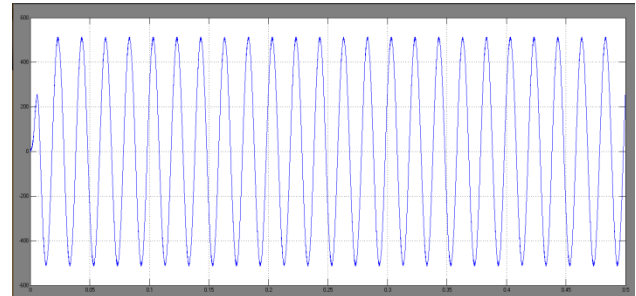


Fig 5 simulation wave form of fault current at the PCC

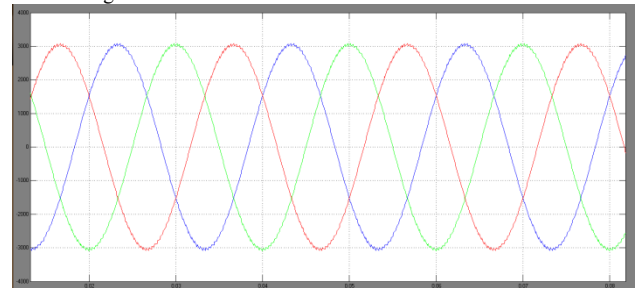


Fig 6 simulation wave form of fault voltage at the PCC

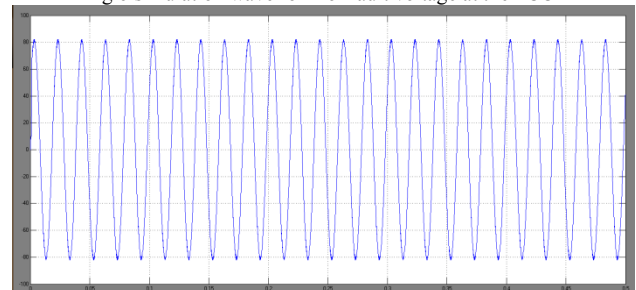


Fig 7 simulation wave form of wind current

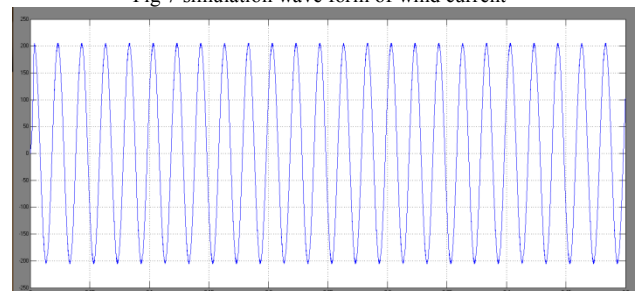


Fig 8 simulation wave form of PV current

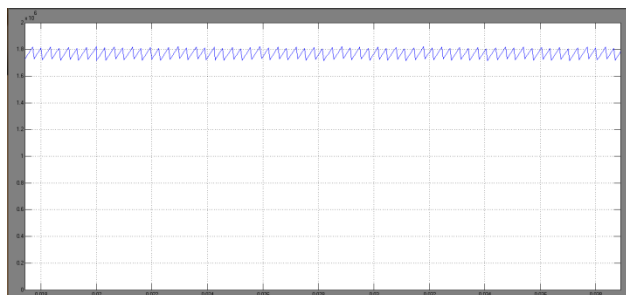


Fig 9 simulation wave form of active and reactive power

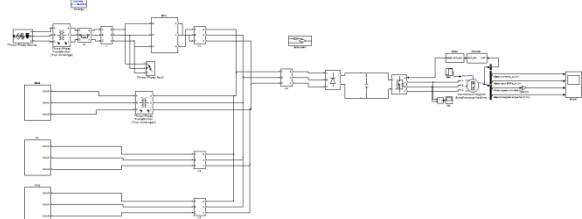


Fig 10 Matlab/simulation circuit of a typical microgrid integrated with the SFCL with PMSM motor drive

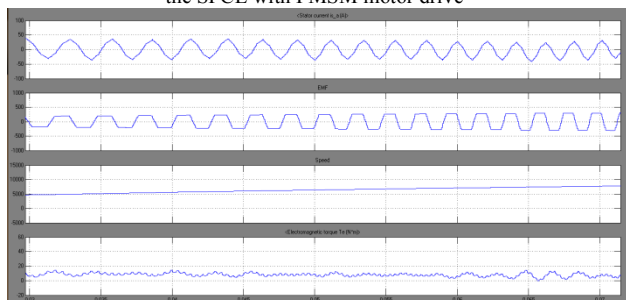


Fig 11 simulation wave form of microgrid integrated with the SFCL with PMSM motor drive current, EMF, speed and torque

#### IV CONCLUSION

The superconducting fault current limiter is a promising device to limit the escalating fault levels caused by the expansion of power grid and integration of renewable. Using SFCL module the transient rise in fault current are suppressed or limit to a desired value using SFCL. SFCL modules have ability to limit the sudden rise in fault current by providing the sufficient value of resistance. For the power frequency overvoltage caused by a single-phase grounded fault, the SFCL can help to reduce the overvoltage's amplitude and avoid damaging the relevant distribution equipment. But from the simulation analysis the FCL can as well suppress the short circuit current induced by a three-phase grounded fault effectively compared to the SFCL, and the power system's safety and reliability can be improved. In recently years, more and more dispersed energy sources, such as wind power and photovoltaic solar power, are installed into distribution systems. Therefore, the study of a coordinated control method for the renewable energy sources and the FCL becomes very meaningful, and it will be performed with

PMSM motor drive to study the characteristics of stator current, EMF, Speed and electromagnetic torque.

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