

# Superconducting Fault Current Limiter For Energy Storage Protection Under Grounded Faults In A Micro Grid

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**Abstract-** In this work, super conducting Fault Current Limiter is proposed to protect the energy storage system in a micro grid. Distributed Generation (DG) in the form of Renewable Power Generation systems is currently preferred for clean power generation. However there unpredictable nature of generation an energy storage system is integrated to the grid. Energy storage needs to be used to ensure that the load is met at all times. Energy storage systems (ESSs) are enabling technologies for well-established and new applications such as power peak shaving, stability purpose, integration of renewable energies. The main objective of this project is to introduce a superconducting fault current limiter to keep the energy storage system from disconnecting from the grid when ground faults occur. The possible advantages of Superconducting Fault Current Limiter (SFCL) as a means to limit the adverse effect of DG on distribution system protection and their effectiveness will be demonstrated. The SFCL has been noticed as the best solution to limit fault current effectively. However, the application of the SFCL in the power system affects the protective coordination of the existing protective devices and can deviate their operation time from the original one. The simulation results are obtained using MATLAB/SIMULINK software.

**Index Terms-** energy Storage, Fault current, interconnecting transformer, Distribution generation, Superconducting Fault Current Limiter.

## 1. INTRODUCTION

Over last few years, Electrical energy requirement has continuously grown and distribution infrastructure has been more complicated. For such reasons micro grid concept is suggested. Due to unpredictable nature of renewable energy sources an energy storage system is integrated to the grid. Energy storage technologies are essential for modern power systems. Although an ESS does not generate energy, its function appears to be vital for the operation and planning of an electrical power system, particularly for the stability, reliability, and power quality of the power output. In addition, the system defers the costs and upgrades of developing the transmission and distribution capacity for satisfying the growing power demand for peak-shaving purposes. The ESS is installed to enhance the dispatching ability of renewable energy sources and to provide ancillary services such as reactive power support for operations. Renewable energy resources for the 21st century power grid in order to supply electric power which is cleaner, reliable, effervescent and responsive than conventional power systems. Smart grid is based on the principle of decentralization of the

power grid network into smaller grids (Microgrid) having distributed generation sources (DG) connected with them. One critical problem due to these integrations is excessive increase in fault current due to the presence of DG within a micro grid [1]. Conventional protection devices installed for protection of excessive fault current in power systems, mostly at the high voltage substation level circuit breakers tripped by over-current protection relay which has a response-time delay resulting in power system to pass initial peaks of fault current [1]. But, SFCL is a novel technology which has the capability to quench fault currents instantly as soon as fault current exceeds SFCL's current limiting threshold level [2].

SFCL achieves this function by losing its superconductivity and generating impedance in the circuit. SFCL does not only suppress the amplitudes of fault currents but also enhance the transient stability of power system [2]. Up to now, there were some research activities discussing the fault current issues of smart grid [4]. But the applicability of SFCLs into micro grids was not found yet. Hence, in order to solve the problem of increasing fault current in power systems having multiple micro grids by using SFCL technology is the main concern of this work. The utilization of SFCL in power system provide them most effective way to limit the fault current and results in considerable saving from not having to utilize high capacity circuit breakers. With Superconducting fault current limiters (SFCLs) utilize superconducting materials to limit the current directly or to supply a DC bias current that affects the level of magnetization of a saturable iron core. Being many SFCL design concepts are being evaluated for commercial expectations, improvements in superconducting materials over the last 20 years have driven the technology [3].

Whenever ground faults occur, the ground fault current trips a ground over current relay on four wires, multigrounded neutral distribution systems. Due to this it can disconnect the energy storage from the grid. To reduce the fault current a SFCL is used in the appropriated location in the distribution system.

## II. MODELING OF AN SFCL AND MICROGRID WITH AN ENERGY STORAGE SYSTEM

### A. Resistive SFCL Model

An SFCL is one of the most promising current limiters to prevent the short-circuit current from increasing in magnitude owing to its rapid current limiting ability. Many models for an SFCL have been developed, such as resistive type, reactive type, transformer type, and hybrid type SFCLs [10], [13]. Among the various types of SFCLs, the resistive type SFCL is preferred because of its simple principle and compact structure of small size [11]–[13]. In this paper, we have modeled a resistive type SFCL using mathematical expressive equations

The time evolution of the SFCL impedance  $RSFCL$  as a function of time  $t$  is given by (1)–(3):

$$R_{SFCL}(t) = R_n [1 - \exp(-\frac{t-t_0}{\tau_f})]^{\frac{1}{2}}; t_0 \leq t < t_1 \quad (1)$$

$$R_{sfcl}(t) = a_1(t - t_1) + b_1 \quad (2)$$

$$R_{sfcl}(t) = a_2(t - t_2) + b_2 \quad (3)$$

Where  $R_{nandTF}$  are the convergence resistance and time constant, respectively.  $t_0$ ,  $t_1$ , and  $t_2$  denote the quench- starting time, first starting time of recovery, and second starting time of recovery, respectively. In addition,  $a_1$ ,  $a_2$ ,  $b_1$ , and  $b_2$  are the coefficients of the first-order linear function denoting the experimental results for the recovery characteristics of an SFCL. The parameter values are listed in Table I.

TABLE I SFCL MODELING PARAMETERS

SFCL	$R_n$ [ $\Omega$ ]	$T_f$	$a_1$	$a_2$	$b_1$	$b_2$
Value	8	0.01	-20	-50	5	3

### B. Configuration of the Distribution System with an ESS

A grid-scale ESS consists of a battery bank, control system, power electronics interface for ac-dc power conversion, protective circuitry, and a transformer to convert the ESS output to the transmission or distribution system voltage level. Fig. 1 shows a four-wire multi-grounded power distribution system with an ESS for simulation to analyze the effect of the SFCL application.

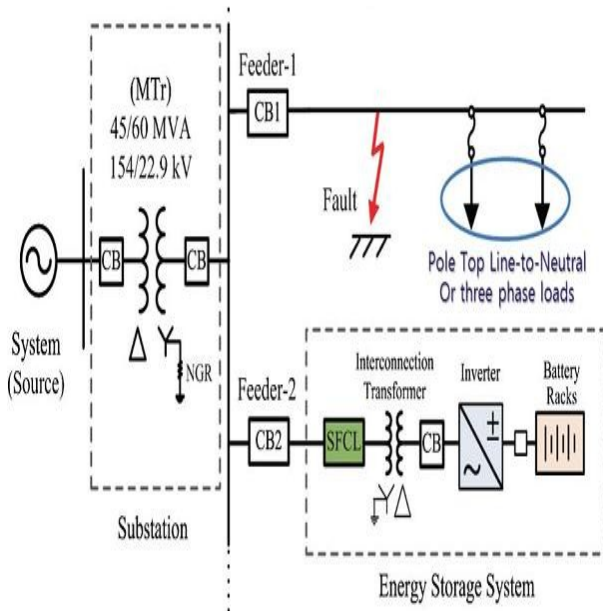


Fig.1. Power distribution system with an energy storage system with SFCL

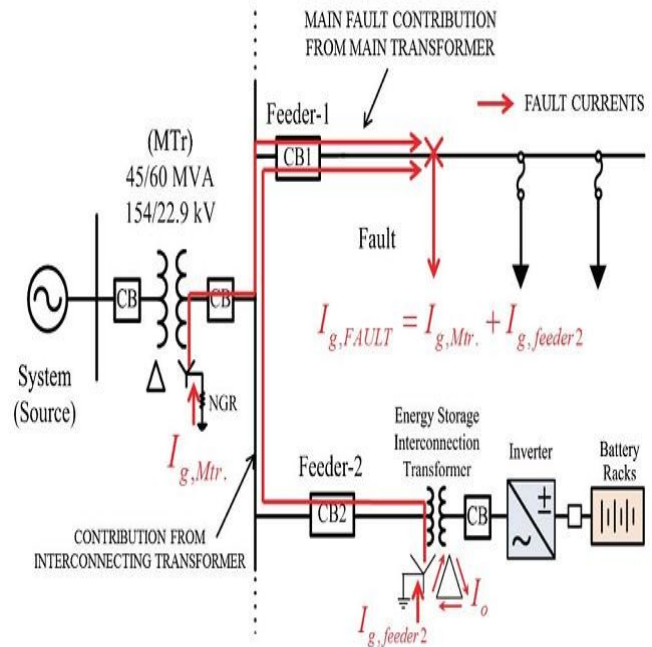


Fig.2. Effect of a single line-to-ground fault on an ESS interconnecting transformer without an SFCL

TABLE II

SPECIFICATIONS OF THE POWER DISTRIBUTION SYSTEM WITH AN ESS

	Data
Source	154kV, 1.75%
Main Transformer (MTr)	154/22.9kV, 60 MVA, j15%, NGR: 5%
Interconnecting Transformer	22.9/0.44kV, 20 MVA, j6%
Line Impedance (ACSR 160mm <sup>2</sup> )	$Z_1 = 3.86 + j7.42\% \text{ km}$ $Z_0 = 9.87 + j22.68\% \text{ km}$
Feeder Length	Feeder-1: 10km Feeder-2: 100 m
Fault Location	Feeder-1: 5 km from CB1
System Base	100 MVA, 22.9 kV

The nominal voltage of this secondary system is 22.9 kV. There is a substation, 6–9 feeders, single- or three phase loads, and an ESS interconnecting transformer, as well as protection devices such as a circuit breaker (CB) and relay. There are two circuit breakers to clear the feeder faults through the operation of the relay. CB1 and CB2 are installed on feeder-1 and feeder-2, which is

interconnected with the ESS. The rated power of the ESS is assumed to be below 20 MW based on a Korea electric power corporation (KEPCO) guideline for a DG and corresponding generations interconnected with a distribution system [18]. In order to integrate with the ESS, a grounded wye (utility side)-delta (ESS side) connection transformer is used in the analysis.

### III. DESCRIPTION OFFAULTS

Electrical powers system is growing in size and complexity in all sectors such as generation, transmission, distribution and load systems. Types of faults like short circuit condition in power system network results in severe economic losses and reduces the reliability of the electrical system. Electrical fault is an abnormal condition, caused by equipment failures such as transformers and rotating machines, human errors and environmental conditions. These faults cause interruption to electric flows, equipment damages and even cause death of humans, birds and animals.

#### Types of Faults

Electrical fault is the deviation of voltages and currents from nominal values or states. Under normal operating conditions, power system equipment or lines carry normal voltages and currents which results in a safer operation of the system. But when fault occurs, it causes excessively high currents to flow which causes the damage to equipments and devices. Fault detection and analysis is necessary to select or design suitable switchgear equipments, electromechanical relays, circuit breakers and other protection devices. There are mainly two types of faults in the electrical power system. Those are symmetrical and unsymmetrical faults

#### 1. Symmetrical faults

These are very severe faults and occur infrequently in the power systems. These are also called as balanced faults and are of two types namely line to line to ground (L-L-L-G) and line to line to line (L-L-L).

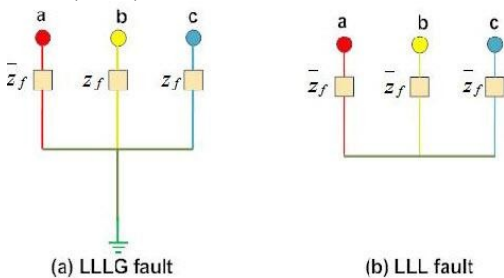


Fig-3:symmetrical faults

Only 2-5 percent of system faults are symmetrical faults. If these faults occur, system remains balanced but results in severe damage to the electrical power system equipments. Above figure shows two types of three phase symmetrical faults. Analysis of these fault is easy and usually carried byper phase basis. Three phase fault analysis or information is required for selecting set-phase relays, rupturing capacity of the circuit breakers and rating of the protective switchgear.

#### 2. Unsymmetrical faults

These are very common and less severe than symmetrical faults. There are mainly three types namely line to ground (L-G), line to line (L-L) and double line to ground (LL-G) faults.

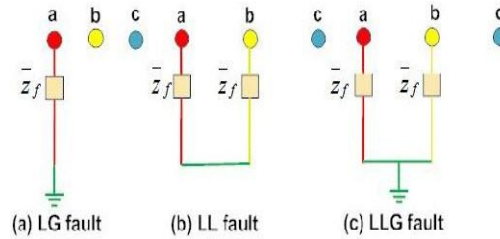


Fig.4. Unsymmetrical faults

Line to ground fault (L-G) is most common fault and 65-70 percent of faults are of this type.

It causes the conductor to make contact with earth or ground. 15 to 20 percent of faults are double line to ground and causes the two conductors to make contact with ground. Line to line faults occur when two conductors make contact with each other mainly while swinging of lines due to winds and 5- 10 percent of the faults are of this type. These are also called unbalanced faults since their occurrence causes unbalance in the system. Unbalance of the system means that that impedance values are different in each phase causing unbalance current to flow in the phases. These are more difficult to analyze and are carried by per phase basis similar to three phase balanced faults.

Fault currents cause equipment damage due to both thermal and mechanical processes. Goal of fault analysis is to determine the magnitudes of the currents present during the fault. need to determine the maximum current to ensure devices can survive the fault, need to determine the maximum current the circuit breakers (CBs) need to interrupt to correctly size the CBs.

### IV. BASICS OF SFCL

Superconducting fault current limiter is a promising technique to limit fault current in power system. Normally non-linear characteristic of superconductor is used in SFCL to limit fault current. In a normal operating condition SFCL has no influence on the system due to the virtually zero resistance below its critical current in superconductors. But when system goes to abnormal condition due to the occurrence of a fault, current exceeds the critical value of superconductors resulting in the SFCL to go resistive state. This capability of SFCL to go off a finite resistive value state from zero resistance can be used to limit fault current. Different types of SFCLs have been developed until now[10-13]. Many models for SFCL have been designed as resistor-type, reactor-type, and transformer-type etc. In this paper a resistive-type SFCL is modeled using simulink. Quench and recovery characteristics are designed on the basis of [14].

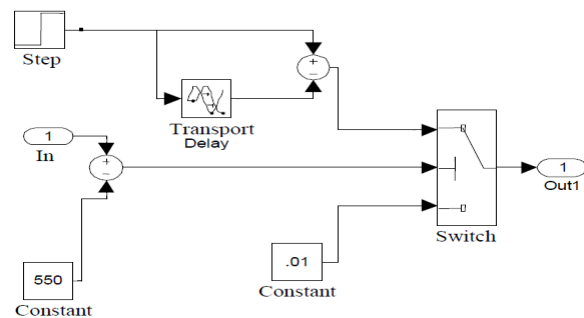


Fig.5. Implementation of resistive SFCL characteristics in simulink.

These parameters are used for implementing resistive SFCL characteristic is shown in Fig. 2. Quenching and recovery time of SFCL are specified using step and transport block respectively. A Switch block is used to give minimum or maximum impedance in output which is determined considering the incoming current. The simulation model of SFCL for a single phase system is shown in Fig. 3. The working principle of the SFCL model developed in Simulink/Sim Power system is described below. Firstly, RMS value of incoming current (passing through current measurement block) is measured by RMS block. Then it compares the current with the specified current in the SFCL

Subsystem. SFCL gives minimum resistance, if the incoming current is less than the triggering current level. But if the current is larger than the triggering current, SFCL's impedance rises to maximum state. It ultimately raises the total impedance of the system which results in limiting the fault current. Finally, the SFCL's resistance will be minimum when the limited fault current is below the triggering value.

## SIMULATION RESULTS

### Case 1: Single Line to Ground Fault without an SFCL

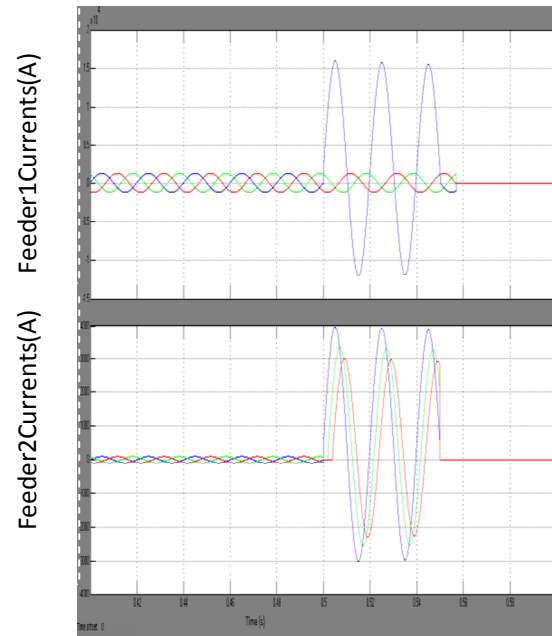
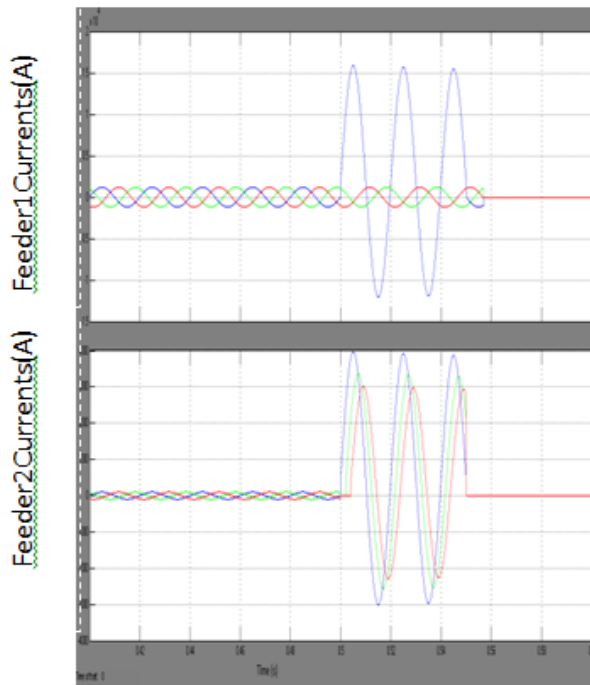


Fig.6. Simulation waveform Feeder currents caused by the single line-to ground fault without SFCL

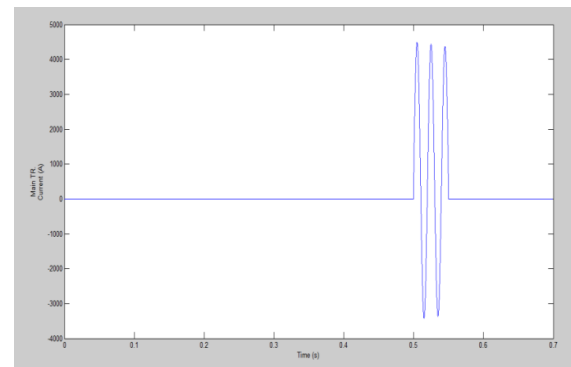


Fig.7. Zero-sequence currents due to a single line-to-ground fault for the main transformer

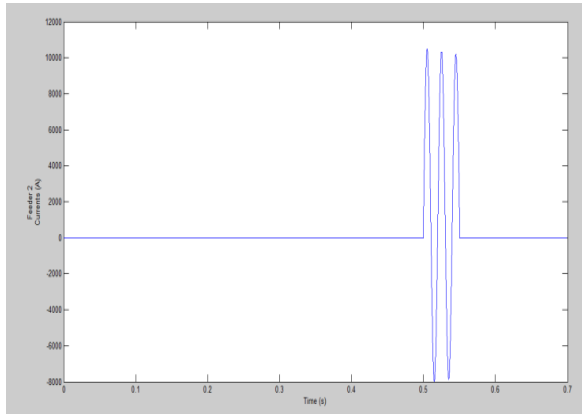


Fig.8. Zero-sequence currents due to a single line-to-ground fault for the interconnecting transformer.

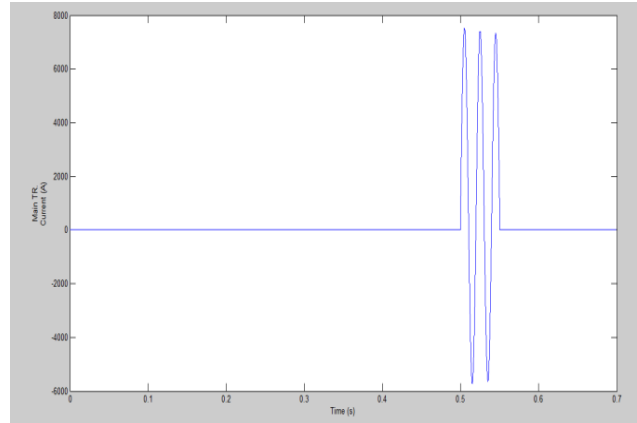


Fig.10. Zero-sequence currents due to a single line-to-ground fault for the main transformer

**Case 2: Single Line to Ground Fault With SFCL**

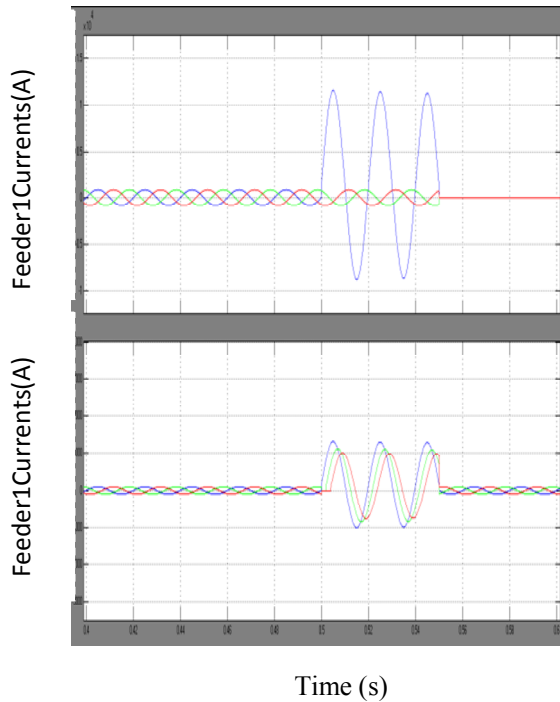


Fig.9. Simulation waveform Feeder currents caused by the single line-to-ground fault with SFCL

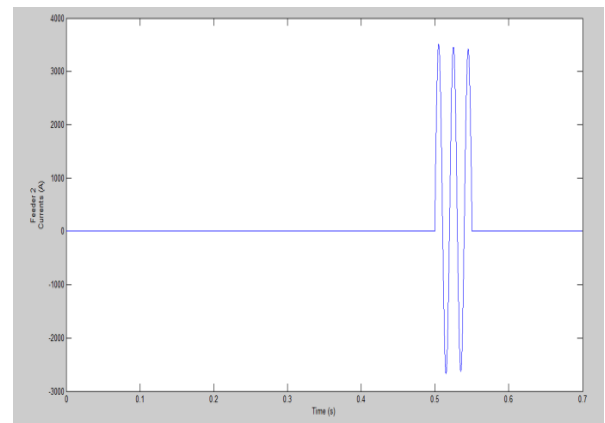


Fig.11. Zero-sequence currents due to a single line-to-ground fault for the interconnecting transformer

**Case 3: Double Line to Ground Fault With SFCL**

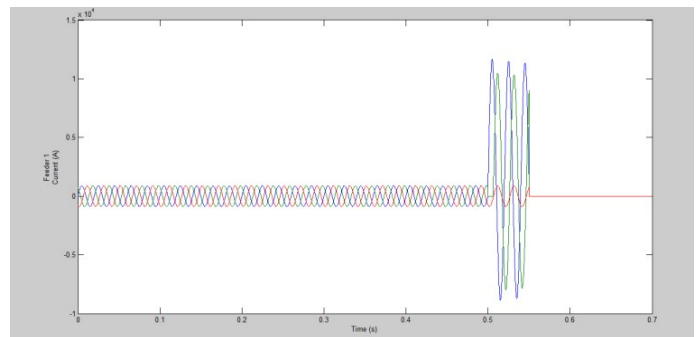
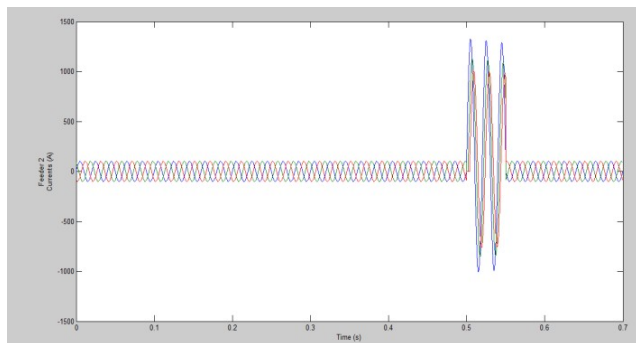
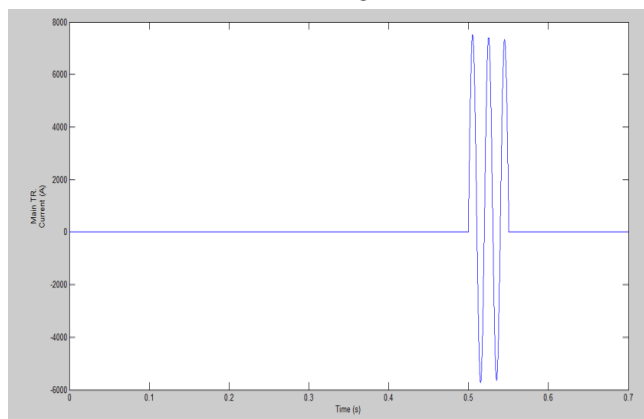


Fig.12. Simulation waveform Feeder 1 currents caused by the double line-to-ground fault.

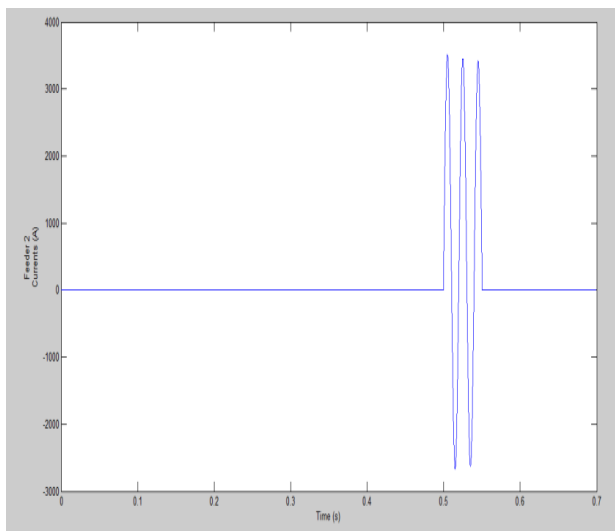




**Fig.13.** Simulation waveform Feeder 2 current caused by the double line-to-ground fault

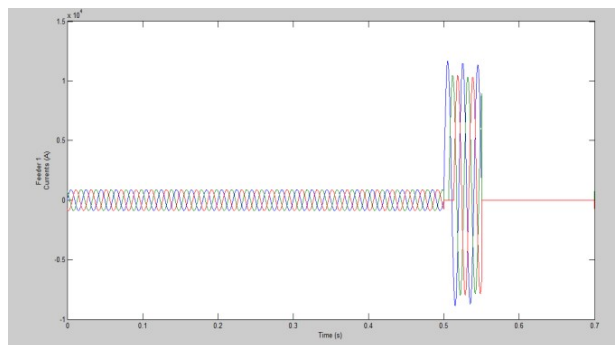


**Fig.14.** Zero-sequence currents due to a double line-to-ground fault for the main transformer

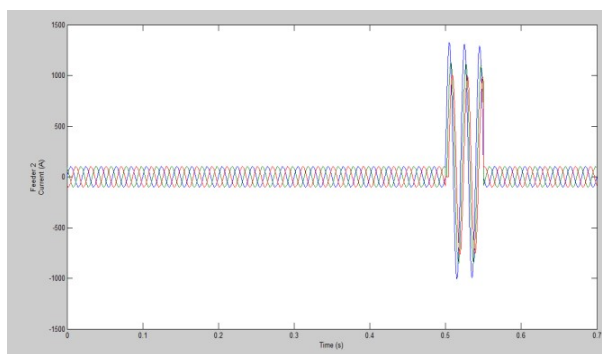


**Fig.15.** Zero-sequence currents due to a double line-to-ground fault for the interconnecting transformer

**Case 4: Triple Line to Ground Fault with SFCL**



**Fig.16.** Simulation waveform Feeder 1 currents caused by the three line to ground fault



**Fig.17.** Simulation waveform Feeder 2 currents caused by the three line to ground fault

**V. CONCLUSION**

Due to the growing concern with energy shortage and network stability, the concepts of distributed generation (DG), microgrid systems become progressively more popular; especially with the decreasing costs of various clean renewable energy sources (RES). These DG systems would be connected to the utility grid under normal operating conditions, but also have the additional capability to sustain a local system by sourcing power directly from the renewable energy sources and energy storage devices if necessary to make grid transmission level black- and brownouts seem transparent to the local system loads. The interconnecting transformer interfaced with an existing power system provides a new zero-sequence current path that is the cause of interruption between the power system and the ESS. The application of the SFCL to the interconnecting transformer solves the problem regarding protective coordination for an ESS.

Therefore, an SFCL applied to the interconnecting transformer is used to improve the interconnection for a power system with energy storage by limiting the fault current of the two cases, we confirmed that the application of an SFCL to the interconnection transformer is an effective solution for sustainable interconnection.

## REFERENCES

- [1] G. Coppez, S. Chowdhury, and S. P. Chowdhury, "Impacts of energy storage in distributed power generation: A review," in *Proc. Int. Conf. Power Syst. Technol.*, Oct. 2010, pp. 1–7.
- [2] A. Oudalov, T. Buehler, and D. Chartouni, "Utility scale applications of energy storage," in *Proc. IEEE ENERGY Conf.*, Atlanta, GA, Nov. 17–19, 2008, pp. 1–7.
- [3] J. A. Carr, J. C. Balda, and J. A. Mantooth, "A survey of systems to integrate distributed energy resources and energy storage on the utility grid," in *Proc. IEEE ENERGY Conf.*, Atlanta, GA, Nov. 17–19, 2008, pp. 1–7.
- [4] A. Mohd, E. Ortjohann, A. Schmelter, N. Hamsic, and D. Morton, "Challenges in integrating distributed energy storage systems into future smart grid," in *Proc. IEEE ISIE*, Cambridge, U.K., Jun. 30–Jul. 2 2008, pp. 1627–1632.
- [5] S. Vazquez, S. M. Lukic, E. Galvan, L. G. Franquelo, and J. M. Carrasco, "Energy storage systems for transport and grid applications," *IEEE Trans. Ind. Electron.*, vol. 57, no. 12, pp. 3881–3895, Dec. 2010.
- [6] H. Qian, J. Zhang, J.-S. Lai, and W. Yu, "A high-efficiency grid-tie battery energy storage system," *IEEE Trans. Power Electron.*, vol. 26, no. 3, pp. 886–896, Mar. 2011.
- [7] W. Shi, J. Jiang, S. Li, S. Lin, P. Lin, and F. Wen, "Applications of battery energy storage system (BESS) for energy conversion base in Expo 2010," in *Proc. 2nd IEEE Symp. Power Electron. Distrib. Gen. Syst.*, 2010, pp. 918–923.
- [8] R. C. Dugan, M. F. McGranaghan, S. Santoso, and H. W. Beaty, *Electrical Power Systems Quality*, 2nd ed. New York: McGraw-Hill, 2002, pp. 373–435.
- [9] H. Saadat, *Power System Analysis*, 2nd ed. New York: McGraw-Hill, 2004, pp. 411–413.
- [10] J.-S. Kim, S.-H. Lim, and J.-C. Kim, "Comparative analysis on current limiting characteristics of Hybrid superconducting fault current limiters (SFCLs) with first half cycle limiting and non-limiting operations," *J. Elect. Eng. Technol.*, vol. 7, no. 5, pp. 659–663, Sep. 2012.
- [11] B. Gromoll, G. Ries, W. Schmidt, H.-P. Kraemer, B. Seebacher, B. Utz, R. Nies, H.-W. Newmuller, E. Baltzer, and S. Fischer, "Resistive fault current limiters with YBCO films-100 kVA functional model," *IEEE Trans. Appl. Supercond.*, vol. 9, no. 2, pp. 656–659, Jun. 1999.
- [12] S.-H. Lim, J.-S. Kim, J.-F. Moon, J.-C. Kim, S.-B. Rhee, C.-H. Kim, and O.-B. Hyun, "Quench and recovery characteristics of a SFCL applied into neutral line of a three-phase power system," *IEEE Trans. Appl. Supercond.*, vol. 19, no. 3, pp. 1835–1838, Jun. 2009.
- [13] J.-F. Moon, S.-H. Lim, J.-C. Kim, and S.-Y. Yun, "Assessment of the impact of SFCL on voltage sags in power distribution system," *IEEE Trans. Appl. Supercond.*, vol. 21, no. 3, pp. 2161–2164, Jun. 2011.
- [14] J.-S. Kim, S.-H. Lim, and J.-C. Kim, "Study on protective coordination for application of superconducting fault current limiter," *IEEE Trans. Appl. Supercond.*, vol. 21, no. 3, pp. 2174–2178, Jun. 2011.
- [15] S.-M. Cho, H.-S. Shin, and J.-C. Kim, "Study on coordination of protective relays between primary feeder and interconnecting transformer grounded by SFCL of wind farm," *IEEE Trans. Appl. Supercond.*, vol. 22, no. 3, p. 5500504, Jun. 2012.

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