

Analysis of Distributed Generators with Fuzzy logic Controller Based Inverter at un-balanced loads

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Abstract-The design of a fuzzy logic controller using the voltage as feedback for significantly improving the dynamic performance of converter. Renewable energy resources (RES) are being increasingly connected in distribution systems utilizing power electronic converters. This paper presents a new control strategy for achieving maximum benefits from these grid-interfacing inverters using the closed loop P-I controller and fuzzy logic controller, when installed in 3-phase 4-wire distribution systems. The inverter is controlled to perform as a multi-function device by incorporating active power filter functionality. The inverter can thus be utilized as: 1) power converter to inject power generated from RES to the grid, and 2) shunt APF to compensate current unbalance, load current harmonics, load reactive power demand and load neutral current. All of these functions may be accomplished either individually or simultaneously. The THD and power factor variations for both PI-controller and fuzzy logic controller are also shown. This new control concept is demonstrated with extensive MATLAB/Simulink .Finally the proposed scheme is applied for both balanced, unbalanced linear and non linear loads. The proposed concept can be implemented to Fuzzy logic controller by using Matlab/Simulink software.

Key words-Distributed generation, 3-leg inverter, 4-leg inverter, unbalanced load.

I. introduction

In recent years, eco-friendly distributed generation systems (DGS) such as wind turbines, solar cells, and fuel cells are dramatically growing because they can fulfill the increasing demand of electric power due to the rapid growth of the economy and strict environmental regulations regarding greenhouse gas emissions. Generally, the DGSs are interconnected in parallel with the electric utility grid and provide maximum electric power to the grid. However, there are some areas (e.g., remote islands or villages) where the connection to the grid is expensive or impractical and then small scaled standalone DGSs are the only efficient and economical options. In such DGSs, depending on consumers "power demand, there are situations where some DGSs operate in parallel or independently. In either case, a stable operation of each DGS unit is as important as the stability of the parallel operating DGSs in which the proper load sharing of each unit is one of main research issues since the voltage controller is commonly used in a single DGS unit or multiple DGS units. For this reason, the voltage

controller design for a single DGS unit, which can guarantee a good voltage regulation under unbalanced and nonlinear loads, is an interesting topic in the field of the DGSs control.

For the purpose of improving the quality of inverter output voltage, many researchers are working on designing the controllers for dc-ac power converters. In a robust controller is developed for balanced and unbalanced systems, which considers the uncertainties of the load parameters. However, nonlinear load is not fully addressed. In a repetitive control is used to regulate the UPS inverters. However, the slow response and lack of the systematic method to stabilize the error dynamics with the repetitive control are being the main problems. In an alternative control strategy with a feed forward compensation component can significantly mitigate the effect of load disturbance and make the controller design simple. Nevertheless, the application of this method is mainly limited to balanced load conditions. In a current control technique based on the spatial repetitive control is applied to a single-phase inverter and it also improves the performance of the current controller by estimating the disturbances. Although this control can obtain good results under nonlinear load, it may not guarantee a good voltage tracking capacity for a three-phase system. In a robust servomechanism voltage controller and a discrete-time sliding mode current controller are presented to control a single distributed generation unit in a standalone mode which can operate well under a sudden load change, an unbalanced load, and a nonlinear load.

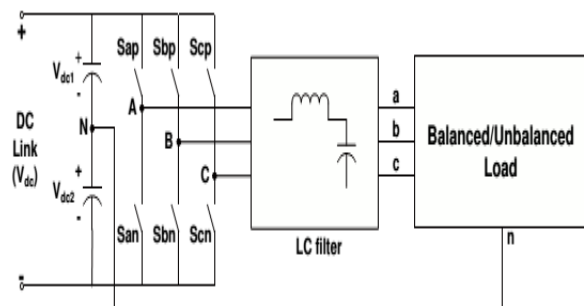


Fig.1: 3-leg inverter with split DC link capacitor.

Nevertheless this is the most popularly used topology for 3-phase, 4-wire DG system. Both the topologies fail while feeding a 3-phase, 4-wire unbalanced load. Hence additional unbalanced load

compensators are usually provided near the unbalanced load end. However with the advent of 4-leg inverters (see Fig.3), it is possible to handle unbalanced loads without affecting terminal voltages [6], [7]. The load neutral can be connected to the center of fourth leg and zero sequence currents flowing through neutral can be controlled.

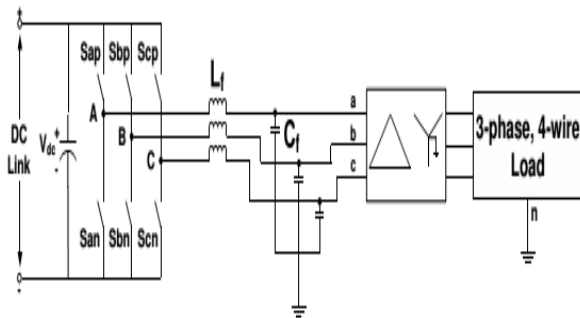


Fig.2: 3-leg inverter interface with -Y transformer.

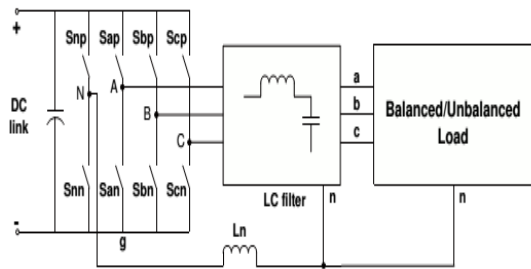


Fig.3: 4-leg inverter.

Advantages of 4-leg inverter interfaces are as follows:

- There are no ripples in DC link voltage when 4-leg inverters cater to unbalanced or nonlinear loads. So there is no need of large DC link capacitors as is the case of 3-leg split DC link topology.
- The 3-dimensional Space Vector Modulation (3-D SVM) technique enhances the utilization of DC link voltage [8].
- Presence of fourth neutral leg eliminates the requirement of bulky transformers for power evacuation (at least in small low voltage stand-alone DG systems).

The 4-leg inverters are widely used as active load compensators [9], [10]. The capability of using 4-leg inverter as an interface for stand-alone micro grid with unbalanced load is discussed in [11]. Its application to handle emergency mode for line interactive system is discussed in [12]. But all the existing DG systems are working with 3-leg inverter interfaces. However while expanding the existing system, the possibility of using 4-leg inverter interface needs to be explored. This idea is addressed in detail in this paper. This is a novel way to mitigate the load unbalance by forming hybrid inverter interfaces instead of local unbalance compensators. The

proposed DG system with 3-leg and 4-leg inverter interfaces is shown in Fig.4.

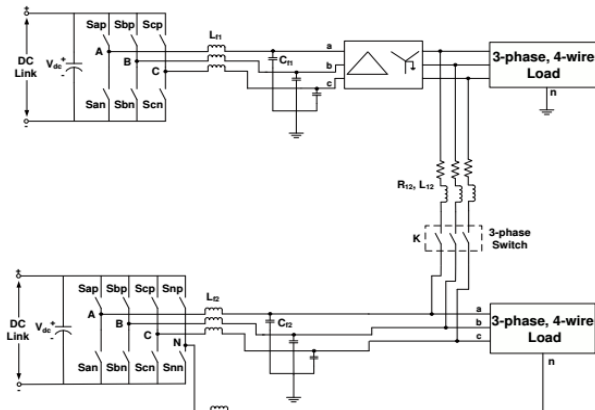


Fig.4: DG system with 3-leg and 4-leg inverter interfaces.

In this paper, we are presenting the work carried out in designing the Fuzzy logic controller for switching operation of inverter. A simple control strategy of inverter is adopted where the measurement. Then the performances of conventional fuzzy logic controller are investigated. Simulation results show that Total Harmonic Distortion in source current is drastically reduced fuzzy controller is included in the inverter control circuit. Simulation work has been done using MATLAB/SIMULINK software.

II. CONTROLLER DESIGN FOR 3-LEG AND 4-LEG INVERTERS

For the 3-leg inverter interface, 2-D SVM scheme [13] is implemented, whereas for the 4-leg inverter, 3-D SVM [8] is used. 3-D SVM is a superset of 2-D SVM technique. In both the SVM modulation techniques, DC link utilization is better as compared to other modulation techniques. Modulation index for 3D-SVM is defined as

$$m = \frac{V_m}{\frac{V_{dc}}{\sqrt{3}}}, \quad (1)$$

Where V_m is peak value of phase voltage and V_{dc} is the DC link voltage. For 230V rms phase voltage, V_m is 325V. With $m = 1$, DC link voltage required is 563V. But to take care of losses, it is maintained at 580V. For closed-loop design of each inverter, terminal voltages of inverters, load currents and capacitor currents are sensed. These are transformed into synchronous reference frame to form a decoupled closed-loop system. It is to be noted here that for 3-leg inverters, there are only q-axis and d-axis controllers (see Fig.5). This is because 3-leg inverters have only two degrees of freedom (DOF). On the other hand, a 4-leg inverter has three DOFs and there are q-axis, d-axis and 0-axis controllers [14] (see Fig.6).

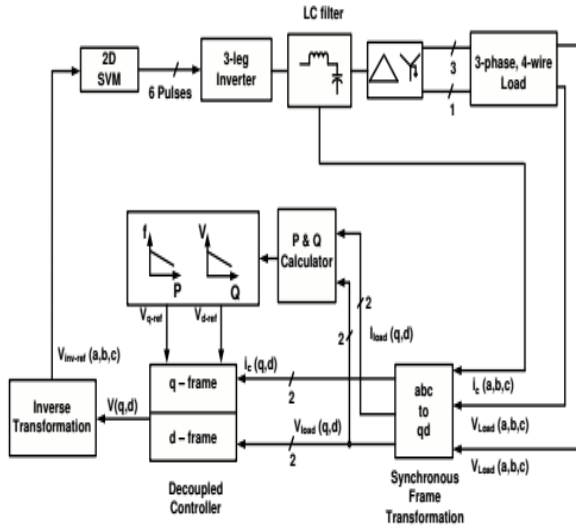


Fig.5: Closed-loop system for 3-leg inverter interfaced DG system.

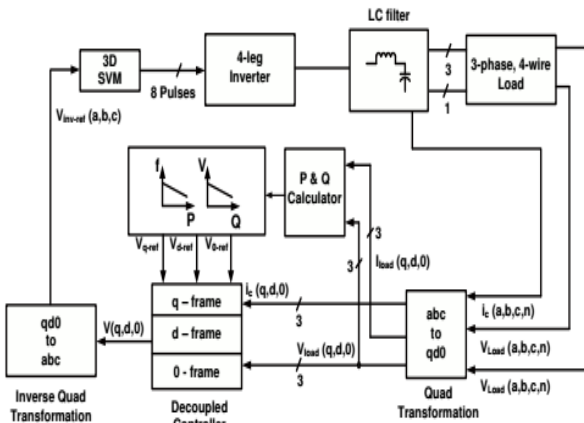


Fig. 6. Closed-loop system for 4-leg inverter interfaced DG system.

For decentralized operation, P-f and Q-V droop laws are applied. Thus there is no communication required between the two DG units. For generating references for inner voltage control loops, frequency and amplitude of voltages is generated by droop laws.

The transformation ratio of Δ-Y transformer used in 3-leg inverter interfaced system is 1:1 for the sake of simplicity. In simulation, renewable energy source of DC link is replaced by 3-phase diode bridge rectifier. All the simulations are carried out using MATLAB Simulink toolbox. Data for two inverters is given.

III. POWER FLOW ANALYSIS IN HYBRID DG SYSTEM

For understanding the power flow in interconnecting line, the single line diagram of DG system with hybrid inverter interfaces is shown in Fig.7. In this section, analysis of power flow in interconnecting

line is carried out as given in [11]. From the above study it is clear that for unbalanced load on

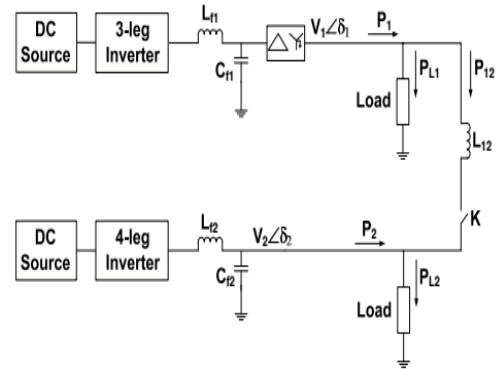


Fig.7: Distributed generation system with hybrid inverter interfaces.

4-leg inverter side, the negative and zero sequence components in terminal voltages of DG units are within acceptable limits specified by the above mentioned standards. The zero sequence currents never contribute to the power flow in interconnecting line [15]. The negative sequence component in terminal voltages is not negligible. Hence for calculating instantaneous power, positive and negative sequence components of currents as well as voltages are considered [11]. The 3-phase terminal voltages and interconnecting line currents are represented in the form of phasors as:

$$\mathbf{V} = V_{qd}^p e^{j\omega t} + V_{qd}^n e^{-j\omega t}, \quad (2)$$

$$\mathbf{I} = I_{qd}^p e^{j\omega t} + I_{qd}^n e^{-j\omega t}. \quad (3)$$

Here superscripts p and n denote respectively the positive and negative sequence component. The coefficients for all sequence components like V_{qd}^p , I_{qd}^p , etc., are vectors. In generalized way, they are represented as:

$$f_{qd} = f_q + j f_d \quad (4)$$

Instantaneous power in the interconnecting line is given as:

$$S = \left(V_{qd}^p e^{j\omega_1 t} + V_{qd}^n e^{-j\omega t} \right) \left(I_{qd}^p e^{j\omega_1 t} + I_{qd}^n e^{-j\omega_1 t} \right)^* \quad (5)$$

From this, the active and reactive power flow in the interconnecting line can be calculated as:

$$\begin{aligned}
 P_{12}(t) &= P_{01} + P_{02} + (P_{c1} + P_{c2}) \cos 2\omega t \\
 &+ (P_{s1} + P_{s2}) \sin 2\omega t, \\
 Q_{12}(t) &= Q_{01} + Q_{02} + (Q_{c1} + Q_{c2}) \cos 2\omega t \\
 &+ (Q_{s1} + Q_{s2}) \sin 2\omega t,
 \end{aligned}
 \tag{6}$$

Where

$$\begin{aligned}
 P_{01} &= \frac{3}{2}(V_{qp}I_{qp} + V_{dp}I_{dp}), & P_{02} &= \frac{3}{2}(V_{qn}I_{qn} + V_{dn}I_{dn}), \\
 P_{c1} &= \frac{3}{2}(V_{dp}I_{dn} + V_{qp}I_{qn}), & P_{c2} &= \frac{3}{2}(V_{dn}I_{dp} + V_{qn}I_{qp}), \\
 P_{s1} &= \frac{3}{2}(V_{qp}I_{dn} - V_{dp}I_{qn}), & P_{s2} &= \frac{3}{2}(V_{dn}I_{qp} - V_{qn}I_{dp}), \\
 Q_{01} &= \frac{3}{2}(V_{dp}I_{qp} - V_{qp}I_{dp}), & Q_{02} &= \frac{3}{2}(V_{dn}I_{qn} - V_{qn}I_{dn}), \\
 Q_{c1} &= \frac{3}{2}(V_{dp}I_{qn} - V_{qp}I_{qn}), & Q_{c2} &= \frac{3}{2}(V_{dn}I_{qp} - V_{qn}I_{dp}), \\
 Q_{s1} &= \frac{3}{2}(V_{qp}I_{qn} + V_{dp}I_{dn}), & Q_{s2} &= -\frac{3}{2}(V_{qn}I_{qp} + V_{dn}I_{dp}).
 \end{aligned}$$

The dc components P01 and P02 contribute to the average power flow. In case of DG system formed using two 4-leginverters as given in [11], it is shown that and $\delta_{12} = \delta_1 - \delta_2$ is the load angle between two units.

$$P_{01}(t) = B_{12} \sin \delta_{12}, \quad \text{with } B_{12} = \frac{3V_1V_2}{\omega_0 L_{12}} \tag{7}$$

It is further shown in [11] that due to both 4-leg inverter interfaces, terminal voltages of DG units are almost balanced and average power P01 is due to positive sequence components of voltages and currents. Hence the power sharing between two units is as per droop laws. However in the proposed DG system with hybrid inverter interfaces, the average power is also due to negative sequence components of voltages and currents (P02) and this component does not allow P1 and P2 to follow exact sharing in proportion to the droop coefficients. This justifies the slight difference in power sharing after load unbalance. Also 2ω oscillations in the power are due to the negative sequence components. This oscillating power shared by each inverter depends on the network parameters and output impedance of inverters.

In the present study only active power flow in hybrid system is studied and hence only resistive loads are considered. But Q-V droop laws can be applied for reactive power sharing. There active power flow will also show 2ω oscillations as derived in (7).

IV INTRODUCTION TO FUZZY LOGIC CONTROLLER

L. A. Zadeh presented the first paper on fuzzy set theory in 1965. Since then, a new language was developed to describe the fuzzy properties of reality, which are very difficult and sometime even impossible to be described using conventional methods. Fuzzy set theory has been widely used in the control area with some application to dc-to-dc converter system. A simple fuzzy logic control is built up by a group of rules based on the human knowledge of system behavior. Matlab/Simulink simulation model is built to study the dynamic behavior of dc-to-dc converter and performance of proposed controllers. Furthermore, design of fuzzy logic controller can provide desirable both small signal and large signal dynamic performance at same time, which is not possible with linear control technique. Thus, fuzzy logic controller has been potential ability to improve the robustness of dc-to-dc converters. The basic scheme of a fuzzy logic controller is shown in Fig 5 and consists of four principal components such as: a fuzzification interface, which converts input data into suitable linguistic values; a knowledge base, which consists of a data base with the necessary linguistic definitions and the control rule set; a decision-making logic which, simulating a human decision process, infer the fuzzy control action from the knowledge of the control rules and linguistic variable definitions; a de-fuzzification interface which yields non fuzzy control action from an inferred fuzzy control action [10].

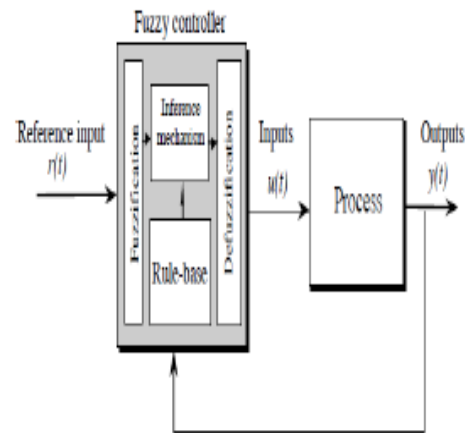


Fig.8. General Structure of the fuzzy logic controller on closed-loop system

The fuzzy control systems are based on expert knowledge that converts the human linguistic concepts into an automatic control strategy without any complicated mathematical model [10]. Simulation is performed in buck converter to verify the proposed fuzzy logic controllers.

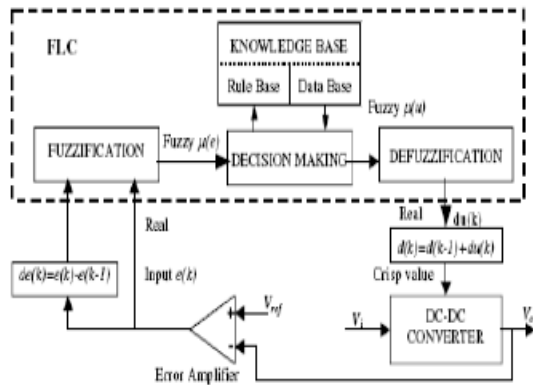
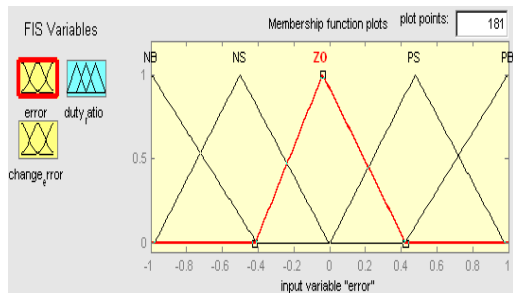


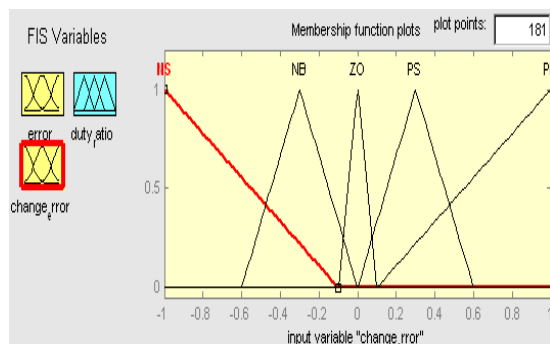
Fig.9. Block diagram of the Fuzzy Logic Controller (FLC) for dc-dc converters

A. Fuzzy Logic Membership Functions:

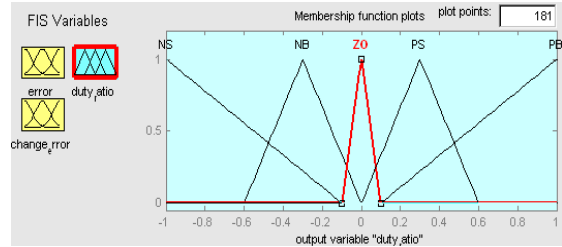
The dc-dc converter is a nonlinear function of the duty cycle because of the small signal model and its control method was applied to the control of boost converters. Fuzzy controllers do not require an exact mathematical model. Instead, they are designed based on general knowledge of the plant. Fuzzy controllers are designed to adapt to varying operating points. Fuzzy Logic Controller is designed to control the output of boost dc-dc converter using Mamdani style fuzzy inference system. Two input variables, error (e) and change of error (de) are used in this fuzzy logic system. The single output variable (u) is duty cycle of PWM output.



The Membership Function plots of error



. The Membership Function plots of change error



the Membership Function plots of duty ratio

B. Fuzzy Logic Rules:

The objective of this dissertation is to control the output voltage of the boost converter. The error and change of error of the output voltage will be the inputs of fuzzy logic controller. These 2 inputs are divided into five groups; NB: Negative Big, NS: Negative Small, ZO: Zero Area, PS: Positive small and PB: Positive Big and its parameter [10]. These fuzzy control rules for error and change of error can be referred in the table that is shown in Table II as per below:

Table II
Table rules for error and change of error

(de) \ (e)	NB	NS	ZO	PS	PB
NB	NB	NB	NB	NS	ZO
NS	NB	NB	NS	ZO	PS
ZO	NB	NS	ZO	PS	PB
PS	NS	ZO	PS	PB	PB
PB	ZO	PS	PB	PB	PB

V. SIMULATION RESULTS

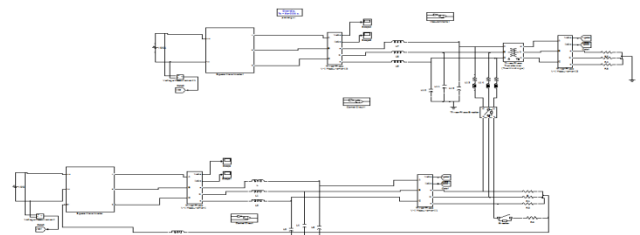


Fig 10 Matlab/simulation circuit of DG system with 3-leg and 4-leg inverter interfaces with fuzzy logic controller

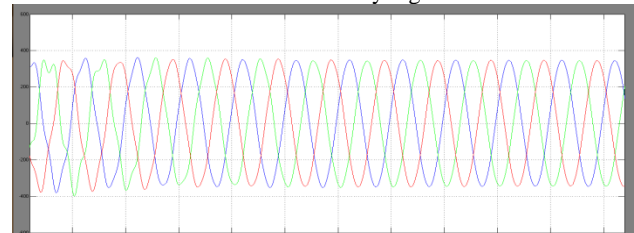


Fig 11 simulation wave form of Terminal voltages of stand-alone DG unit with 3-leg and 4-leg inverter interface with unbalance load

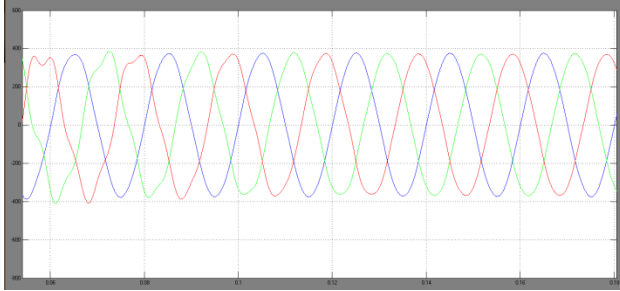


Fig 12 simulation wave form of Terminal voltages of stand-alone DG unit with 4-leg inverter interface with unbalance load

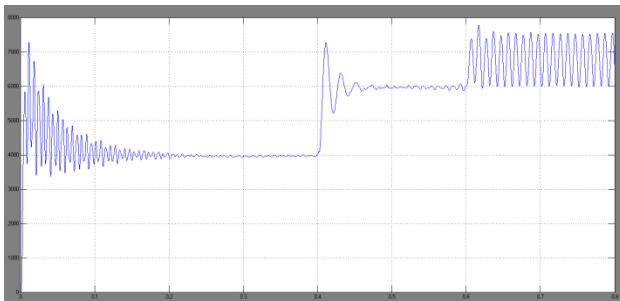
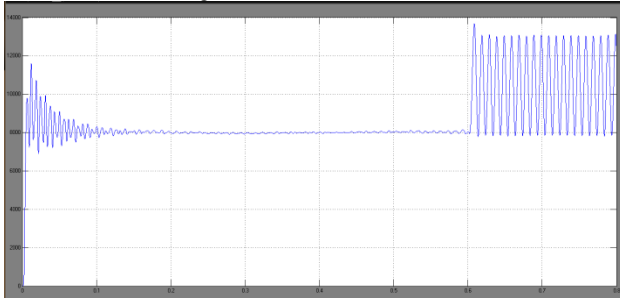


Fig 13 simulation wave form of Power sharing with unbalance on 3-leg inverter side.

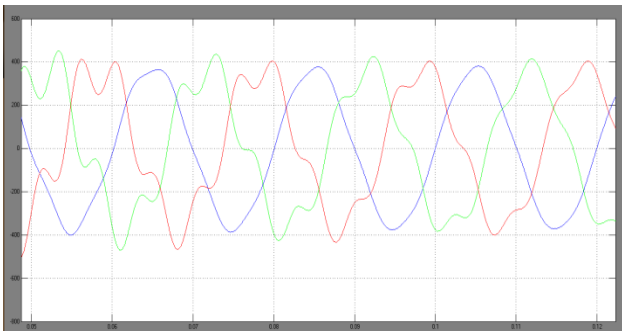
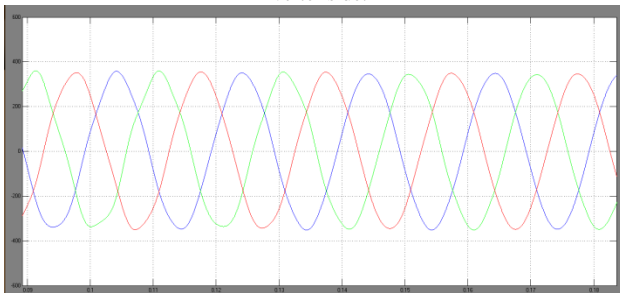


Fig 14 simulation wave form of Terminal voltages of DG units with unbalance on 3-leg inverter side.

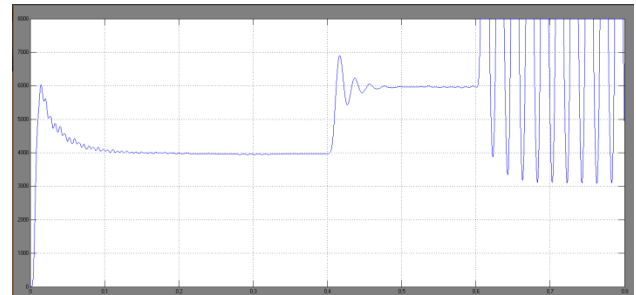
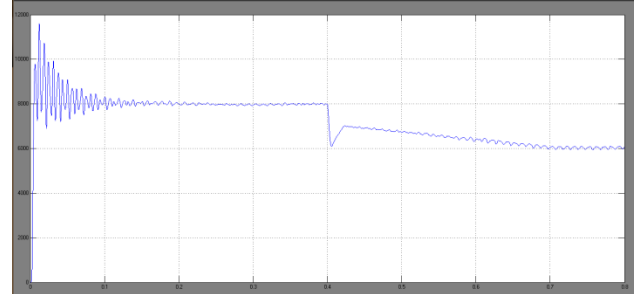


Fig 15 simulation wave form of Power sharing with unbalance on 4-leg inverter side

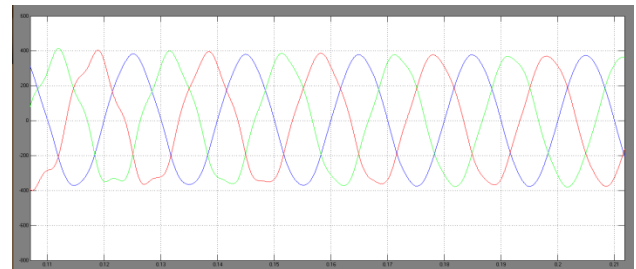
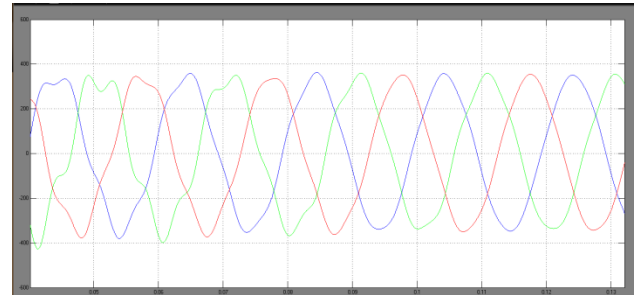
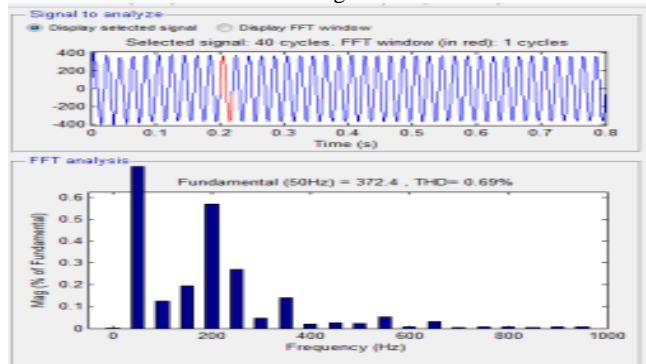


Fig 16 simulation wave form of Terminal voltages of DG units with unbalance on 4-leg inverter side.



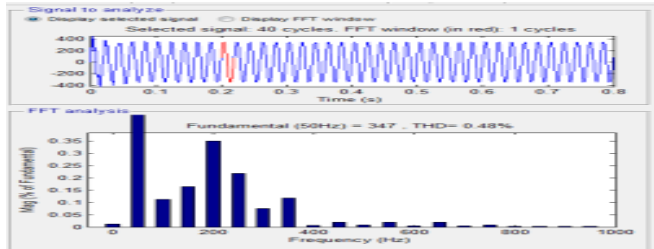


Fig 17 THD analysis of voltage and current in 3-leg and 4-leg inverter operation with fuzzy logic

VI. CONCLUSION

This paper presents a novel method to improve the power quality at point of common coupling for a 3-phase 4-wire DG system using PI controller and fuzzy logic controller for grid interfacing inverter. The grid interfacing inverter is effectively utilized for power conditioning. This approach eliminates the additional power conditioning equipment to improve power quality at PCC. The grid interfacing inverter with the proposed approach can be utilized to inject real power generation from RES to the grid, and operate as a shunt Active Power Filter (APF). The current unbalance, current harmonics and load reactive power, due to unbalanced and non-linear load connected to the PCC, are compensated effectively such that the grid side currents are always maintained as balanced and sinusoidal at unity power factor. Moreover, the load neutral current is prevented from flowing into the grid side by compensating it locally from the fourth leg of inverter. When the power generated from RES is more than the total load power demand, the grid-interfacing inverter with the proposed control approach not only fulfils the total load active and reactive power demand (with harmonic compensation) but also delivers the excess generated sinusoidal active power to the grid at unity power factor. The settling of the system is improved hence proposed fuzzy logic controller has fast response, high accuracy of tracking the DC-voltage reference, and strong robustness to load sudden variations.

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