

# Karnaugh Map

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## Abstract–

*In this paper, we outline the use of karnaugh map in order to simplify logical expressions. The karnaugh (pronounced as “car-no”) map was proposed by Maurice Karnaugh, a tele-communication engineer, at Bell Laboratories in 1953. Realization of expressions having two or three variables is what this paper aims at. Systematic way of representing and labelling a k-map is also discussed in the paper. Further it mentions the properties of k-map which should be used while carrying out sum of minterms i.e., sum of products (SOP) and product of maxterms i.e., product of sum (POS) of a logic expression.*

## 1. Introduction

At present there exist many techniques for minimizing Boolean logic expressions. A easy but very effective method was proposed by Karnaugh (1953). It is well known that the Karnaugh-map (K-map) technique is an elegant teaching resource for academics and a systematic and powerful learning tool for a digital designer in minimizing low order Boolean functions [14]. The reasons for simplifying logic expressions are obvious. By simplifying the logic function we can reduce the original number of digital components (gates) required to implement digital circuits. Therefore, by reducing the number of gates, the chip size and the cost will be reduced and the computing speed

will be increased. However, simpler handling of the Karnaugh map made us choose this technique that by visualizing the error positions in the map, offers remarkable ease in code generation and also understanding.

In this paper a K-map based logic minimization algorithm and its personal digital assistant (PDA) implementation is proposed for simplifying up to four-variable Boolean functions. The algorithm and implementation is found to have excellent results. The proposed PDA application is a useful tool for students and professors [13] in the fields of electrical and computer engineering and computer science. It provides a fast and portable way to check and solve problems in digital logic, discrete mathematics and computer architecture courses.

## 2. Algorithm

The proposed algorithm is based on the looping of redundant terms. In order to take a closer look on how to loop two (create a pair), four (create a quad), or eight (create an octet), [12] in order to get the smallest possible number of groups in a K-map table setting, consider the following simple example. Note that the lower case letter represents the complement value, for example “a” means complement of “A”:

$$F = aBcd + ABcd + ABcD + AbcD + AbCD + AbCd.$$

	cd	cD
ab	0	0
aB	1 <sup>1</sup>	0
AB	1 <sup>2</sup>	1 <sup>2</sup>
Ab	0	1 <sup>2</sup>

Analyzing the above K-map table, the following looping observation can be made for each 1 present in the Table [1]. (Superscript numbers show the pairing possibility of a cell.)

Cell

- aBcd has one possibility to be paired, with ABcd,
- ABcd has two possibilities to be paired, with aBcd and ABcD,
- ABcD has two possibilities to be paired, with ABcd and AbcD,
- AbcD has two possibilities to be paired, with ABcD and AbCD,
- AbCD has two possibilities to be paired, with AbcD and AbCd,
- AbCd has one possibility to be paired, with AbCD.

The basic principle behind the proposed algorithm is the method for choosing the next candidate cell to loop. A classical approach to this problem is to assign different priorities to cells. In the proposed algorithm, cells are assigned priorities based on their looping possibilities [10], intuitively the smaller the looping possibility the higher the looping

PDA-based Boolean Function Simplification: a Useful Educational Tool 331

priority. Back to the previous example, it is obvious that there are two cells that have one possibility to be paired, namely aBcd and AbCd, so we assign them the highest priority. These two cells get paired the first, aBcd gets paired with ABcd and AbCd gets paired with AbCD. After these

pairings, the pairing possibilities of ABcD and AbcD are decremented by one [2], leaving both with one possibility to be paired. Finally they get paired together having an optimal result with three pairs.

$$F = Bcd + AbC + AcD$$

The observation reveals the presence of a consistent rule that lies beneath this logic. Extending the priority from possibility idea [9], the following algorithm is derived for the optimal looping of cells (1's) in a four variable K-map table.

Step 1: Find and loop a possible octet.

Step 2: Find and loop cells that have one possibility to pair.

Step 3: Find and loop cells that have one possibility to quad.

Step 3a: Repeat Step 2 for new cells with one possibility to get paired.

Step 3b: Repeat Step 3 for new cells with one possibility to get quaded.

Step 4: Find and loop cells that have two possibilities to get quaded without sharing.

Step 4b: Repeat Step 4 until no quads found.

Step 5: Find and loop cells that have two possibilities to get quaded with sharing, choose one quad out of two, with the least sharing [8].

Step 6: Find and loop cell that have two possibilities to be paired without sharing.

Step 7: Find and loop cell that have two possibilities to be paired with sharing.

Step 7a: For each success repeat Step 2.

Step 8: If there are cells that have a value of one and are not quaded or paired, then these cells either

- a) Can not be looped or,
- b) Have more than 2 possibilities to get paired or quaded.

Step 8a: If a) is valid then, no further action is needed.

Step 8b: If b) is valid then change the possibilities of one of these cells to 2 and go to Step 3 to repeat the procedure.

Step 8c: Repeat Step 7 until no cells that qualify for this step are found.

It is noted that the looping possibilities of a cell are reduced by one when a looping takes place and this cell can be looped with any of the cells [7] that just formed a pair, quad or octet. It is noted that sharing, in the above algorithm, means that a cell is included in more than one looping.

### 3. Design

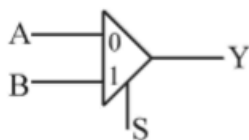
The program was developed using the CodeWarrior For Palm OS 7.0, which supports C, C++ and Java. The implementation of the program was done in C++ using the Object Oriented paradigm. The program is divided into two major classes: a parent class(Kmap), and a child class (KmapElement). Each of these classes represents a logical division of the K-map table [6]. The Kmap object controls everything related to the K-map table as a whole, such as initializing and simplifying. In order to apply these functions, the Kmap object creates 16 “smaller” objects representing each box.

Each “smaller” object (KmapElement) [3] learns its own properties and its methods never change any other object’s properties [11], but they can trigger an event, such as when a looping occurs. Breaking the program down this way is advantageous because the amount of complete, detailed, organized and correct information about the K-map is maximized. The parent object holds 16 children of the class KmapElement and administers the way the simplification methods are called. The child class KmapElement represents one element of the Kmap [5]. This object has all the properties, such as: pairing and quading possibilities, the value of the cell and the looping status of this cell. These objects can learn about other objects of this class through their parent since they have a reference of the parent.

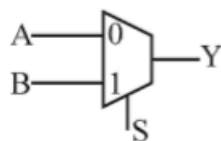
### 4. Example

Consider the truth table for a basic 2-input multiplexer. We can view the truth table as a sort of specification which says what a circuit should do.

### 2-input multiplexer



or



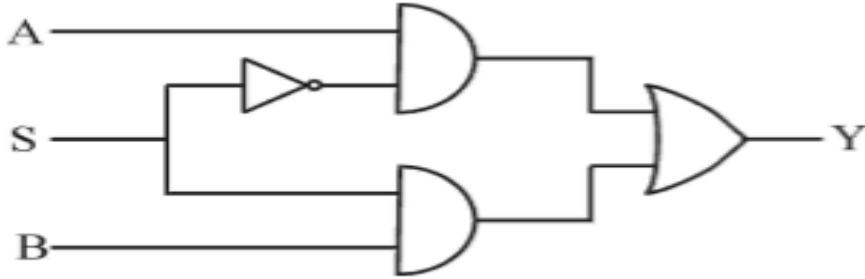
S	A	B	Y
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

We can write this as a standard sum of products (SSoP):

$$Y = S' \cdot A \cdot B' + S' \cdot A \cdot B + S \cdot A' \cdot B + S \cdot A \cdot B = m_2 + m_3 + m_5 + m_7 = E(2, 3, 5, 7)$$

We can reduce this circuit using Boolean algebra (specifically, the distributive axiom and the identity axiom):

$$Y = S' \cdot A \cdot (B' + B) + S \cdot B \cdot (A' + A) = S' \cdot A \cdot (1) + S \cdot B \cdot (1) = S' \cdot A + S \cdot B$$



Consider the following arrangements of cells:

2-input

$a' \cdot b'$ 00	$a' \cdot b$ 01
$a \cdot b'$ 10	$a \cdot b$ 11

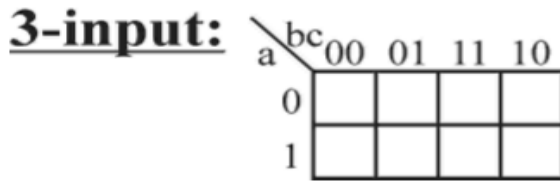
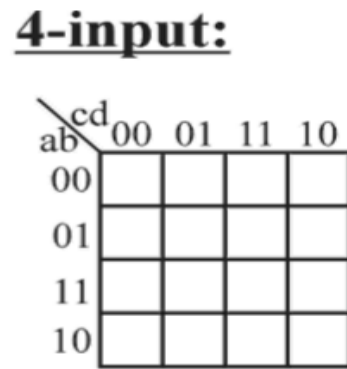
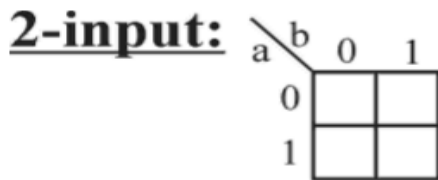
3-input

$a' \cdot b' \cdot c'$ 000	$a' \cdot b' \cdot c$ 001	$a' \cdot b \cdot c$ 011	$a' \cdot b \cdot c'$ 010
$a \cdot b' \cdot c'$ 100	$a \cdot b' \cdot c$ 101	$a \cdot b \cdot c$ 111	$a \cdot b \cdot c'$ 110

4-input

$a' \cdot b' \cdot c' \cdot d'$ 0000	$a' \cdot b' \cdot c' \cdot d$ 0001	$a' \cdot b' \cdot c \cdot d$ 0011	$a' \cdot b' \cdot c \cdot d'$ 0010
$a' \cdot b \cdot c' \cdot d'$ 0100	$a' \cdot b \cdot c' \cdot d$ 0101	$a' \cdot b \cdot c \cdot d$ 0111	$a' \cdot b \cdot c \cdot d'$ 0110
$a \cdot b \cdot c' \cdot d'$ 1100	$a \cdot b \cdot c' \cdot d$ 1101	$a \cdot b \cdot c \cdot d$ 1111	$a \cdot b \cdot c \cdot d'$ 1110
$a \cdot b' \cdot c' \cdot d'$ 1000	$a \cdot b' \cdot c' \cdot d$ 1001	$a \cdot b' \cdot c \cdot d$ 1011	$a \cdot b' \cdot c \cdot d'$ 1010

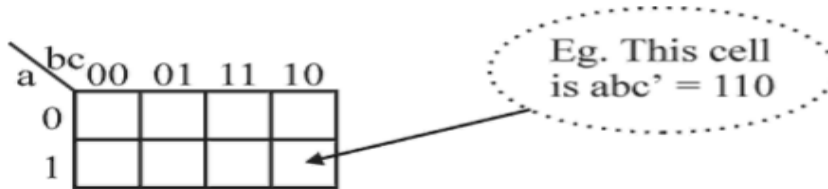
The cells are arranged as above, but we write them empty, like this:



Note that the numbers are not in binary order, but are arranged so that only a single bit changes between neighbours. This one-bit change applies at the edges, too. So cells in the same row on the left

and right edges of the array also only differ by one bit.

Note: The value of a particular cell is found by combining the numbers at the edges of the row and column.



Also, in general, it is easier to order the inputs to a K-map so that they can be read like a binary number. (Show example.) So, we have this grid. What do we do with it?

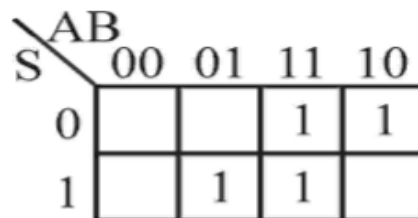
- We put 1's in all the cells that represent minterms in the SSoP [4]. (In other words, we find the 1's in the truth table output, and

put 1's in the cells corresponding to the same inputs.)

Let's do this in relation to the 2-input multiplexer example:

If there are two neighbouring 1's in the grid, it means that the input bit change between the two cells has no effect on the output, and thus there is redundancy. This leads to a basic strategy.

S	A	B	Y
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1



## 5. Conclusions

In this paper a useful educational tool was presented. The application can minimize a Boolean expression using a Palm PDA. Today PDA's are very popular among students and can be used easily in the classroom. For the implementation of the algorithm C++ coding was used on the CodeWarrior-Palm environment. The .prc file, which is executable on a Palm-based PDA, is 54K and is available from the authors for educational use.

## Acknowledgement

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