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Power Quality improvement by using DSTATCOM in power distribution system fed BLDC motor

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Abstract: *A Power quality problem is an occurrencemanifested as a nonstandard voltage, current or frequencythat results in a failure or a miss operation of end userequipment's. Utility distribution networks, sensitiveindustrial loads and critical commercial operations sufferfrom various types of outages and service interruptionswhich can cost significant financial losses. With therestructuring of power systems and with shifting trendtowards distributed and dispersed generation, the issue ofpower quality. Utility distribution networks, sensitiveindustrial loads and critical commercial operations sufferfrom various types of outages and service interruptionswhich can cost significant financial losses.Distribution static compensator DSTATCOM) is ashunt compensation device that is generally used tosolve power quality problems in distribution systems. DSTATCOMforces load to operate always at rated power. Hence,customers need to pay for constant power continuously,whereas rated heating losses will take place in theequipments for entire operation. This concept proposes a hybrid control algorithm to maximize utilization and functionality of distribution staticcompensator (DSTATCOM). In this paper,implementing Power quality circuit with voltagesource converter in the BLDC Motor drive isdeveloped. The control of BLDC drive used is FieldOriented Control using pulse with Modulation. Thepower quality circuit proposed here improves thepower factor and reduces harmonic distortion.Simulation work is performed using MATLAB /SIMULINK environment.*

Key Words: DSTATCOM, power Quality, BLDC motor, Distribution Generation.

I INTRODUCTION

Electric power distribution network havebecome more increasingly important and plays anessential role in power system planning. This type ofpower systems has a major function to servedistributed customer loads along a feeder line;therefore under competitive environment ofelectricity market service of electric energy transfermust not be interrupted and at the same time theremust provide reliable, stable and high quality ofelectric power [1-2].However,one might consider an additional device to beinstalled somewhere in the network. Such devicesare one of capacitor bank, shunt reactor, seriesreactors [3-4], and automatic voltage regulators and/orrecently developed dynamic voltage restorers,distribution static compensator (DSTATCOM), orcombination of them [5-6]. The DSTATCOM [9-10]is a voltage source converter (VSC) based custompower technology which can perform as a reactivepower source in power systems. The D-STATCOM[7] can regulate magnitude of voltage at a particular ACbus, at the point where it is connected, via generatingor absorbing reactive power from the system.

D-STATCOM acts as a shunt compensatorconnected in parallel to the system so that it can injectappropriate compensation currents. The D-STATCOMhas several advantages compared to a conventional staticvar compensator (SVC). The D-STATCOM gives fasterresponse and can produce reactive power at low voltages [8].Also, it doesn't

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require thyristor-controlled reactors(TCR) or thyristor-switched capacitors.

In present day distribution systems (DS), major powerconsumption has been in reactive loads. The typicalloads may be computer loads, lighting ballasts, smallrating adjustable speeds drives (ASD) in airconditioners, fans, refrigerators, pumps and otherdomestic and commercial appliances are generallybehaved as nonlinear loads [9-10]. These loads draw laggingpower-factor currents and therefore give rise toreactive power burden in the DS. Moreover, situationworsens in the presence of unbalanced and non-linearloads, affect the quality of source currents to a largeextent. It affects the voltage at point of commoncoupling (PCC) where the facility is connected. Thishas adverse effects on the sensitive equipmentsconnected to PCC and may damage the equipmentappliances [11].

In this paper, a hybrid control scheme has been proposed to maximize the DSTATCOM utilization while considering the above mentioned issues. Instantaneous symmetrical component theory, with flexibility of choosing p_f, is used to compute reference source currents [12]. The reference load voltages are computed such that the least allowable p_f is maintained at the PCC. Consequently, load power is appropriately controlled and advantages of energy conservation are also achieved. If reference load voltage at the predefined minimum pf comes less than the lowest allowable operating voltage, then p_f is improved to get new reference load voltage. Therefore, proposed scheme ensures that energy conservation is achieved while drawing allowable reactive power from the source.

The BLDC Motor used to make low power ratingapplication devices such as Refrigerator, WashingMachine, House-hold appliances, Medical Equipment,Wide speed range of servo drives and industrial robots.BLDC drives are used for its high efficiency, fast dynamic response and small size etc. For the operationof BLDC Drive first need to convert AC supply powerto DC power using rectifier circuit and then DC powerto variable magnitude and variable frequency ACpower to feed BLDC.

II SYSTEMDESCRIPTION

Circuit configuration of a DSTATCOM, as shown in Fig.1, is connected at the PCC in a three-phase four-wire distribution system.v_{sj}, v_{tj}, i_{sj}, and i_{lj} are source voltage, load voltage, source current, and load current respectively, where j=a, b, c represent phases. Rs and Xs represent the source impedance in three phases. The load consists of a diode rectifier feeding an RL load plus an unbalanced linear load. The DSTATCOM uses two-level, neutral-point-clamped VSI topology due to its ability to control the operation of each VSI leg independently. The dc link capacitors are represented by C_{dc1} =C_{dc2} = C_{dc}, whereas the voltages maintained across them are V_{dc1}= V_{dc2}=V_{dc}=V_{dcref}. An LC filter is used at the front end of VSI to achieve an appropriate output voltage at the PCC.

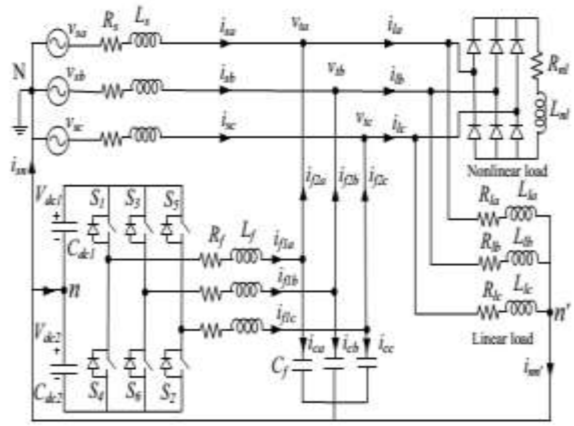

Fig 1 DSTATCOM configuration in a distribution system

III PROPOSEDHYBRIDCONTROLALGORITHM

Objective of proposed hybrid control algorithm based DSTATCOM is to reduce the voltage and current harmonics, balance the source currents, improve the pf, compensate for voltage disturbances such as sag and swell, reduce the losses in VSI, reduce the rating of VSI, and control the load power for energy conservation while analyzing their effects on the consumers.

During normal operating conditions, at which system operates most of the time, it is desired that harmonic component of load current is supplied by the filter. Additionally, pf at the PCC is maintained in such a way that the penalty for reactive power drawn from the source is avoided. In literature, several reference generation schemes have been proposed. In this paper, instantaneous symmetrical component theory (ISCT) based algorithm is used for computation of reference source currents due to its flexibility in achieving desired pf [11]. Following three conditions are simultaneously satisfied while using ISCT based algorithm:

1. Source neutral current should be zero

$$i_{sa} + i_{sb} + i_{sc} = 0 \quad (1)$$

2. A definite angle (φ_{vi}+) between fundamental positive sequence load voltage and source current (v_{ta1}+ and i_{la1}+respectively) is maintained. Considering for phase-a

$$\angle v_{ta1+} = \angle i_{la1+} + \phi_{vi+} \quad (2)$$

3. Source must supply average load power (P_{lavg}) and VSI losses (P_{loss}). Source power is given as

$$P_s = \frac{1}{T}\int_{t_1}^{t_1+T} v_{ta} i_{sa} + v_{tb} i_{sb} + v_{tc} i_{sc} = P_{lavg} + P_{loss} \quad (3)$$

where

$$\begin{cases} P_{lavg} = \frac{1}{T}\int_{t_1}^{t_1+T} v_{ta} i_{la} + v_{tb} i_{lb} + v_{tc} i_{lc} \\ P_{loss} = \frac{1}{T}\int_{t_1}^{t_1+T} v_{ta} i_{f2a} + v_{tb} i_{f2b} + v_{tc} i_{f2c} \end{cases} \quad (4)$$

Solving (3.1), (3.2), and (3.3) expressions for reference source currents are given as follows:

$$i_{sa} = \frac{v_{ta} + \beta(v_{tb} - v_{tc})}{\sum_{j=a,b,c} v_{tj}^2}(P_{lavg} + P_{loss})$$
$$i_{sb} = \frac{v_{tb} + \beta(v_{tc} - v_{ta})}{\sum_{j=a,b,c} v_{tj}^2}(P_{lavg} + P_{loss})$$
$$i_{sc} = \frac{v_{tc} + \beta(v_{ta} - v_{tb})}{\sum_{j=a,b,c} v_{tj}^2}(P_{lavg} + P_{loss}) \quad (5)$$

Where

$$\beta = \frac{tan\phi_{vi+}}{\sqrt{3}}$$

The phase-a load voltage and source current are given as follows:

$$v_{ta}(t) = \sqrt{2}V_{ta}\sin(\omega t - \delta)$$
$$i_{sa}(t) = \sqrt{2}I_s\sin(\omega t - \delta - \phi_{vi+}). \quad (6)$$

Phase-a source voltage is given as

$$v_{sa}(t) = \sqrt{2}V_s\sin\omega t \quad (7)$$

Applying Kirchhoff's voltage law between source and load points

$$\overline{V}_{sa} = \overline{V}_{ta} + \overline{I}_{sa}(R_s + jX_s) \quad (8)$$

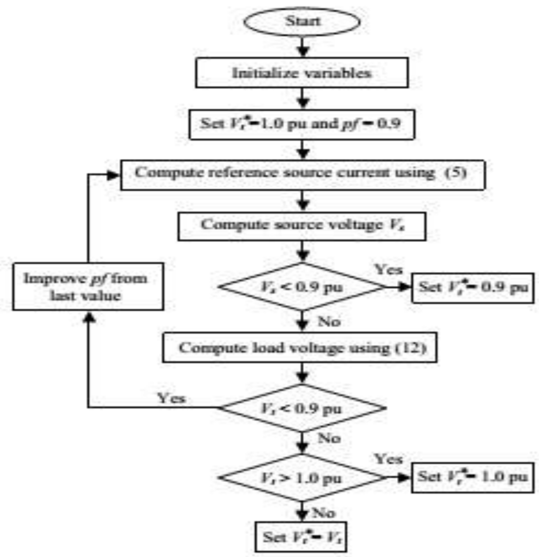

Fig2 Proposed hybrid control algorithm for reference load voltage generation

Using (6) and (7) in (8), we have

$$V_{sa}\angle 0 = V_{ta}\angle -\delta + [I_{sa}\angle(-\delta - \phi_{vi+})]Z_s\angle\theta_z \quad (9)$$

Where $Z_s = \sqrt{R_s^2 + X_s^2}$ and $\theta_z = \tan^{-1}\frac{X_s}{R_s}$

Equating real and reactive part in both the sides of above equation

$$V_{sa} - I_{sa}Z_s\cos(\phi_{vi+} + \delta - \theta_z) = V_{ta}\cos\delta$$
$$-I_{sa}Z_s\sin(\phi_{vi+} + \delta - \theta_z) = V_{ta}\sin\delta \quad (10)$$

Squaring and adding the above equation

$$V_{ta}^2 = V_{sa}^2 + (I_{sa}Z_s)^2 - 2V_{sa}I_{sa}Z_s\cos(\phi_{vi+} + \delta - \theta_z) \quad (11)$$

Finally, load voltage will be

$$V_{ta} = \sqrt{V_{sa}^2 + (I_{sa}Z_s)^2 - 2V_{sa}I_{sa}Z_s\cos(\phi_{vi+} + \delta - \theta_z)} \quad (12)$$

With balanced supply, computed rms load voltage will be same for all three-phases and it will be used as V_t for further explanations. Once the expression for load voltage from above equation is computed, reference load voltage magnitude for several DSTATCOM operating conditions will be developed. The flow chart of choosing the suitable reference load voltage is given in Fig.2 and explained as follows.

a)*Source Voltage Is Less Than 0.9 Pu:*

Voltage sag, which is most common voltage disturbance, is defined as the decrease in voltage from 0.9 pu to 0.1 pu for half cycle to one minute. Hence, if load voltage goes below 0.9 pu, then system experiences sag. During sag, primary aim of the compensator is to protect the load from this

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voltage disturbance. In conventional VCM operation of DSTATCOM, load voltage is maintained at 1.0 pu during sag. It results in more current injection by the VSI, increased losses in VSI, and rated power drawn by the load. In this paper, the reference load voltage is set at 0.9 pu during voltage sag. At this voltage, following features are obtained

i. Load will remain operational even during voltage disturbances.

ii. Load will draw minimum power, as compared to rated power in conventional DSTATCOM operating in VCM.

iii. Filter current will less compared to conventional VCM operation. Hence, VSI losses will decrease and so, efficiency will increase. Moreover, size of VSI can be reduced due to reduced current requirement.

b)*Source Voltage Is Greater Than 0.9 Pu:*

When source voltage is greater than 0.9 pu, then it is necessary to find appropriate reference load voltage. Conventional CCM operation of DSTATCOM maintains UPF at the PCC. Usually, most of the utilities permit customers to draw allowable reactive power without paying for tariff (however, it may vary depending upon customers). Therefore, from customer point of view, it is not necessary to maintain UPF at PCC. In proposed scheme, we have considered that the customers are allowed to operate load up to 0.9pfwithout any penalty. Keeping this into account, reference source currents are calculated using instantaneous symmetrical component theory at a pf of 0.9. i.e., β= 0.28. With this source current, load voltage is calculated using (12). At this voltage, following conditions are possible:

1) V_{ta} is Less Than 0.9 pu: This voltage is computed at the lowest permissible value of p_f. But, improvement in pf will increase the load voltage. Therefore, pf is improved in a fixed step of 0.05 from previous value of 0.9 pu and modified reference source currents are again computed using (5). For this current, modified load voltage is computed. If this voltage becomes greater than 0.9 pu then same voltage is used as reference load voltage. This method gives following advantages:

i. Source currents will be balanced and sinusoidal.

ii. Reduced currents are supplied by the filter compared to conventional CCM operation. Therefore, VSI losses decrease and its efficiency increases.

iii. Load voltage is lesser compared to conventional CCM operation. Hence, power drawn by the load will decrease. It will reduce the power tariff, reduce the heating loss, and increase the device life.

However, if the load voltage is not more than 0.9 pu, then above process is repeated until the load voltage becomes more than 0.9 pu. But, there must be a limit on the value of p_f that can be achieved. Our objective is to keep load operational by keeping load voltage within the permissible range, while ensuring that allowable amount of reactive current is also drawn from the source. Importantly, load voltage will be more as compared to conventional CCM operation if pf is set to leading. It will force load to draw additional real power. Therefore, maximum p_f is limited to 1.0. If load voltage does not become greater than 0.9 pu with this p_f, then a flat voltage of 0.9 pu is set as reference load voltage.

2) Vta is Less Than 1.0 pu: This is the normal operating conditions as load voltage lies between 0.9 pu to 1.0 pu, where system operates most of the time. The reference load voltage is set at a value obtained from (12). This voltage will indirectly control the source currents and maintains 0.9 p_f at the PCC. Therefore, operation of proposed scheme in this case will be similar to conventional CCM operation of DSTATCOM. However, following additional advantages are achieved in proposed scheme:

i. Predefined minimum p_f is maintained at the PCC by the compensator. Hence, filter currents are reduced. Consequently, VSI losses decreases and its efficiency increases.

ii. The least $p_f$at PCC makes load voltage lesser compared to conventional DSTATCOM operating in CCM. Hence, power drawn by the source decreases. It reduces the power tariff, reduce the heating loss, and increase the device life.

3) Vta is Greater Than 1.0 pu: Maintaining load voltage at a value greater than 1.0 pu forces load to draw more power than the rated power. Further, filter will have to supply more reactive current to maintain this voltage. If load voltage comes more than 1.0 pu, then source voltage is also more than 1.0 pu. This voltage is computed at a p_f of 0.9. If load voltage does not become less than 1.0 even for 0.9pf, a flat voltage of 1.0 is set as reference voltage. In this case, performance of DSTATCOM and load in proposed scheme will be same as that of conventional DSTATCOM operating in VCM.

IV BLDC MOTOR

BLDC engine comprises of the perpetual magnet rotor and an injury stator. The brushless engines are controlled utilizing a three stage inverter. The engine obliges a rotor position sensor for beginning and for giving legitimate compensation arrangement to turn on the force gadgets in the inverter extension. In light of the rotor position, the force gadgets are commutated consecutively every 60 degrees. The electronic compensation takes out the issues connected with the brush and the commutator plan, in particular starting and destroying of the commutator brush course of action, along these lines, making a BLDC engine more rough contrasted with a dc engine. Fig.4 demonstrates the stator of the BLDC engine and fig.5 shows rotor magnet plans.

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Fig.3. BLDC motor stator construction

Fig.4. BLDC motor Rotor construction.

The brush less dc engine comprise of four fundamental parts Power converter, changeless magnet brushless DC Motor (BLDCM), sensors and control calculation. The force converter changes power from the source to the BLDCM which thus changes over electrical vitality to mechanical vitality. One of the remarkable highlights of the brush less dc engine is the rotor position sensors, in view of the rotor position and order signals which may be a torque charge, voltage summon, rate order etc; the control calculation s focus the entryway sign to every semiconductor in the force electronic converter.

The structure of the control calculations decides the sort of the brush less dc engine of which there are two principle classes voltage source based drives and current source based drives. Both voltage source and current source based commute utilized for perpetual magnet brushless DC machine. The back emf waveform of the engine is demonstrated in the fig.5. Be that as it may, machine with a non sinusoidal back emf brings about diminishment in the inverter size and lessens misfortunes for the same influence level.

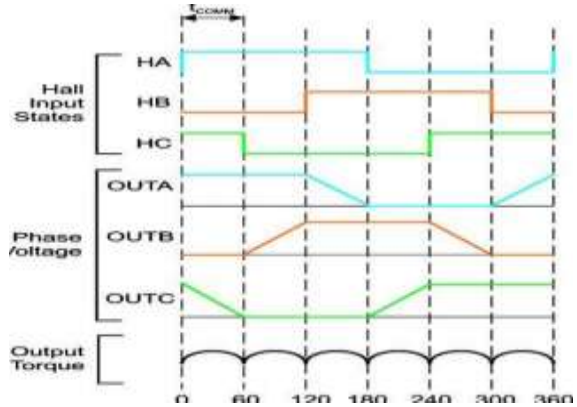

Fig.5. Hall signals & Stator voltages.

V MATLAB/SIMULINK MODEL

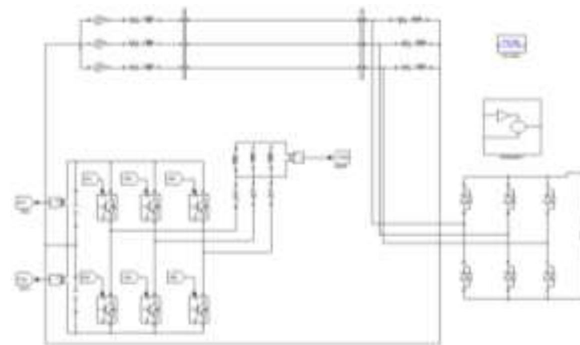

Fig 6 Matlab/simulation circuit ofwithout DSTATCOM configuration in a distribution system.

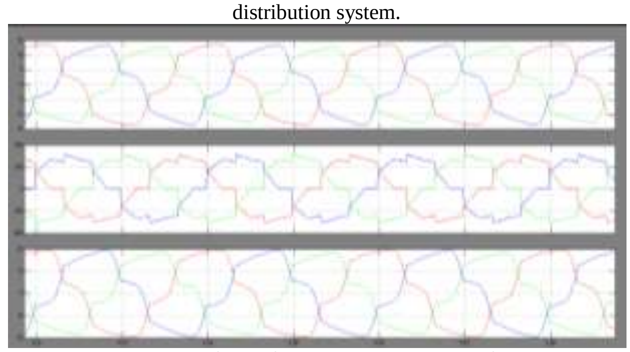

Fig 7 simulation wave form of without compensation of output source voltage and current

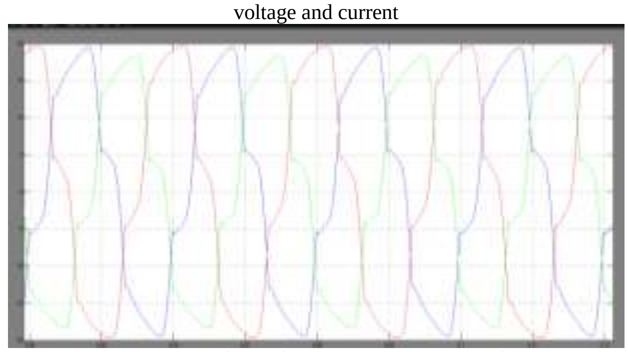

Fig 8 simulation wave form of without compensation of output load current.

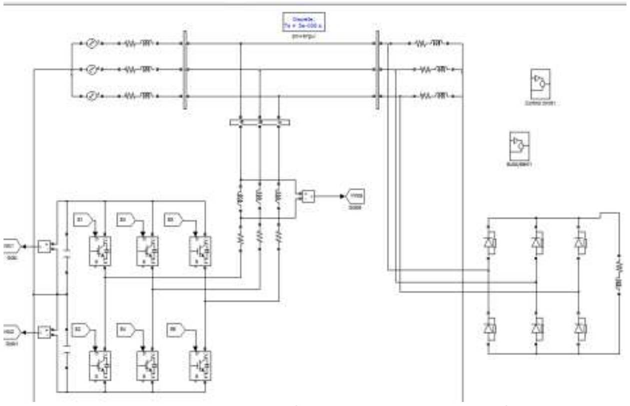

Fig 9 Matlab/simulation circuit of with DSTATCOM configuration in a distribution system.

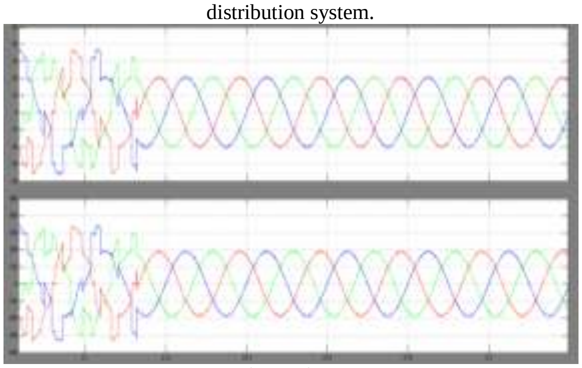

Fig 10 simulation wave form of source voltage and current

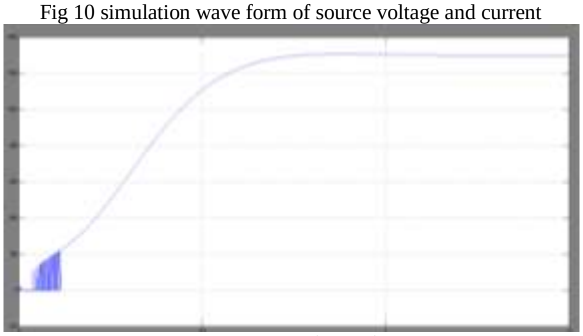

Fig 11 simulation wave form of dc voltage.

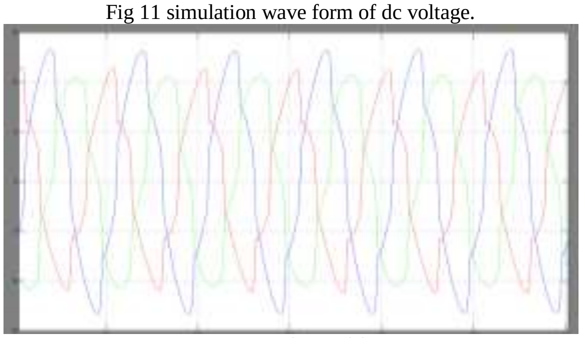

Fig 12 simulation wave form of filtering voltage.

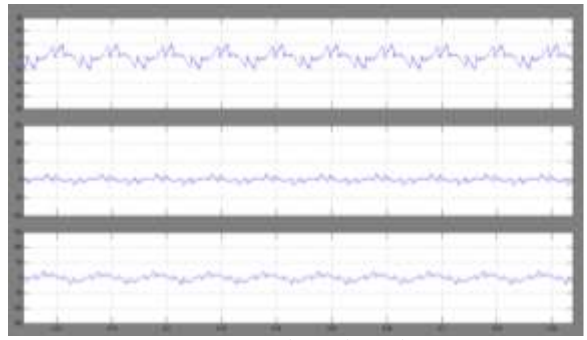

Fig 13 simulation wave form of load filter voltage.

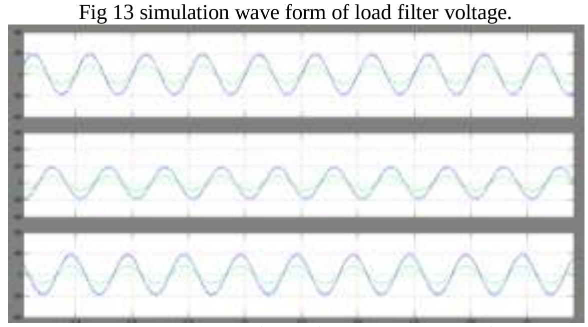

Fig 14 simulation wave form of source side power factor.

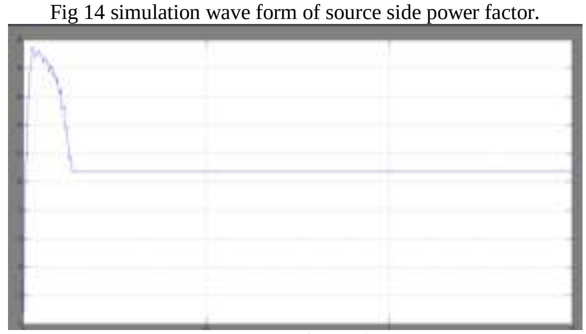

Fig 15 simulation wave form of RMS voltage.

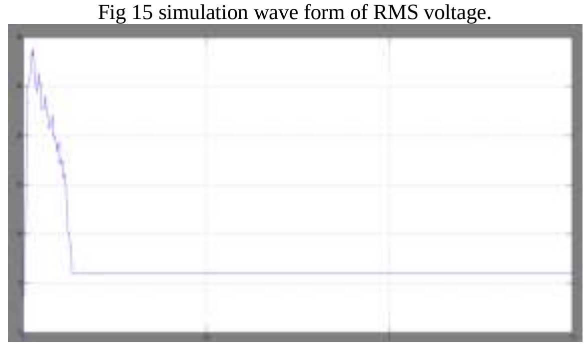

Fig 16 simulation wave form of RMS current

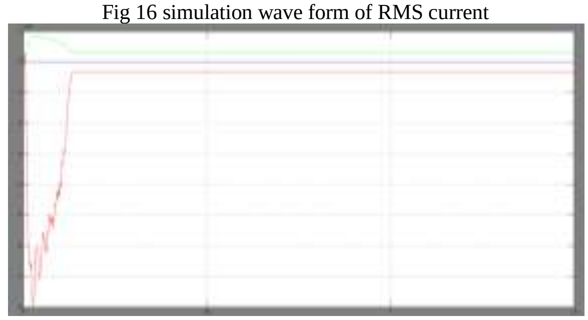

Fig 17 simulation wave form of reactive power

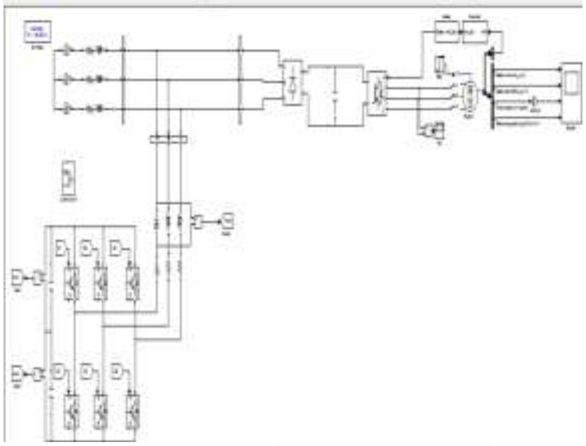

Fig 18 Matlab/simulation circuit of with DSTATCOM configuration in a distribution system with BLDC drive

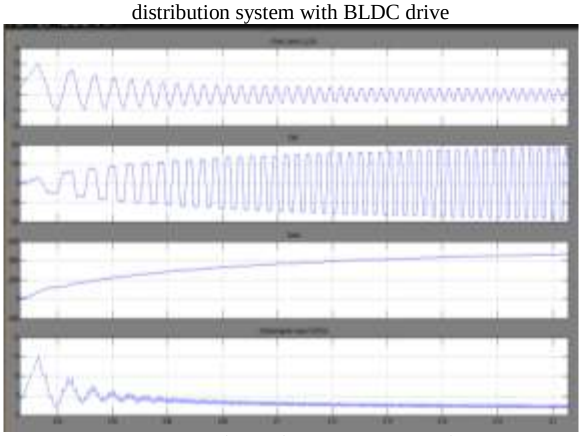

Fig 19 simulation wave form of DSTATCOM configuration in a distribution system with BLDC drive with stator current, EMF, speed, and torque.

V CONCLUSION

A new hybrid DSTATCOM with reduced dc-linkvoltage has been discussed in detail which has theability to compensate the load improving power qualityof distribution system. Design of various parametersand control theories involved in this study wereexplained.In this paper, a control algorithm has been proposedfor the generation of reference load voltage for avoltage-controlled DSTATCOM. The performance ofthe proposed scheme is compared with the traditionalvoltage-controlled DSTATCOM.This scheme notonly provides several PQ improvement features but alsoreduces load real and reactive power, filter currentrequirement to achieve desired compensation performance,and leads to reduction in the size of VSI. Extensivesimulation studies validate effectiveness of the proposedBLDC drive to study the characteristics, and to achievinggood performance.

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