

# Analysis of bidirectional dc-dc converter interfacing ultracapacitor using PV cells

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**ABSTRACT:** This paper highlights the controller of a bidirectional converter to interface an ultra-capacitor as storage device to renewable energy systems. Ultra capacitors are typically used in renewable energy systems to improve the system's reliability and energy conversion efficiency. The controller of the converter system has been designed and simulated based on the integration of both Current Mode Control and Linear Quadratic Regulator methods. The controller performance is tested under different modes of operating conditions in bidirectional converter using MATLAB/Simulink simulation package. The simulation results show that a good DC bus voltage regulation is achieved in the tested conditions. In addition to that, the controller ensures smooth transition between the buck and boost modes of the bidirectional converter operation.

Index Terms—bidirectional converter; ultra-capacitor; peak current mode control; linear quadratic regulator.

## I. INTRODUCTION

In recent days, the number of applications which require more than one power source is increasing. Distributed generating systems or micro-grid systems normally use more than one power source or more than one kind of energy source. Also, to increase the utilization of renewable energy sources, diversified energy source combination is recommended. The combination of more power sources and diversified power sources make it possible to obtain higher availability in a power system. A parallel connection of converters has been used to integrate more than one input energy source in a power system. However, a MIC [1]-[4] can generally have the following advantages compare to a combination of several individual converters like cost reduction, compactness, more expandability and greater manageability. MICs are being used in aerospace, electric and hybrid vehicles, sustainable energy sources and microgrid applications. The widespread industrial use of induction motor (IM) has been stimulated over the years by their relative cheapness, low maintenance and high reliability. The control of IM variable speed drives often requires control of machine input

voltage, which is normally achieved by using a voltage source inverter. Amongst storage devices, ultracapacitor is preferred due to its long life-time, good electrical behavior and to its relatively low initial cost in comparison with modern batteries [5]. In addition, it is positively characterized by its high power density, low losses while charging and discharging, and its very low equivalent series resistor (ESR) which allows it to deliver and absorb very high currents and to be charged very quickly [6-7]. Furthermore, ultracapacitor can provide large transient power instantly [8].

Various control methods have been proposed in the literature to interface renewable energy sources with a storage device using a bidirectional converter. The authors in reference [8] applied the dynamic evolution control method to interface a fuel cell and the ultracapacitor.

In literature [5], the PI controller was designed for the integration of wind energy conversion system and ultracapacitor.

The current programmed mode (CPM) duty ratio control and linear PI compensator was reported in [8] for controlling a bidirectional converter interfacing wind energy conversion and battery storage system.

A combination of both fuzzy and sliding-mode control strategies to interface the wind energy conversion system and the storage device has been proposed in [8].

Different from that available in the literature, the proposed controller in this paper introduces feedback paths that are calculated optimally to minimize an associated cost function, which is expected to improve the dynamic performance of the system. Due to its simplicity, high bandwidth, and low implementation cost, current mode control (CMC) approach is popular in controlling the power electronic converters [16]. Among the different types available for CMC, Peak current mode control

(PCMC) is the most common one in which the peak value of the inductor current is sensed and compared with the current reference for the generation of the PWM signal [7].

Another control method that is most cited for controlling the PWM converters is linear quadratic regulator (LQR) control [8]. Since the controller feedback gain-vector is determined optimally in LQR, the designers can guarantee that the converter has good closed-loop behavior, and is relatively insensitive to system parameter variations or external

## II. Modeling of the Ultra capacitor and Bidirectional DC-DC Converter

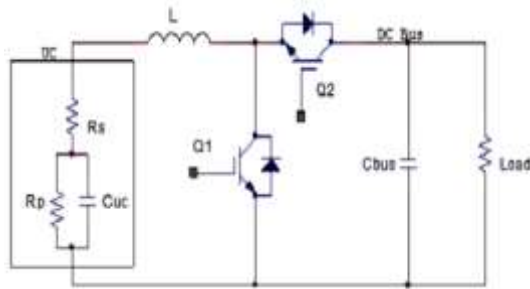


Fig. 1. The electrical circuit of ultra-capacitor-bidirectional DC-DC converter topology

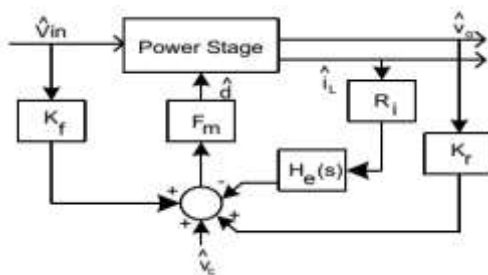


Fig. 2. The small-signal model of PCMC converter.

$$F_m = \frac{1}{(M_1 + M_c)T_s} \dots \dots \dots 2$$

$$k_f = \frac{-T_s R_i}{2L} \dots \dots \dots 3$$

$$k_r = \frac{-D'^2 T_s R_i}{2L} \dots \dots \dots 4$$

Where  $M_1$  is the rising slope of the inductor current,  $M_c$  is the slope of the artificial ramp signal that is used for slope compensation. It is stated that there is an inherent stability when  $D > 0.5$  for all types of

converters. In order to guarantee the controller stability for all ranges of the duty-cycle, an artificial ramp with slope  $M_c \geq 0.5M_2$  has to be added ( $M_2$  is the falling slope of the inductor current).  $T_s$  is the switching period. As it is very small, the  $k_r$  can be neglected.

Based on Fig. 2, when  $k_r$  is neglected, the duty ratio law can be expressed as:

$$\hat{d} = F_m (-R_i \hat{i}_L + k_f \hat{v}_{in} + \hat{v}_c) \dots \dots \dots 5$$

## III. The Linear Quadratic Regulator-Current Mode

As aforementioned, the objective of the controller in this paper is to ensure a good voltage regulation at the DC bus. Thus, the small signal model of the CMC boost converter is augmented to include the new feedbacks from the state variables of the converter. In addition, a new state variable, the error between the reference and the output voltage, is added, as shown in Fig. 3.

$$\hat{\xi} = \hat{x}_3 = \hat{V}_{ref} - C\hat{x}$$

With the new state-space vector  $\hat{X}_a = [\hat{x} \ \hat{\xi}]$ , the augmented small-signal model can be written as,

$$\begin{aligned} \hat{X}_a = & \begin{bmatrix} A_{2 \times 2} & 0_{2 \times 1} \\ -C_{1 \times 2} & 0_{1 \times 1} \end{bmatrix} \begin{bmatrix} \hat{x} \\ \hat{\xi} \end{bmatrix} + \begin{bmatrix} b_{1 \times 2} \\ 0_{1 \times 1} \end{bmatrix} \hat{v}_{in} \\ & + \begin{bmatrix} b_{2 \times 1} \\ 0_{1 \times 1} \end{bmatrix} \hat{v}_c \\ & + \begin{bmatrix} 0_{2 \times 1} \\ I_{1 \times 1} \end{bmatrix} v_{ref} + \begin{bmatrix} b_{3 \times 2} \\ 0_{1 \times 1} \end{bmatrix} \hat{i}_0 \end{aligned}$$

where the matrices A and B are obtained from the small signal state-space model of the CMC PWM DC-DC boost converter system in (6).  $C = [0 \ 1]$ .

In addition, we have

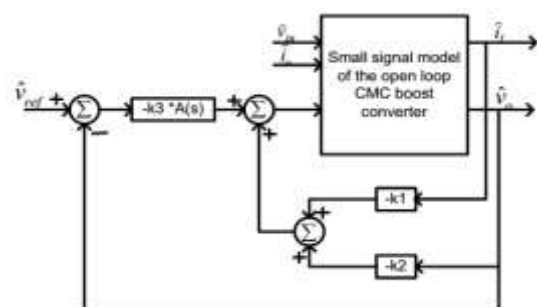


Fig. 3. The small signal model of closed-loop CMC PWM boost converter with linear feedback control.

#### IV. EXPERIMENTAL RESULTS

In this section, the MATLAB/Simulink simulation results for different operation modes of the bidirectional converter that interfaces the ultracapacitor to the DC bus are depicted and discussed. The simulated system diagram is shown in Fig. 4, and the used parameters for the converter and the ultracapacitor are listed in Table I.

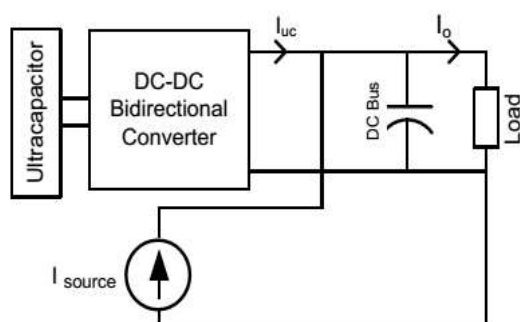


Fig. 4. The block diagram of the proposed interfacing system.

When the renewable source current was maintained at 10 A

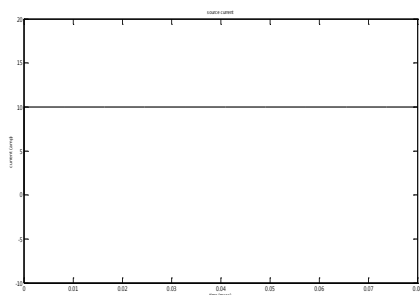


Fig.5 Constant source current

The response of a step variation in the load current from 5A to 15A and then to 5A, when the source current is fixed at 10A.

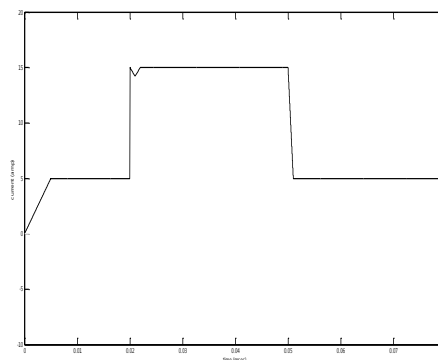


Fig. 6 Load current when source current is constant  
The response of a step variation in ultra capacitor current from 5A to 15A and then to 5A, when the source current is fixed at 10A. By charging and discharging modes of operation.

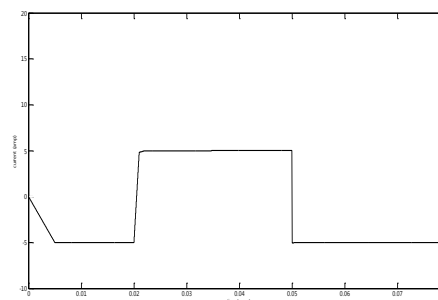


Fig.7 ultra capacitor current when source current is constant

The load current was changed in steps from 5 A to 15 A and then to 5 A. In the first interval (between  $t=0$  and  $t=0.02$  s) the renewable source covered the load demand and injected its excess current to the ultra capacitor. In this interval, the bidirectional converter operated in a buck mode. When an additional 10 A was required by the load (between  $t=0.02$  and  $t=0.05$  s), the renewable source was unable to provide the full load demand. Thus, the bidirectional converter acts as boost mode to discharge the ultra capacitor and supply the extra load demand (5 A).

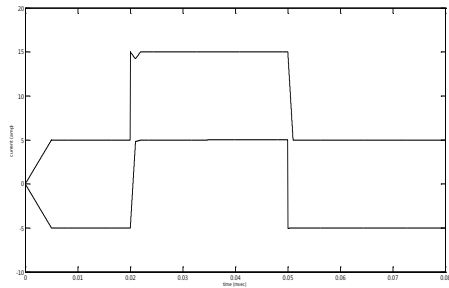


Fig.8 Combination of Load &ultra capacitor currents

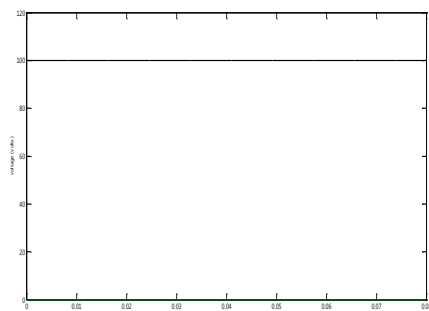


Fig .9 Reference voltage fixed at 100 V  
When the ultra capacitor changing from charging and discharging mode of operation the spikes are obtained at different time intervals.

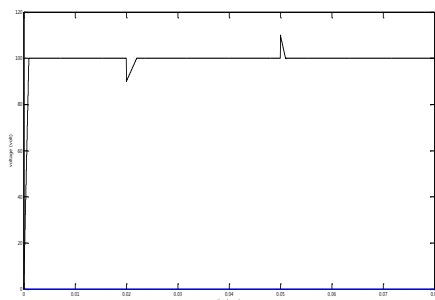


Fig. 10 output voltage when reference voltage is constant

The DC bus voltage , it was regulated at the desired value 100V . The figure shows clearly for the two modes of operation.

When the source current is varied at different time intervals. Isource was changed from 0 A to 10 A at time of 0.04 sec. Since we are using PWM technique the waveform will be in step variation

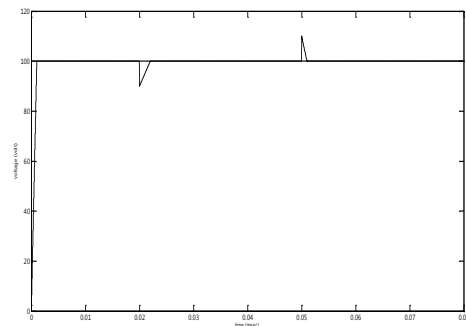


Fig.11 The combination of output voltage & reference voltage

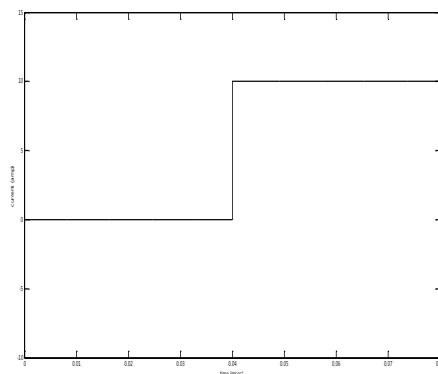


Fig. 12 variation in source current

The slight changes obtained in the load current when the source current is varied. The spikes are obtained since we are using capacitor and inductor.

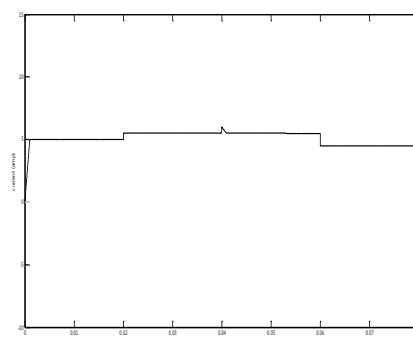


Fig.13 Load current when source current is varied  
The ultra capacitor current operates in two modes i.e., buck and boost modes. From 0.01 sec the buck mode is operated by charging the ultracapacitor.

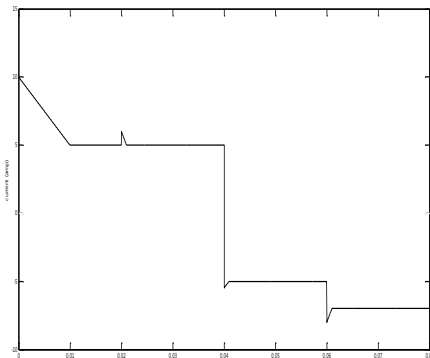


Fig.14 ultra capacitor current when source current is varied

The combination of both load and ultra capacitor current is shown.

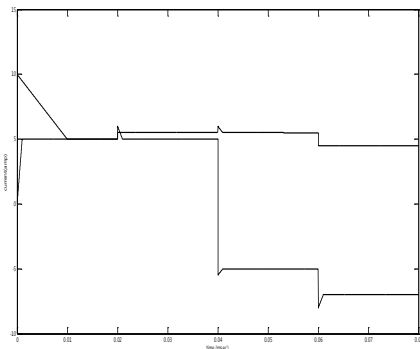


Fig.15 combination of load & ultra capacitor currents

The reference voltage is changing from 100 V to 110 V and again back to 90 V.

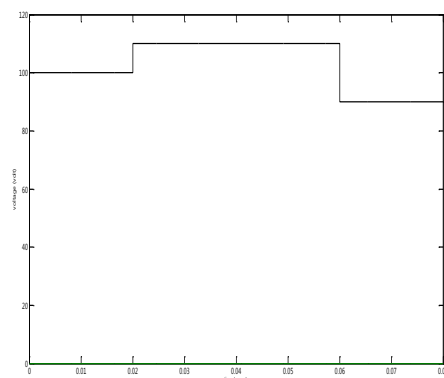


Fig.16 Variation in reference voltage

The output voltage obtained when the reference voltage is varied. The boost mode is operated from

0.02 sec to 0.06 sec and remaining in buck mode, here the voltage will be at 110 V .

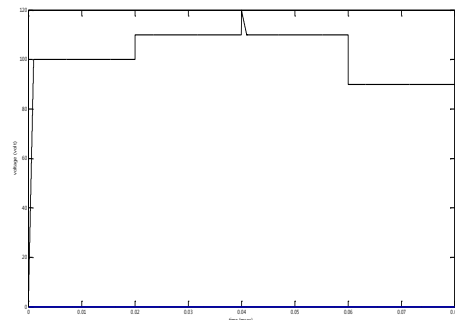


Fig.17 Output voltage when voltage is varied  
The combination of both reference voltage and output voltage .

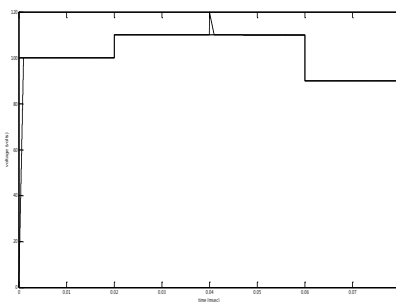


Fig 18 Reference voltage and output voltages

## V. CONCLUSION

The LQR-CMC method has been successfully applied to control the bidirectional converter in the case of boost and buck modes. The objectives of the controller were to regulate the output voltage and to achieve a smooth transition between the two operation modes of the bidirectional converter, namely buck and boost modes. In addition, the proposed controller ensures continuous power supply to the load, regardless of the load and renewable energy power changes. In short, the proposed controller is capable of increasing the reliability and energy conversion efficiency of renewable energy systems.

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