

# Experimental Study of Electronic Transporte in TiO<sub>2</sub>/Si Junctions

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## Abstract

*In this work, a heterojunction TiO<sub>2</sub> on Si base has been made by use the D.C magnetron sputtering method with Ti target and oxygen plasma. The film was deposited on Si wafer and then undergone to the annealing process at various annealing temperatures were employed to assess film properties. The capacitance with reverse bias has been measured as a function of bias voltage at frequency 10 kHz, and these measurements have indicated the width of the depletion layer (W) surges with increasing of annealing temperature. The current–voltage characteristic It turned out through the current–voltage graph of TiO<sub>2</sub>/Si heterojunction the forward current at dark changes with change of applied voltage, in demeanor concordant with recombination-tunneling model.*

*The results have shown that the dark current rises with growth of Ta. Also under illumination condition its clear to show there is a positive correlation relationship between the photocurrent and Ta.*

**Keywords** : magnetron sputtering, annealing , heterojunction, depletion layer, photocurrent, tunneling current .

## 1- Introduction

Titanium dioxide (Titania, TiO<sub>2</sub>) material has wide applications in optoelectronic fields due to their characterizes and properties. TiO<sub>2</sub> is one of the of transition metal oxides, there are three common polymorphs types for this material, anatase (tetragonal), and brookite (orthorhombic) and : rutile (tetragonal)[1].

Currently, it employ to use as an electrode in electrochemical systems, particularly for gas detection devices [1-5]. The molecular structure of Titanium dioxide (TiO<sub>2</sub>) provides advantageous properties, for instance the high value of the dielectric constant, chemical stability, mechanical rigidity, besides the perfect insulating properties. The optical transmittance and high refractive index within the extend of the visible spectrum, for the Titanium dioxide made it a suitable material for using as protective coatings for very large-scale integrated circuits [6, 7].

The most common method to prepare TiO<sub>2</sub> thin films is the reactive sputtering, in this technique offers the possibility to use , evaporation rates, pressures of reactive gases, and , and substrate temperatures to control and adjust the packing density of the films. The D.C. sputtering process involves electron ejecting from the cathode material and forced to move away from it, by exploiting appropriate magnetic field to make the electrons take circular orbits and stay near the target surface, that give rise to growth high flux of electrons and creates high density plasma, this plasma is utilized to pluck out target atoms and then directed towards the substrate [8-10].

Heterojunction is a semiconductor interface formed between two different types of semiconductors. These two semiconductors are assumed possess different values of energy gap ( $E_g$ ), permittivity ( $\epsilon_s$ ), work functions ( $\Phi_m$ ), and electron affinities ( $\chi$ ) as well as a difference in lattice constant. Heterojunction has been widely studied, and there are many essential applications have exploited this formation [11, 12]

## 2- Experimental prate.

### 2.1 Magnetron Sputtering:

d.c magnetron sputtering a circular magnetron with 10 cm diameter and 400 Gausses was used. The discharge characteristics have been controlled using variable d.c supply (5kV and 500 mA) Pure Ti (99.99%) (LB) with a diameter of 10 cm and 0.4 cm thickness was used as a sputtering target;

The first step before the starting of the deposition process, it was the evacuation of the growth chamber, then Ar (99.99%) gas was introduced into the chamber as working gas. The chamber total pressure was fixed at 0.6 Pa during the whole deposition. A direct voltage (power was maintained at 100W) was applied and O<sub>2</sub> (99.99%) gas was introduced into the chamber as effective gas.

### 2.2 X-Ray Diffraction spectra

The structural properties were studied using X-ray diffraction measurements have been done and identified according to the JCPDS cards ( Brookite, Anatase and Rutile [13-15]. X-ray diffractometer system, Miniflex(600), with capper K<sub>α</sub> radiation of wavelength λ= 1.5408Å was used. The X-ray diffraction of a crystal can be formulated by means of Bragg's law :

$$2d \sin\theta = n\lambda \quad \dots\dots\dots(1)$$

where d is the d-spacing, perpendicular distance between pairs of adjacent planes in the crystal. The peaks of thin films was used to determine the average grain size using Scherrer's equation [16]:

$$D = \frac{0.9\lambda}{\beta \cos\theta} \quad \dots\dots\dots(2)$$

Where D is the average grain size (nm), λ is the X-ray wavelength (1.5408 Å), β is the full-width at half-maximum (FWHM) intensity (in radians).

### 2.3 The Electrical Measurement of TiO<sub>2</sub> /Si Heterojunction

The capacitance measurements of the heterojunction as a function of the reverse bias voltage in the range 0.0-1.0 V were performed in dark using LCR meter model HP-R2CC 4274A. The test of the frequency was set at 10 kHz. The potential energy barrier at the junction can be calculated by using small-signal capacitance–voltage (C–V) graphs. it was found that the capacitance decreases with an rises in the reverse bias, so it is negative linear correlation between the square of the capacitor value and reverse potential difference, the built – in junction potential can obtained from the voltage axis intercept at C equal zero. Theoretically, the C–V characteristic is described by the conventional heterojunction relation: [17, 18]

$$C^2 = \frac{qN_D N_A \epsilon_1 \epsilon_2}{2(N_A \epsilon_1 + N_D \epsilon_2)} \frac{1}{(V_{bi} - V)} \quad \dots\dots\dots(3)$$

Here, N<sub>D</sub> and N<sub>A</sub> is the donor density in n-TiO<sub>2</sub>, the acceptor density in p-Si, ε<sub>1</sub> and ε<sub>2</sub> are the dielectric constants of n-TiO<sub>2</sub> and p-Si, respectively, V<sub>bi</sub> is the built – in potential, and V is the applied voltage. The slope of this line gives the impurity concentration of the substrate (N<sub>A</sub>).

The width of the depletion region can be calculated by: [19,20]

$$W = \epsilon_s A_j / C_o \quad \dots\dots\dots(4)$$

where C<sub>o</sub> is the capacitance at zero biasing voltage, A<sub>j</sub> is the effective area of the junction and ε<sub>s</sub> is the permittivity of semiconductor calculated from the equation:

$$\epsilon_s = (\epsilon_1 \epsilon_2) / (\epsilon_1 + \epsilon_2) \dots\dots\dots (5)$$

where  $\epsilon_1$  and  $\epsilon_2$  is the semiconductor permittivity of the two semiconductor materials.

The dark current as a function of forward and reverse bias voltage in the range (0.0-2.0 Volt) and (-2.0-0.0 Volt) respectively, for all prepared TiO<sub>2</sub> /Si junction with various annealing temperatures were carried out using Keithley digital electrometer

616 and d.c power supply. The barrier height ( $\phi_B$ ) and ideality factors ( $\beta$ ) are extracted by using the following equations [21].

$$I_s = AA^* T^2 e^{-q\phi_B / kt} \dots\dots\dots (6)$$

$$I = I_s (e^{qV / \beta kT} - 1) \dots\dots\dots (7)$$

where A is the cross-sectional area, A\* is Richardson constant, I<sub>s</sub> is the saturation current and  $\beta$  is the ideality factor having a value between 1 and 2.

The current is due to tunnelling that dominates across the junction and it could be represented by the expression: [22]

$$I = I_0 \exp(\gamma V) \dots\dots\dots (8)$$

From the plot of the forward current (I<sub>f</sub>) versus applied forward bias voltage (V<sub>f</sub>),

### 3- Results and discussion

X- Ray Diffraction was used to study the crystal structure of TiO<sub>2</sub> thin films, with different annealing temperature.

Fig (1) shows the TiO<sub>2</sub> thin films deposited on glass substrate for as deposited and annealing for different temperature 373K, 473K and 573K for two hour in air. The first structure (as deposited) have peaks A(112), B(112) R(210), B(032), B(213), B(431) and A(224) at angles ( $2\theta=38.6^\circ$ ), ( $2\theta=39.9^\circ$ ), ( $2\theta=44.7^\circ$ ), ( $2\theta=46.3^\circ$ ), ( $2\theta=64.9^\circ$ ), ( $2\theta=78.7^\circ$ ) and ( $2\theta=82.4^\circ$ ) respectively.

The XRD spectra of the TiO<sub>2</sub> thin film annealed at Ta=473K show the same result, and grown at Ta=573K show same result but the peak B (032) at angle ( $2\theta=46.3^\circ$ ) was vanish. Indicating that the crystallinity of the films depends on the annealing temperature. No significant sign of structural transformation was observed in the X-ray diffraction patterns after annealing temperature to 573K.

The annealing treatment has affected both the full at half maximum (FWHM) intensities, and the peak position of XRD by which the peaks height increases while the peak width decreases with increasing the annealing temperature. The FWHM values are related to the grain size of the film. The smaller the FWHM lead to larger grain size and the better of crystal quality of the whole film. By increasing the annealing temperature, the grain size of film was increased as shown in Table (1). The peak of A(112) was dominated for all XRD samples. The average grain size was 26nm in (as deposited) thin film and increase to 27.7nm and 28.8nm after annealing from 673K to 773K respectively.

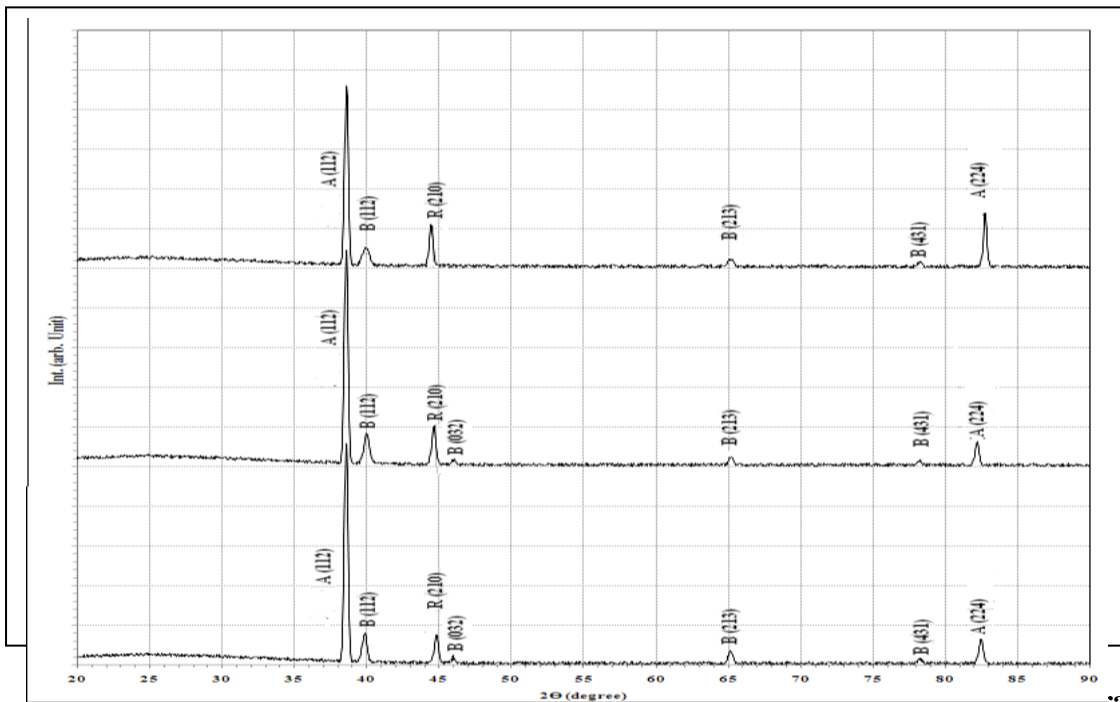


Fig (1) XRD of TiO<sub>2</sub> thin films on glass substrate for different annealing temperature.

Table (1) The experimental and standard

peaks <sup>[13]</sup> appear at XRD patterns at different annealing temperature.

T <sub>a</sub> (K)	2θ (degree) (Exp.)	d <sub>hkl</sub> (Å) (Exp.)	Grain size (nm)	d <sub>hkl</sub> (std.) (Å)	Phase	(hkl)
373	38.606	2.330	27.51	2.332	Anatase	(112)
	39.961	2.254	22.48	2.254	Brookite	(112)
	44.748	2.024	28.08	2.054	Rutile	(210)
	46.374	1.956	24.69	1.433	Brookite	(032)
	64.981	1.434	20.0	1.434	Brookite	(213)
	78.710	1.215	30.22	1.220	Brookite	(431)
	82.413	1.169	29.91	1.166	Anatase	(224)
473	38.587	2.331	28.06	2.332	Anatase	(112)
	39.974	2.254	24.15	2.254	Brookite	(112)
	44.690	2.026	27.71	2.054	Rutile	(210)
	46.077	1.968	26.98	1.433	Brookite	(032)
	65.218	1.429	28.67	1.434	Brookite	(213)
	78.256	1.221	30.75	1.220	Brookite	(431)
	82.232	1.171	29.87	1.166	Anatase	(224)
573	38.587	2.331	29.02	2.332	Anatase	(112)
	39.881	2.259	21.13	2.254	Brookite	(112)
	44.412	2.038	28.6	2.054	Rutile	(210)
	65.125	1.431	31.42	1.434	Brookite	(213)
	78.256	1.221	32.0	1.220	Brookite	(431)

	82.787	1.165	31.15	1.166	Anatase	(224)
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The electrical properties of these structures are analyzed from capacitance-voltage (C-V) and current-voltage (I-V) characteristics. The results demonstrated that all the samples show strong rectifying behavior.

The photovoltage extremely depends on the built-in potential of heterojunction, therefore, C-V characteristics were performed in order to examine the built-in potential of the samples. The changing of capacitance as a function of reverse bias voltage for TiO<sub>2</sub>/Si heterojunction with different annealing temperature T<sub>a</sub> shown in figure (2). It is clear that the capacitance decreases with the raise of T<sub>a</sub>. this behavior is attributed to the increasing in the depletion region width and also due to the decrease in the surface states [23].

The growth of the width of depletion layer with creasing of the T<sub>a</sub> could be due to the decreasing in the carrier concentration which leads to a decrease of the capacitance as given in table (2). Figure (3) shows the inverse capacitance square is plotted against applied reverse bias voltage for TiO<sub>2</sub>/Si junction at different T<sub>a</sub>. From the same figure, we have deduced the carrier's concentration of the abrupt TiO<sub>2</sub>/Si heterojunction at different T<sub>a</sub> from the slope of the straight line. Table (2) exhibits that these values which decrease with increasing T<sub>a</sub>.

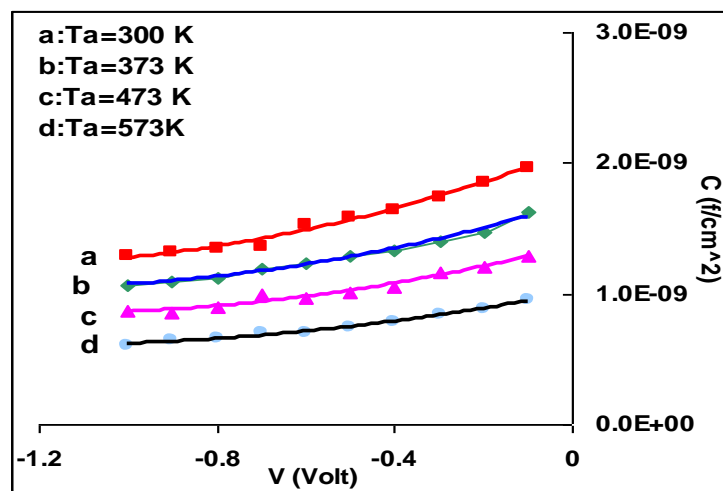


Fig.(2) Variation of capacitance as a function of reverse bias voltage for TiO<sub>2</sub>/Si junction at different T<sub>a</sub>.

Figure (4) shows the I-V characteristics of TiO<sub>2</sub>/Si heterojunction with different value of T<sub>a</sub>. As shown, the reverse currents are very weak and the forward currents increase sharply with the bias voltage. This testifies that the samples show apparently rectifying behavior.

The main source of the forward dark current is the flow of majority carriers, which are injected by the applied voltage and give rise to decrease of the width of the depletion layer. When the majority and minority carrier concentration is higher than the intrinsic carrier concentration ( $n^2_i < np$ ) may cause to generate of the recombination current at lower voltage region. This is because the excitation of electrons from the valence band (V.B.) to the conduction band (C.B.) will recombine them with the holes which are found at the V.B, and this is observed by the little increase in recombination current at low voltage region, while the tunneling current occurs at the high voltage region. The reverse bias current also contains two regions figure (5). In the first region of low voltages, the current slightly increases with increasing of the applied voltage, and the generation current dominates, while in the second high voltage region, the diffusion current dominates [20,21].

We also observed that the current increases with increasing of  $T_a$  because the increasing of temperatures will cause rearrangement of the interface atoms and reduce the dangling bond which leads to the improvement of the junction characteristics [24].

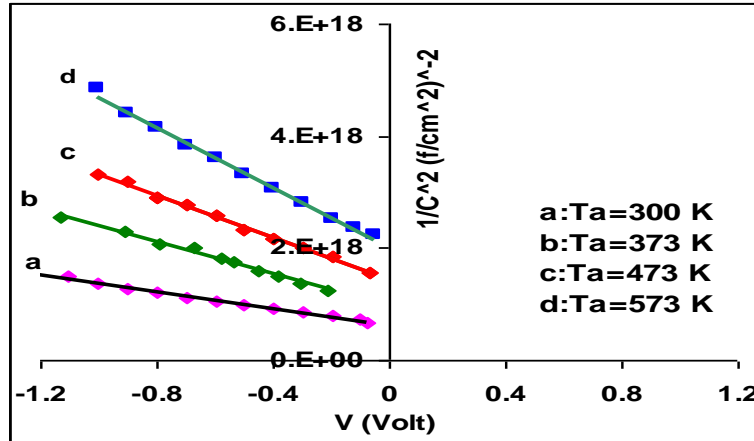


Fig. (3) the variation of  $1/C^2$  as a function of reverse bias voltage for  $TiO_2/Si$  heterojunction at different  $T_a$ .

Table (2) Values of  $W$ ,  $N_D$ , and  $V_{bi}$ , for  $TiO_2/Si$  heterojunction at different  $T_a$ .

$T_a$ (K)	$V_{bi}$ (Volt)	$C_o$ (nf/cm <sup>2</sup> )	$W$ (nm)	$N_D$ (cm <sup>-3</sup> )
300	0.761	5.91	324	$4.14 \times 10^{15}$
373	0.793	5.72	382	$2.11 \times 10^{15}$
473	0.825	5.55	412	$8.46 \times 10^{14}$
573	0.852	5.11	465	$4.73 \times 10^{14}$

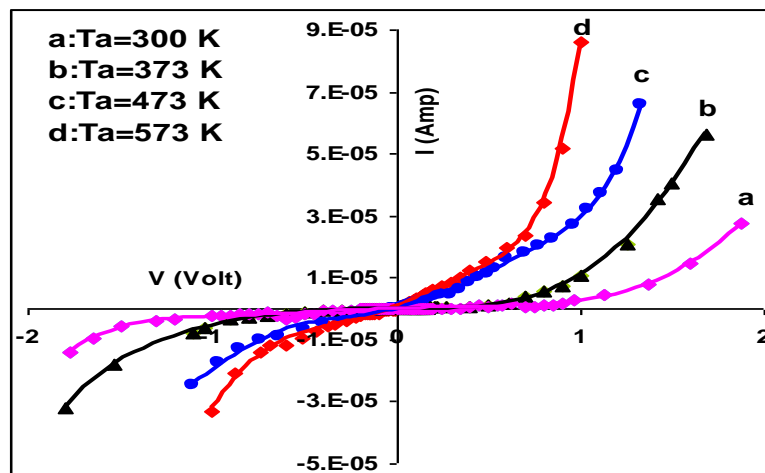


Fig. (3) I-V characteristics under dark for  $TiO_2/Si$  heterojunction at forward and reverse bias voltage at different  $T_a$ .

Figure (5) shows the semi-log relation between dark forward current density and bias voltage (0-2.5) volt for TiO<sub>2</sub>/Si junctions at different T<sub>a</sub>. The mechanism of the forward current coincides with the recombination tunneling mechanism [25], its clear from this figure that the saturation current decreases with T<sub>a</sub>.

The current transport across the semiconductors interface dependent on temperature, thus, the electrons at low temperature would be able to cross only the lower barriers and therefore current transport will be dominated by current passing through the lower barrier. With increasing temperature, more and more electrons would acquire significant energy to cross higher barrier.

The barrier height (Φ<sub>b</sub>) and ideality factor, seems that they have increasing with T<sub>a</sub> as shown in table (3). These increases may be associated with the increase of the available charge carriers to be transported across the barrier for Fermi level equalization.

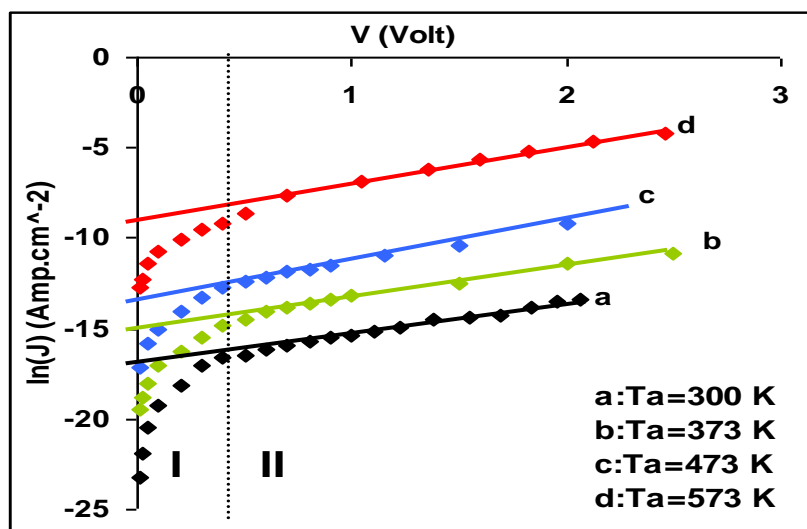


Fig. (5) ln (J) versus V for forward bias of dark of the fabricated TiO<sub>2</sub>/ Si heterojunction at different T<sub>a</sub>.

Table (3) Ideality ,tunneling factor the barrier height and saturation current values for TiO<sub>2</sub>/Si heterojunction at different T<sub>a</sub>.

T <sub>a</sub> (K)	Ideality Factor(β)	Tunneling Factor (γ)(V) <sup>-1</sup>	Φ <sub>b</sub> eV	I <sub>s</sub> μ Amp
300	3.25	3.9 2	0.695	2.1
373	2. 44	3.59	0.721	1. 8
473	1. 80	3.17	0.763	1.1
573	1.23	2.83	0.789	0.4

### Conclusions

The XRD analysis showed that TiO<sub>2</sub> thin films was polycrystalline and have three phases mixture ( Rutile, Anatase and Brookite).

This work has two main measurement, the C-V and I-V for the TiO<sub>2</sub> Heterojunction, according to the results of first measurement (C-V) the capacitance decreases with the both increasing of the reverse bias voltage and Ta, also the width of the depletion layer increases with increasing of Ta.

the second measurement (I-V) indicate there were rectifying behavior, and the dark current increases slightly with increasing of Ta. Also easy to realized the barrier height ( $\Phi_b$ ) increase with Ta.

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