

Improvement of Power Quality Features Using Dual Voltage Source Inverter

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ABSTRACT: *This paper provides a dual voltage deliver inverter (DVSI) software to improve the strength nice further to reliability of the microgrid system. The proposed scheme is absolutely composed of 2 inverters, which makes it feasible for the microgrid to change power produced by way of the disbursed power resources (ders) and to compensate the nearby unbalanced as well as nonlinear load. The control algorithms are genuinely developed predicated on on the spot symmetrical factor theory (ISCT) to use DVSI in grid posting and grid injecting methods. The proposed plan has more dependability, decrease bandwidth dependence at the critical inverter, less pricey because of to lessen in filtration size, and much higher use of micro grid capacity while operating with decreased dc link voltage score for the vital inverter. The DVSI is really made through the characteristics layout a promising alternative for micro grid offering hypersensitive plenty. The manage and topology algorithm are simply verified through experimental outcomes and complete simulation.*

KEYWORDS: DVSI, Instantaneous Symmetrical Aspect Theory (ISCT), DERs.

I. INTRODUCTION

To drive the energy device there are many models or pattern with more renewable strength sources interlinked with the network with the aid of the usage of allotted technology (DG). These DG gadgets can manage of nearby era, storage facilities from a micro-grid. A micro-grid, energy may be taken from the different renewable power sources such as fuel cells, photovoltaic structures, and wind electricity structures are merged to grid and hundreds using (%) strength digital converters. To alternate the electricity from micro-grid to grid and linked load by

using the usage of an interactive grid inverter. This micro-grid inverter can both work in a grid sharing mode or in grid injecting mode, the grid sharing is finished at the same time as offering part of nearby load, the grid injecting is accomplished by way of injecting power to the primary grid. Some other critical issue is retaining power excellent it has to be completed while the microgrid is connected to the principle grid. The range of feeder impedance inside the distribution systems, the propagation of these harmonic currents distorts the voltage on the point of not unusual coupling (%). The microgrid producing power from most important voltage supply inverter (MVSI) as actual power and the compensation of reactive, harmonic, and unbalanced load compensation which carried out with the aid of (AVSI) auxiliary voltage source inverter. An important advantage of MVSI that it can continually be used to inject real power to the grid with rated capacity and it additionally as enough renewable power is available at the dc hyperlink. The 2 inverters is supplies general power to the load and also reduces the switching losses throughout these semiconductor switches. By this reduction of losses will boom the gadget reliability whilst it compares to single inverter. On this scheme a smaller length modular inverters are used. Because these inverters can function at high switching frequencies with a reduced size of interfacing inductor, in order that the clear out fee gets reduced.

The principle inverter which supplies the actual power to song the fundamental wonderful series of modern and also the inverter reduces the bandwidth requirement of the primary inverter. The inverters in the present state of affairs use two separate dc links. For the reason that auxiliary inverter is providing zero sequence of load cutting-edge. In MVSI the

unmarried dc storage capacitor with 3-section three-leg inverter structure can be used. It reduces the dc-link voltage requirement of the main inverter. As a result, the usage of AVSI and dvsi inverters within the device structure which gives the machine reliability, micro-grid strength is applied better and also reduces the voltage rating of the grid.

Control algorithms is evolved (ISCT) to operate DVSI in grid connected mode whilst considering nonstiff grid voltage. The dq0 transformation approach is used to extract the essential fine series of pccvoltage q0 transformation. The manipulate approach are measured with the parallel inverters connected to a three phase 4-cord distribution device.

II. PROPOSED SYSTEM

Electric power quality (EPQ), or simply Power quality, refers to "maintaining the near sinusoidal waveform of power distribution bus voltages and currents at rated magnitude and frequency." [1] determining the fitness of electric power to consumer devices. Synchronization of the voltage frequency and phase allows electrical systems to function in their intended manner without significant loss of performance or life. The term is used to describe electric power that drives an electrical load and the load's ability to function properly. Without the proper power, an electrical device (or load) may malfunction, fail prematurely or not operate at all. There are many ways in which electric power can be of poor quality and many more causes of such poor quality power. The electric power industry comprises electricity generation (AC power), electric power transmission and ultimately electric power distribution to an electricity meter located at the premises of the end user of the electric power.

The electricity then moves through the wiring system of the end user until it reaches the load. The complexity of the system to move electric energy from the point of production to the point of consumption combined with variations in weather, generation, demand and other factors provide many opportunities for the quality of supply to be compromised. While "power quality" is a convenient

term for many, it is the quality of the voltage rather than power or electric current that is actually described by the term. Power is simply the flow of energy and the current demanded by a load is largely uncontrollable.

The proposed DVSI topology is shown in Fig. 1. It consists of a neutral point clamped (NPC) inverter to realize AVSI and a three-leg inverter for MVSI. These are connected to grid at the PCC and supplying a nonlinear and unbalanced load. The function of the AVSI is to compensate the reactive, harmonics, and unbalance components in load currents. Here, load currents in three phases are represented by i_{la} , i_{lb} , and i_{lc} , respectively. Also, $i_g(abc)$, $i_{\mu gm}(abc)$, and $i_{\mu gx}(abc)$ show grid currents, MVSI currents, and AVSI currents in three phases, respectively. The dc link of the AVSI utilizes a split capacitor topology, with two capacitors C1 and C2. The MVSI delivers the available power at distributed energy resource (DER) to grid. The DER can be a dc source or an ac source with rectifier coupled to dc link. Usually, renewable energy sources like fuel cell and PV generate power at variable low dc voltage, while the variable speed wind turbines generate power at variable ac voltage. Therefore, the power generated from these sources use a power conditioning stage before it is connected to the input of MVSI.

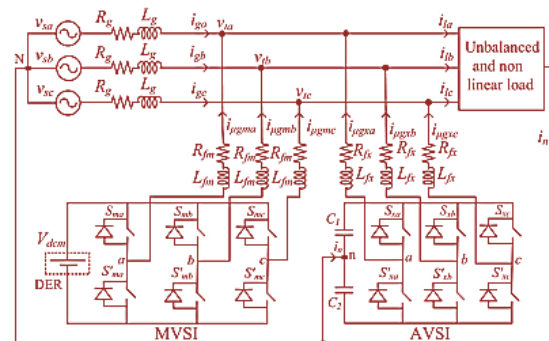


Fig. 1. Topology of proposed DVSI scheme

In this study, DER is being represented as a dc source. An inductor filter is used to eliminate the high-frequency switching components generated due to the switching of power electronic switches in the inverters. The system considered in this study is

assumed to have some amount of feeder resistance R_g and inductance L_g . Due to the presence of this feeder impedance, PCC voltage is affected with harmonics.

A. Design of DVSI Parameters

AVSI: The vital parameters of AVSI like dc link voltage (V_{dc}), dc storage capacitors (C_2 and C_1), interfacing inductance (L_{fx}), as well as hysteresis band ($\pm h_x$) are actually selected based on the layout technique of split capacitor DSTATCOM topology. The dc link voltage across each capacitor is actually taken as 1.6 times the good of phase voltage. The total dc link voltage reference (V_{dcref}) is actually discovered to remain 1040 V. Values of dc capacitors of AVSI are picked based on the shift in dc link voltage during transients. Let total load rating is actually S kVA. In the most severe situation, the load power might differ from minimum to maximum, i.e., from zero to S kVA. AVSI requires to exchange power that is real during transient to keep the ton power demand. This transfer of power that is real during the transient will outcome in deviation of capacitor voltage from its reference worth. Believe that the voltage controller requires n cycles, i.e., nT seconds to act, in which T is actually the process time period. Hence, highest power exchange by AVSI during transient is going to be nST . This energy is going to be identical to shift in the capacitors saved energy. Therefore

$$\frac{1}{2} C_1 (V_{dcr}^2 - V_{dc1}^2) = nST \dots\dots\dots (1)$$

where V_{dcr} and V_{dc1} are the reference dc voltage and maximum permissible dc voltage across C_1 during transient, respectively. Here, $S = 5$ kVA, $V_{dcr} = 520$ V, $V_{dc1} = 0.8V_{dcr}$ or $1.2 V_{dcr}$, $n = 1$, and $T = 0.02$ s. Substituting these values in (1), the dc link capacitance (C_1) is calculated to be 2000 μ F. Same value of capacitance is selected for C_2 . The interfacing inductance is given by

$$L_{fx} = \frac{1.6V_m}{4h_x f_{max}} \dots\dots\dots (2)$$

Assuming a maximum switching frequency (f_{max}) of 10kHz and hysteresis band (h_x) as 5% of load current

(0.5 A), the value of L_{fx} is calculated to be 26 mH. 2) **MVSI:** The MVSI uses a three-leg inverter topology. Its dc-link voltage is obtained as $1.15 V_m$, where V_m is the peak value of line voltage. This is calculated to be 648 V. Also, MVSI supplies a balanced sinusoidal current at unity power factor. So, zero sequence switching harmonics will be absent in the output current of MVSI. This reduces the filter requirement for MVSI as compared to AVSI. In this analysis, a filter inductance (L_{fm}) of 5 mH is used.

B. GRID-TIE Inverter:

A grid-tie inverter is a power inverter that converts direct current (DC) electricity into alternating current (AC) with an ability to synchronize to interface with a utility line. Its applications are converting DC sources such as solar panels or small wind turbines into AC for tying with the grid. Residences and businesses that have a grid-tied electrical system are permitted in many countries to sell their energy to the utility grid. Electricity delivered to the grid can be compensated in several ways. "Net metering" is where the entity that owns the renewable energy power source receives compensation from the utility for its net outflow of power.

A multifunctional power electronic converter for the DG power system is described in. This scheme has the capability to inject power generated by WES and also to perform as a harmonic compensator. Most of the reported literature in this area discuss the topologies and control algorithms to provide load compensation capability in the same inverter in addition to their active power injection. When a grid-connected inverter is used for active power injection as well as for load compensation, the inverter capacity that can be utilized for achieving the second objective is decided by the available instantaneous micro grid real power. Considering the case of a grid-connected PV inverter, the available capacity of the inverter to supply the reactive power becomes less during the maximum solar isolation periods. At the same instant, the reactive power to regulate the PCC voltage is very much needed during this period. It indicates that providing multifunctionalities in a single inverter

degrades either the real power injection or the load compensation capabilities. This paper demonstrates a dual voltage source inverter (DVSI) scheme, in which the power generated by the micro grid is injected as real power by the main voltage source inverter (MVSI) and the reactive, harmonic, and unbalanced load compensation is performed by auxiliary voltage source inverter (AVSI).

This has an advantage that the rated capacity of MVSI can always be used to inject real power to the grid, if sufficient renewable power is available at the dc link. In the DVSI scheme, as total load power is supplied by two inverters, power losses across the semiconductor switches of each inverter are reduced. This increases its reliability as compared to a single inverter with multifunctional capabilities. Also, smaller size modular inverters can operate at high switching frequencies with a reduced size of interfacing inductor, the filter cost gets reduced. Moreover, as the main inverter is supplying real power, the inverter has to track the fundamental positive sequence of current. This reduces the bandwidth requirement of the main inverter. The inverters in the proposed scheme use two separate dc links. Since the auxiliary inverter is supplying zero sequence of load current, a three-phase three-leg inverter topology with a single dc storage capacitor can be used for the main inverter. This in turn reduces the dc-link voltage requirement of the main inverter. Thus, the use of two separate inverters in the proposed DVSI scheme provides increased reliability, better utilization of micro grid power, reduced dc grid voltage rating, less bandwidth requirement of the main inverter, and reduced filter size. Control algorithms are developed by instantaneous symmetrical component theory (ISCT) to operate DVSI in grid-connected mode, while considering non-stiff grid voltage. The extraction of fundamental positive sequence of PCC voltage is done by dq0 transformation. The control strategy is tested with two parallel inverters connected to a three-phase four-wire distribution system. Effectiveness of the proposed control algorithm is validated through detailed simulation and experimental results.

III. SIMULATION AND RESULTS

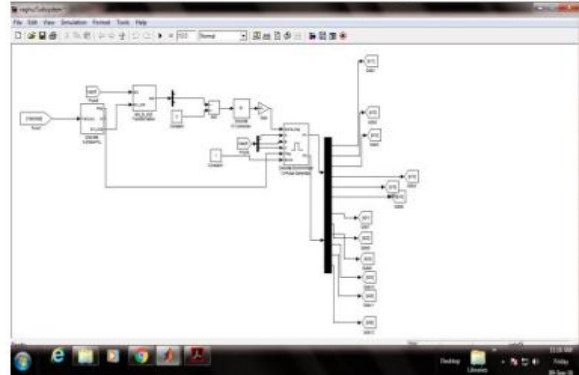


Fig. 2 Simulation diagram showing the control strategy of proposed DVSI scheme

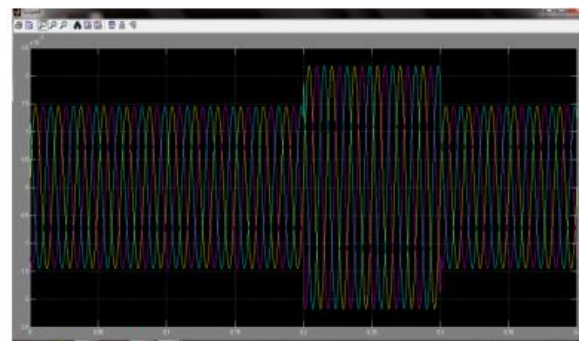


Fig. 3 Voltage swell during non linear load parallel to the dual inverter connected load

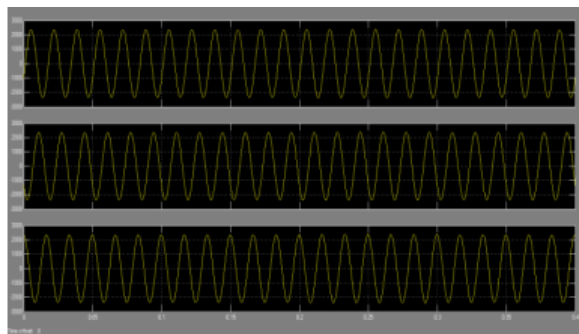


Fig. 4 voltages of dual inverter fed line of 3-phase

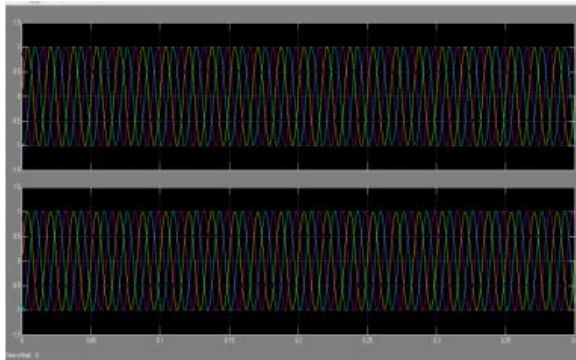


Fig.5 Voltages and currents of 3-phase load

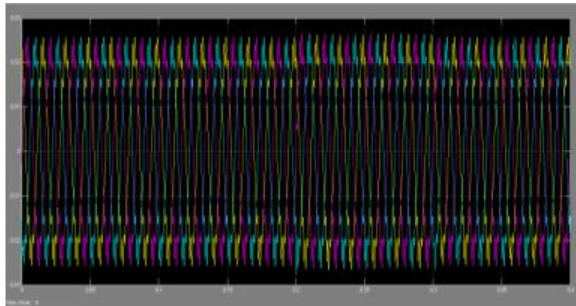


Fig .6 dual fed line of 3-phase currents

IV. CONCLUSION

A DVSI scheme is proposed for microgrid structures with superior energy nice. Control algorithms are advanced to generate reference currents for DVSI the usage of ISCT. The proposed scheme has the functionality to change electricity from distributed turbines (dgs) and also to compensate the local unbalanced and nonlinear load. The performance of the proposed scheme has been established thru simulation and experimental studies. In comparison to a unmarried inverter with multifunctional competencies, a DVSI has many blessings consisting of, improved reliability, decrease value due to the reduction in clear out size, and extra utilization of inverter potential to inject real power from dgs to microgrid. Furthermore, the usage of 3-phase, three twine topology for the main inverter reduces the dc-hyperlink voltage requirement.

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