

A New Approach for Three Phase Grid Connected System with Distributed Generation Systems

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ABSTRACT: *Currently distributed technology systems are extensively inhabiting their vicinity within the strength generation. Grid codes from the transmission machine operators pronounce the behaviour of the strength supply, regulating voltage limits and reactive power injection to stay linked and supports the grid under fault. Happening the basis that in contrast to sorts of voltage sags require different voltage support strategies, a flexible manage scheme for three phase grid-related inverter is proposed here. For the three segment balanced voltage sags, the inverter should inject reactive electricity on the way to increase the voltage in all levels. In one-phase or -phase faults, the main situation of the DG inverter is to equalize voltages by means of reducing the negative symmetric collection and clean the phase leap. Owing to system obstacles, a stability among those two extreme regulations is obligatory. Thus, over voltage and under voltage may be averted, and the proposed control scheme prevents disconnection while achieving the desired voltage support service. The chief contribution of this painting is the introduction of a control algorithm for reference current generation that provides bendy voltage support under grid faults.*

KEYWORDS- Distributed Generation Inverters
Reactive power manage, voltage sag, voltage support

I. INTRODUCTION

Renewable energy sources are being broadly used nowadays for strength generation. Three segment inverter implemented inside the unified manage strategy is powerful and gives the higher inductor modern [1]. Distributed era (DG) is rising as a possible alternative whilst renewable or nonconventional energy sources are available, which includes wind turbines, photovoltaic arrays, gas cells, micro turbines [2], [4]. Most of those assets are

related to the application through power electronic interfacing converters, i.e., three-phase inverter. Moreover, DG is a suitable form to offer high reliable electrical energy deliver, as it can perform both in the grid-tied mode or inside the islanded mode [3]. In the grid-tied operation, DG delivers power to the application and the local critical load. Upon the incidence of application outage, the islanding is fashioned. Under this circumstance, the DG must be tripped and end to energize the part of software as quickly as possible according to IEEE Standard 929-2000 [5]. However, as a way to improve the power reliability of a few neighborhood critical load, the DG need to disconnect to the software and retain to feed the neighborhood critical load [6]. The load voltage is fundamental difficulty of those two operation modes, because it's far fixed by using the application within the grid-tied operation, and formed by using the DG inside the islanded mode, respectively. Therefore, upon the going on of lonely i.e., islanding, DG must take over the load voltage as soon as possible, in order to reduce the transient inside the load voltage. And this difficulty brings a venture for the operation of DG. Droop-based manipulate is used extensively for the energy sharing of parallel inverters [12], [13], that is known as voltage mode control on this paper, and it is able to also be applied to DG to realize the electricity sharing between DG and utility in the grid-tied mode [14]. In this situation, the inverter is always regulated as a voltage supply by way of the voltage loop, and the quality of the load voltage can be guaranteed throughout the transition of operation modes. However, the limitation of this approach is that the dynamic overall performance is terrible, due to the fact the bandwidth of the outside energy loop, figuring out hunch manage, is a lot lower than the voltage loop. Moreover, the grid current isn't always managed directly, and the difficulty of the inrush grid current all through the transition from the islanded

mode to the grid-tied mode usually exists, despite the fact that phase locked loop (PLL) and the virtual inductance are adopted. In the hybrid voltage and contemporary mode manage, there's a need to switch the controller when the operation mode of DG is changed. During the change from the incidence of utility outage and switching the controller to voltage mode, the load voltage is neither fixed by means of the software, nor regulated through the DG, and the period of the time program language period is decided by using the islanding detection method. Therefore, the main problem in this approach is that it makes the great of the load voltage heavily reliant on the speed and accuracy of the islanding detection approach [7]-[11].

This paper proposes a unified control method that avoids the aforementioned shortcomings. First, the traditional inductor modern-day loop is employed to manipulate the Neutral point clamped (NPC) inverter with a greenback converter which gives neutral factor within the dc voltage in DG to behave as a contemporary source with a given reference in the synchronous reference frame (SRF). Second, a unique voltage controller is presented to supply reference for the inner inductor modern loop, wherein a proportional-plus-quintessential (PI) compensator and a proportional (P) compensator are employed in D-axis and Q-axis, respectively. In the grid-tied operation, the weight voltage is dominated by way of the application, and the voltage compensator in D-axis is saturated, whilst the output of the voltage compensator in Q-axis is forced to be 0 by way of the PLL. Therefore, the reference of the internal modern-day loop cannot be regulated through the voltage loop, and the DG is controlled as a cutting-edge supply just by the internal current loop. Upon the occurrence of the grid outage, the burden voltage is no greater decided with the aid of the utility, and the voltage controller is routinely activated to regulate the burden voltage. These take place obviously, and, thus the proposed control method does now not want a compelled switching between two awesome sets of controllers. Further, there may be no need to stumble on the islanding speedy and correctly, and the islanding detection method is no extra vital in this approach.

In Fig.1, a block diagram of the controller for DG inverters underneath grid fault is shown. The inputs of the controller are the measured phase voltages v at the PCC, the currents i flowing thru L_i inductor, and the dc-link voltage V_{dc} . Voltage v and modern I are transformed into SRF values. Voltages v_α and v_β are then decomposed into symmetric additives using a collection extractor.

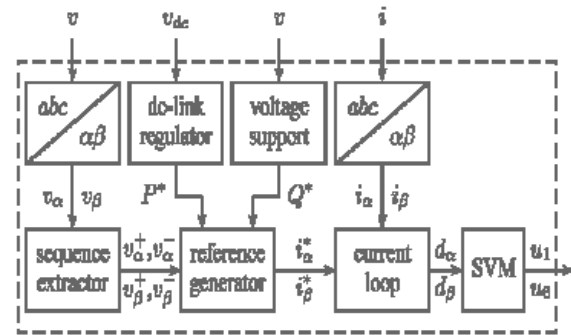


Fig 1. Control diagram of three-phase DG inverter under grid fault

II. PROPOSED STRATEGY

Under grid connected operation DG should be synchronized with the grid. In this mode each DG inverter works for the system by the measured voltage and desired power levels. For unity power factor operation, it is essential that the grid current reference signal is in phase with the grid voltage. Current controller design using Flexible Voltage Support Controller is shown in fig.2

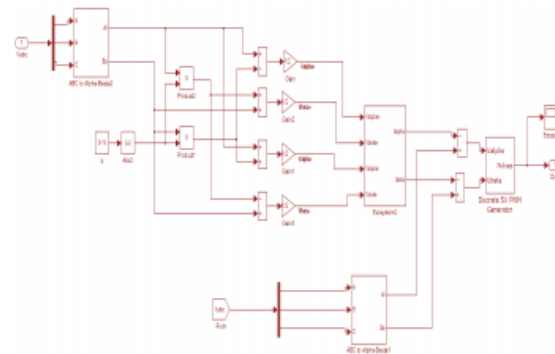


Fig 2 Controller Design

2. Point of Common Coupling



The PCC is a factor in the electrical system where multiple clients or multiple electrical loads may be linked. In step with IEEE-519, this should be a factor which is accessible to both the software and the customer for direct size. Even though in many cases the percent is considered at the metering point, carrier front or facility transformer, IEEE-519 states that "inside an commercial plant, the % is the point between the non-linear load and different loads." % at service front, metering factor or facility transformer it'll normally be less difficult to meet harmonic distortion limits when the % is considered at the metering factor, facility transformer or service entrance.

In maximum cases, the present day flowing at this point represents a mixture of natural fundamental current flowing to linear hundreds and each fundamental and distorted modern flowing to non-linear loads. The distortion current will regularly be a smaller percent of the overall (mixed) essential modern at this point. p.c inside the plant and between the non-linear and linear loads thinking about the % on the gadget will often meet the IEEE-limits each at this factor and also a percent near the provider entrance. The IEEE-519 limit at this factor, which is basically on the enter to the non-linear masses, is regularly 12%, 15% or maybe 20% THD-I.

The ratio of quick circuit present day to load current is usually plenty large at this %, which typically has much less total load, than on the metering factor, where the complete plant load is attached. Usually, if the THD restrict is met at every non-linear load inside the plant, the TDD limits at the carrier entrance can even

Be met. Even though the THD limits are typically lower for the p.c considered near the software metering point, the overall THD at this p.c can also be considerably lower if there are additional linear loads in the plant that percentage the energy supply.

Filter out: The rectifier circuitry takes the initial ac sine wave from the transformer or different source and converts it to pulsating dc. A complete-wave

rectifier will produce the waveform shown to the proper, while a halfwave rectifier will pass only each different half-cycle to its output. This will be appropriate sufficient for a basic battery charger, even though some styles of rechargeable batteries still may not love it. In any case, it's far nowhere close to good enough for maximum digital circuitry. We need a manner to ease out the pulsations and provide a far "cleaner" dc strength supply for the load circuit. To accomplish this, we need to use a circuit called a clear out. In well known terms, a filter is any circuit on the way to dispose of a few components of a signal or power source, at the same time as permitting other parts to keep on without full-size problem. In a power supply, the filter ought to remove or considerably lessen the ac variations while nonetheless making the desired dc available to the load circuitry.

Filter out circuits are not normally very complex, however there are several versions. Any given filter may involve capacitors, inductors, and/or resistors in some combination. Every such mixture has both advantages and downsides, and its personal variety of practical utility. If we vicinity a capacitor at the output of the overall-wave rectifier as proven to the left, the capacitor will charge to the peak voltage every half cycle, after which will discharge greater slowly via the load whilst the rectified voltage drops back to zero before beginning the following 1/2-cycle. hence, the capacitor enables to fill within the gaps between the peaks, as shown in pink in the first determine to the right. Even though we've used directly strains for simplicity, the decay is surely the everyday exponential decay of any capacitor discharging thru a load resistor. The volume to which the capacitor voltage drops relies upon at the capacitance of the capacitor and the amount of current drawn by using the load; those two factors efficaciously shape the RC time consistent for voltage decay. As a result, the actual voltage output from this mixture never drops to zero, but rather takes the shape shown inside the 2d discern to the right. The blue part of the waveform corresponds to the portion of the enter cycle where the rectifier provides current to the load, whilst the red portion shows when the capacitor gives contemporary to the burden.

III. SIMULATION RESULTS

1. Over All Simulation Diagram with Symmetrical Fault

The modelling of the system with flexible voltage support control is designed in simulink. The gain parameters of flexible voltage support controller obtained by proper tuning.

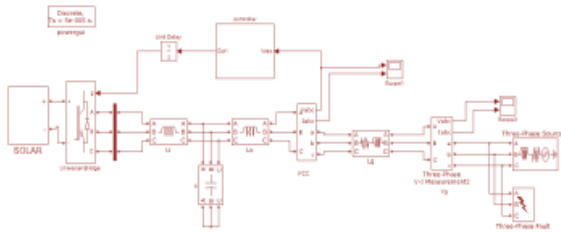


Fig.3 Simulation Diagram with Symmetrical Fault Flexible voltage support control works as a regulator of the voltage and current during transition from grid connected to Symmetrical Fault. α and β for flexible voltage support control is chosen proper tuning. The Overall Simulation Diagram with Flexible Voltage Support controller Fig 3.

2. Over all Simulation Diagram with Unsymmetrical Fault

The modelling of the system with flexible voltage support control is designed in simulink. The gain parameters of flexible voltage support controller obtained by proper tuning. Flexible voltage support control works as a regulator of the voltage and current during transition from grid connected to Unsymmetrical Fault. α and β for flexible voltage support control is chosen proper tuning. The overall Simulation Diagram with Flexible Voltage Support controller fig.4.

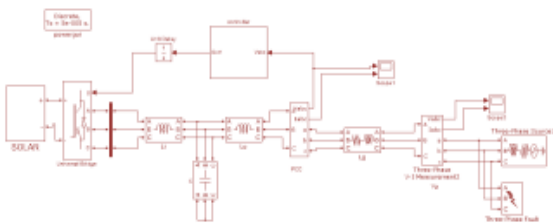


Fig.4 Simulation Diagram with Unsymmetrical Fault

3. Grid Voltage and current for symmetrical fault

The grid voltage and current waveforms without fault is shown Fig.6. The grid voltage is 565V and current value is 25A.

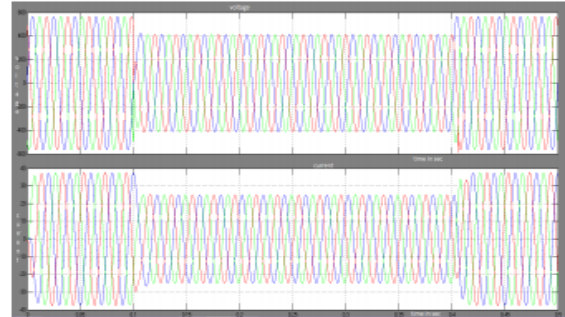


Fig.5 Grid Voltage and Current for symmetrical Fault

The Fig.5 shows the voltage and current value of grid and interconnection of solar power plant and three phase conventional source. In the Fig.5 normal condition the voltage and current values are calculated by using voltage current measurement. In normal condition with any disturbances grid voltage value 400V and current value is 38A in grid.

4. Grid Voltage and current for Unsymmetrical fault

The grid voltage and current waveforms without fault is shown Fig. 6. The grid voltage is 560V and current value is 40A. The Fig.6 shows the voltage and current value of grid and interconnection of solar power plant and three phase conventional source.

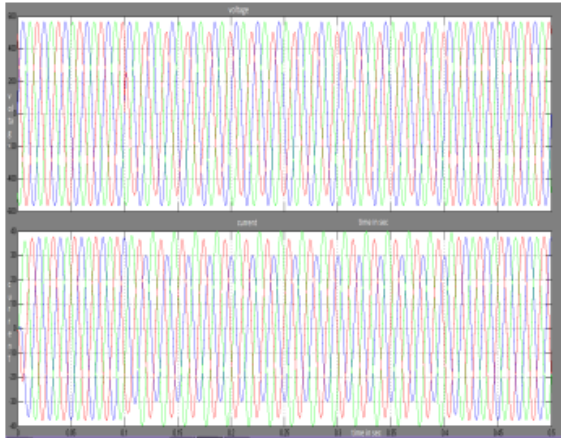


Fig.6 Grid Voltage and Current for symmetrical Fault

In the Fig 6, Unsymmetrical condition the voltage and current values are calculated by using voltage current measurement. Here R&Y phases are fault condition. The grid voltage value in RY&B phases - 565V per phase and grid current value in B phase 40A in R&Y phases 38A. So reduces the fault current values with in limit using reactive power injection in normal condition.

IV. CONCLUSION

The voltage support strategy can be changed by a control parameter in line with the kind of voltage sag. In three phase balanced sags, the best answer appears to be to raise the voltage in all stages. In one or two phase faults, voltage equalization is a desired choice because conventional strategies can cause overvoltage and motive disconnection. When the sag is less deep, a balance among those intense guidelines need to be implemented.

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